# MSKSEMI 美森科













**ESD** 

VS

rss -

MOV

GDT

PIFD

# **AON6566-MS**

**Product specification** 





## **Description**

The AON6566-MS uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

## **Features**

Vps = 30V Ip =80A

 $RDS(ON) < 6m\Omega$  VGS=10V

## **Application**

- Battery protection
- Load switch
- Uninterruptible power supply

### **Reference News**

PACKAGE OUTLINE	N-Channel MOSFET	Marking	
S S S S S S S S S S S S S S S S S S S		MSKSEMI AON6566 N30	
DFN5X6-8L	S		

# Absolute Maximum Ratings (TC=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	40	V
Vgs	Gate-Source Voltage	±20	V
ID @Tc=25°C	Continuous Drain Current, V gs @ 10V <sup>1</sup>	50	Α
ID @Tc=70°C	Continuous Drain Current, V gs @ 10V <sup>1</sup>	45	Α
Ірм	Pulsed Drain Current <sup>2</sup>	280	Α
EAS	Single Pulse Avalanche Energy <sup>3</sup>	56	mJ
Тѕтс	Storage Temperature Range	-55 to 175	°C
TJ	Operating Junction Temperature Range	-55 to 175	°C
Reja	Thermal Resistance Junction-Ambient <sup>1</sup>	30	°C/W



# **Electrical Characteristics** (TC=25°C Unless Otherwise Noted)

Symbol	Parameter Condition			Тур.	Max.	Unit
Static Elec	etrical Characteristics @ T <sub>j</sub> =25°C (unles	ss otherwise stated)	)			
V( BR) DSS	Drain- Source Breakdown Voltage VGS=0V ID=250 µA		30			V
	Zero Gate Voltage Drain Current VDS=30V, VGS=0V				0.1	μΑ
loss	Zero Gate Voltage Drain Current( T <sub>j</sub> = 1 2 5 ℃ ) V <sub>DS</sub> =30 V, V <sub>GS</sub> =0 V				100	μΑ
Igss	Gate- Body Leakage Current	Vgs=±20V,Vps=0V			±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	nreshold Voltage VDS=VGS, ID=250 µ A			2.5	V
RDS(ON)	Drain- Source On- State Resistance③	Drain- Source On- State Resistance③ Vgs=10V, Ib=20A			6	mΩ
RDS(ON)	Drain- Source On- State Resistance③ V <sub>GS</sub> =4.5V, I <sub>D</sub> =16A			5.4	8	mΩ
Dynamic E	Electrical Characteristics @ $T_j = 25$ °C ( $\iota$	unless otherwise sta	ated)			
Ciss	Input Capacitance			1930		pF
Coss	Output Capacitance	V <sub>DS</sub> =15V,V <sub>GS</sub> =0 V, f=1 MHz		310		pF
$C_{rss}$	Reverse Transfer Capacitance	· · · · · · · · · · · · · · · · · · ·		260		pF
$R_g$	Gate Resistance	f= 1 MHz		0.85		
$Q_{g}$	Total Gate Charge			38		nC
$Q_{gs}$	Gate- Source Charge	VDS=15V, ID=20 A, VGS=10V		5.1		nC
$Q_{\sf gd}$	Gate- Drain Charge	71, 100 101		12		nC
Switching	Characteristics					
<b>t</b> d(on)	Turn- on Delay Time			8.5		nS
<b>t</b> r	Turn- on Rise Time VDD=15 V,			9		nS
$\mathbf{t}_{ ext{d(off)}}$	Turn- Off Delay Time	ID=20A, RG=3,		31		nS
<b>t</b> f	Turn- Off Fall Time			9		nS
Source- Drain Diode Characteristics@ T <sub>j</sub> = 25°C (unless otherwise stated)						
VsD	Forward on voltage Isp=20A, Vgs=0V			0.8	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	Tj=25°C , lsd=20A,		16		nS
Qrr	Reverse Recovery Charge	Vgs= 0 V di/dt=500A/µs		42		nC

#### NOTE:

<sup>1.</sup>Repetitive rating; pulse width limited by max. junction temperature.

<sup>2.</sup>Limited by  $T_{Jmax}$ , starting  $T_J = 25^{\circ}C$ , L = 0.5mH,  $R_G = 25$ ,  $I_{AS} = 15A$ ,  $V_{GS} = 10V$ . Part not recommended for use above this value

<sup>3.</sup>Pulse width  $\leq$  300 $\mu$ s; duty cycle $\leq$  2%.



## Typical Electrical and Thermal Characteristics

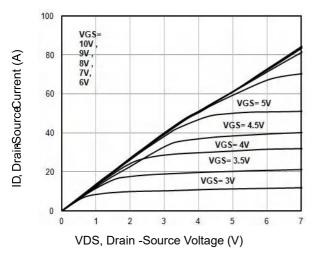


Fig1. Typical Output Characteristics

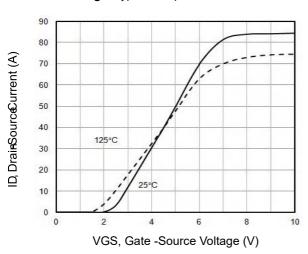
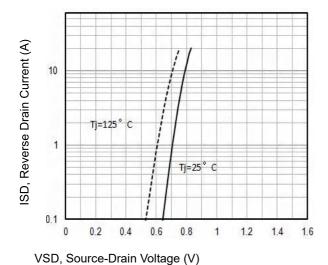


Fig3. Typical Transfer Characteristics



**Fig6.** Maximum Safe Operating Area Voltage

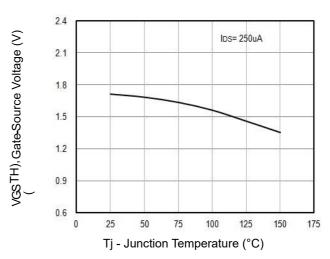


Fig2. V<sub>GS(TH)</sub> Gate -Source Voltage Vs.Tj

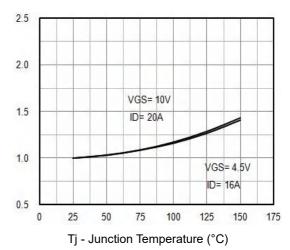
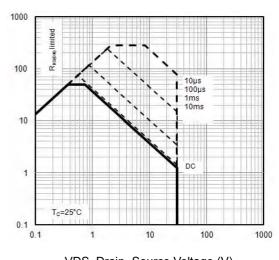


Fig4. Normalized On-Resistance Vs. Tj



VDS, Drain -Source Voltage (V)

Fig5. Typical Source-Drain Diode Forward

Normalized On Resistance

ID - Drain Current (A)



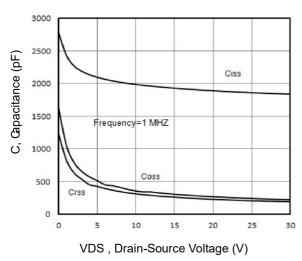


Fig7. Typical Capacitance Vs.Drain-Source Voltage

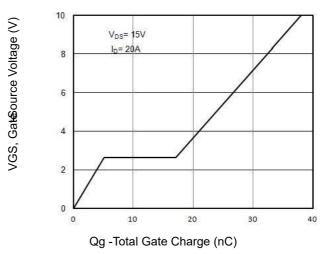


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

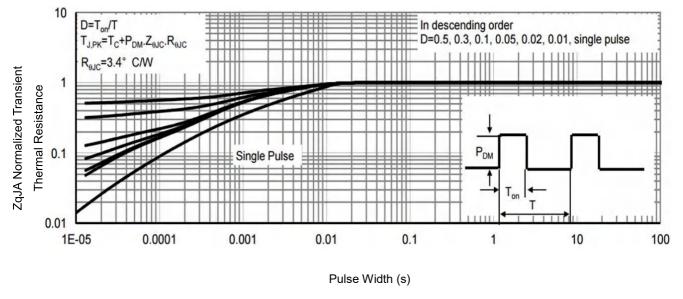


Fig9. Normalized Maximum Transient Thermal Impedance

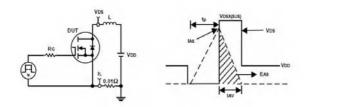


Fig10. Unclamped Inductive Test Circuit and waveforms

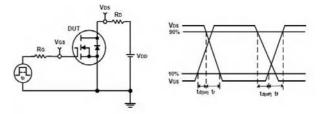
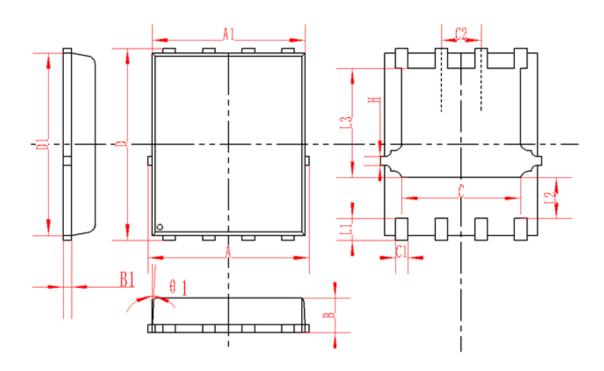


Fig11. Switching Time Test Circuit and waveforms



# DFN5X6-8L Package Information



SYMBOL	MM			INCH		
STIVIDOL	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8。	10 <sub>°</sub>	12。	8。	10 <sub>°</sub>	12。
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

## **REEL SPECIFICATION**

P/N	PKG	QTY
AON6566-MS	DFN5X6-8L	5000



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