# MSKSEMI 美森科













**ESD** 

SS

MOV

GDT

PIFD

## 2N7002DWH6327-MS

**Product specification** 





#### **General Features**

- 60V,0.3A,RDS(ON)=1.8Ω@VGS=10V
   Improved dv/dt capability
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded

## **Application**

- Motor Drive
- Power Tools
- LED Lighting

#### **Reference News**

PACKAGE OUTLINE	Pin Configuration	Marking
SOT-363	D1 D2 G1 G2	К72** КЛ2**



## Absolute Maximum Ratings (TA=25℃ unless otherwise

Symbol	Parameter	Rating	Units
Vos	Drain- Source Voltage	60	V
Vgs	Gate- Source Voltage	±20	V
lo	Drain Current - Continuous (T <sub>A</sub> =250)	0.3	А
li D	Drain Current - Continuous (T <sub>A</sub> =700)	0.24	Α
Ірм	Drain Current – Pulsed <sup>1</sup>	1.2	Α
Po	Power Dissipation (T <sub>A</sub> =25C)	0.28	W
PU	Power Dissipation – Derate above 250	0.002	W/°C
Тѕтс	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 150	°C

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		450	°C/W

## ElectricalCharacteristics(TJ=25 °C, unless otherwise noted) Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain- Source Breakdown Voltage	VGS=0V , ID=250uA	60			V
∆BVDSS/ ∆TJ	BVDSS Temperature Coefficient	Reference to 25C , ID=1 mA		0.04		V/℃
IDSS	Drain-Source Leakage Current	VDS=60V , VGS=0V , TJ=25C			1	А
		VDS=48V , VGS=0V , TJ=125C			100	Α
IGSS	Gate-Source Leakage Current	VGS= ±20V , VDS=0V			±10	Α

#### On Characteristics

RDS(ON)	Static Drain-Source On-Resistance	VGS=10V , ID=0.3A		1.8	2.8	Ω
, ,		VGS=4.5V , ID=0.2A		2.2	3	Ω
VGS(th)	Gate Threshold Voltage	VGS=VDS , ID =250uA	1	1.6	2.5	V
△ VGS(th	VGS(th) Temperature Coefficient			-4		Mv/℃
)						
gfs	Forward Transconductance	VDS=10V , ID=0. 1A		0.24		S



## Dynamic and switching Characteristics

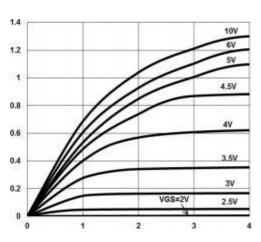
Qg	Total Gate Charge <sup>2</sup> , 3		 1.1	
Qgs	Gate-Source Charge <sup>2</sup> , 3	VDS=30V , VGS=10V , ID=0.2A	 0.1	nC
Qgd	Gate-Drain Charge <sup>2</sup> , 3	ID-0.2A	 0.23	
Td(on)	Turn-On Delay Time <sup>2</sup> , 3		 3	
Tr	Rise Time <sup>2</sup> , <sup>3</sup>	VDD=30V , VGS=10V ,	 5	~0
Td(off)	Turn-Off Delay Time <sup>2</sup> , 3	RG=6Ω ID=0.2A	 14	nS
Tf	Fall Time <sup>2</sup> , 3	100 0 10 0.27	 9	
Ciss	Input Capacitance		 30.6	
Coss	Output Capacitance	VDS=10V , VGS=0V , F=1MHz	 5.5	pF
Crss	Reverse Transfer Capacitance		 4	

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
IS	Continuous Source Current	VG=VD=0V , Force Current			0.3	Α
ISM	Pulsed Source Current				0.6	Α
VSD	Diode Forward Voltage	VGS=0V , IS=1A , TJ=25C			1.2	V

#### Note:

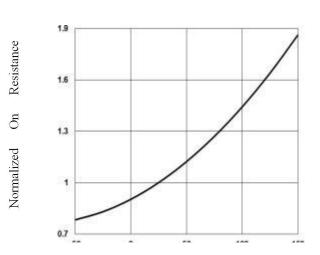
- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. Essentially independent of operating temperature.





 $\ensuremath{\mathsf{VDS}}$  , Drain to Source Voltage (V)

Fig. 1 Output Characteristics



 $\begin{tabular}{ll} TJ & , JunctionTemperature ($^{\circ}$C) \\ \hline \begin{tabular}{ll} Fig. 3 & Normalized RDSON vs. & TJ \\ \hline \end{tabular}$ 

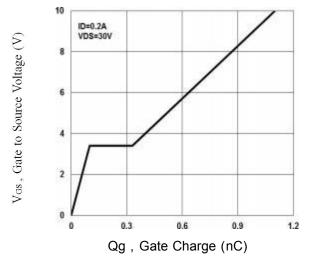
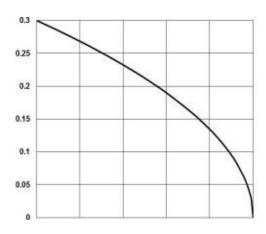


Fig. 5 Gate Charge Waveform



TJ , Juction Temperature ( $^{\circ}$ C)

Fig. 2 Continuous Drain Current vs. TJ

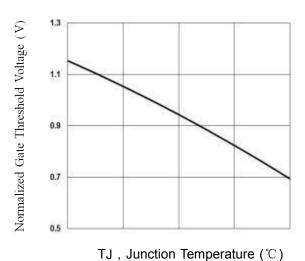
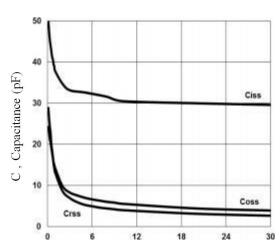
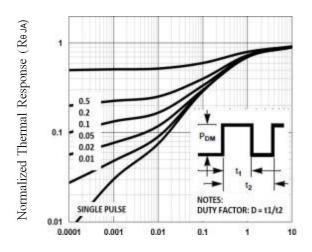


Fig. 4 Normalized Vth vs. TJ

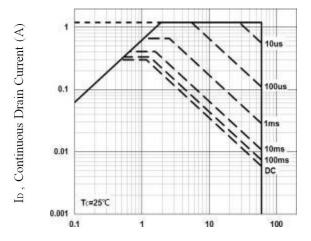


VDS , Drain to Source Voltage (V)

Fig. 6 Capacitance Characteristics



Square Wave Pulse Duration (s) Fig. 7 Normalized Transient Impedance



VDS ,Drain to Source Voltage(V) Fig. 8 Maximum Safe Operation Area

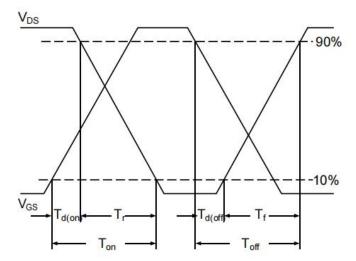
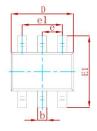


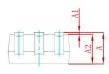
Fig.9 Switching Time Waveform



## PACKAGE MECHANICAL DATA

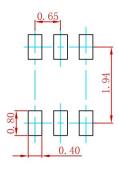






Ohad	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
С	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
е	0.650	TYP	0.026	TYP
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021	REF
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

## **Suggested Pad Layout**



#### Note:

- Controlling dimension in millimeters.
- 2.General tolerance: ± 0.05mm.
- 3. The pad layout is for reference purposes only.

#### **REEL**

P/N	PKG	QTY
2N7002DWH6327-MS	SOT-363	3000



### **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

## 单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)