



Product data sheet

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Features

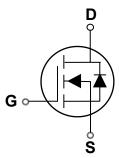
- 20 V, 3A, RDS(ON) =50mΩ @ VGS = 4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Notebook
- Load Switch
- Hend- Held Instruments

BVDSS	RDSON	ID
20V	50mΩ	3A

SOT-23-3L



Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
Vds	Drain- Source Voltage	20	V
Vgs	Gate-Source Voltage	±10	V
	Drain Current – Continuous (Tc=250)	3	A
ID Drain C	Drain Current – Continuous (Tc=1000)	2.5	A
ЫМ	Drain Current – Pulsed ¹	16	A
_	Power Dissipation (T _C =250)	1.56	W
Po	Power Dissipation – Derate above 250	0.012	W/ C
Тѕтс	Storage Temperature Range	-55 to 150	C
ТJ	Operating Junction Temperature Range	-55 to 150	С

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		80	C/W



Off Characteristics

Symbol	Parameter	Conditions		Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	Vgs=0V , Ip=250uA	20			V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25C , $I_D = 1 \text{ mA}$		0.02		V/ C
	Drain-Source Leakage Current	Vds=20V , Vgs=0V , Tj=250			1	uд
loss		Vds=16V , Vgs=0V , TJ=1250			10	uĄ
lgss	Gate-Source Leakage Current	$V_{GS=} \pm 10V$, $V_{DS}=0V$			±100	nĄ

On Characteristics

_	- Statia Durain Courses On Desistance	Vgs=4.5V , Id=2A		50	60	mΩ
R _{DS(ON)} Static Drain-Source On-Resistance	Vgs=2.5V , Id=1A		55	70	116.2	
VGS(th)	Gate Threshold Voltage		0.4	0.7	1	V
$\triangle V_{GS(th)}$	VGS(th) Temperature Coefficient			2		mV/ C
gfs	Forward Transconductance	Vds=10V , Is=2A		4.4		S

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{2, 3}		 3.6	
Qgs	Gate-Source Charge ^{2,3}	V_{DS} =10V , V_{GS} =4.5V , I_{D} =1A	 0.38	 nC
Qgd	Gate-Drain Charge ^{2,3}		 0.6	
Td(on)	Turn-On Delay Time ^{2, 3}		 1.8	
Tr	Rise Time ^{2,3}	V_{DD} =10V , V_{GS} =4.5V , R_{G} =25 Ω	 5.6	 nS
Td(off)	Turn-Off Delay Time ^{2, 3}	I _D =1A	 11.3	
Tf	Fall Time ^{2 , 3}		 3.2	
Ciss	Input Capacitance		 180	
Coss	Output Capacitance	V_{DS} =15V , V_{GS} =0V , F=1MHz	 32	 PF
Crss	Reverse Transfer Capacitance		 26	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V _G =V _D =0V . Force Current			3	А
lsм	Pulsed Source Current	VG-VD-UV, FOICE Current			6	А
Vsd	Diode Forward Voltage	Vgs=0V , Is=1A , TJ=250			1.2	V

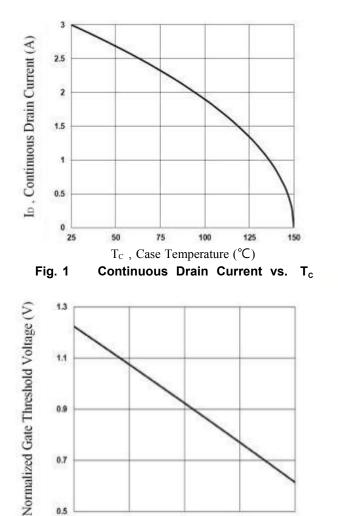
Note :

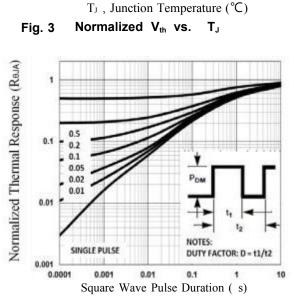
 $\label{eq:response} 1 \ . \qquad \qquad \mbox{Repetitive Rating} \ : \ \mbox{Pulsed width limited by maximum junction temperature} \ .$

- 2 . The data tested by pulsed , pulse width \leq 300 us , duty cycle \leq 2%.
- 3 . Essentially independent of operating temperature.



AO3414 HF Semiconductor Compiance





50

100

150

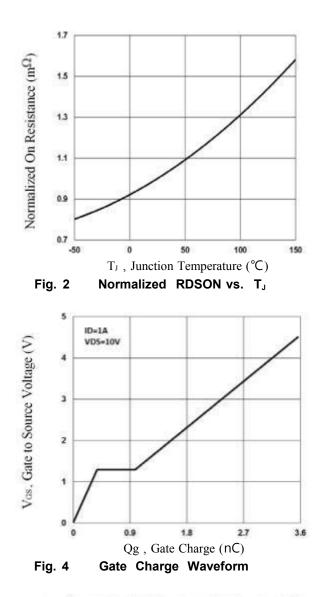
Fig. 5 Normalized Transient Impedance

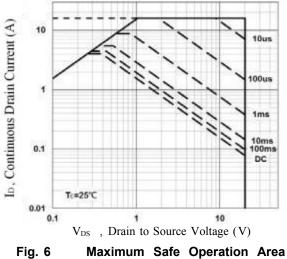
0.7

0.5

-50

0









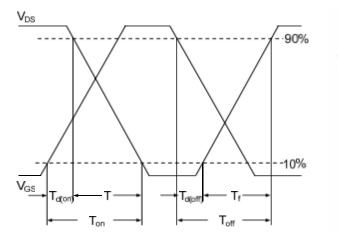
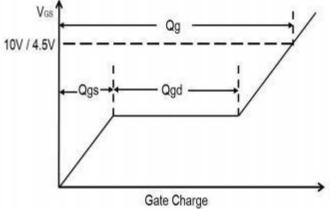
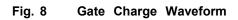


Fig. 7 Switching Time Waveform

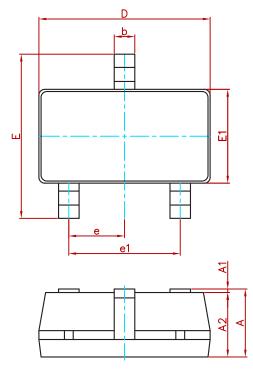


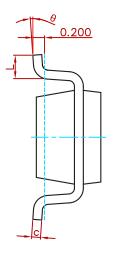






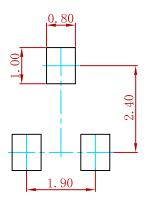
PACKAGE MECHANICAL DATA





Symbol	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)	0.037	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
AO3414	SOT-23-3L	3000





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