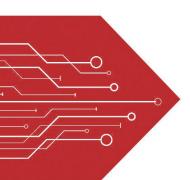
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Brodnet data speet

www.msksemi.com



SOT-23

FEATURES

- Fast Switching Speed
- For General Purpose Switching Applications
- High Conductance

MARKING:

MMBD4148A	MMBD4148CA	MMBD4148CC	MMBD4148SE
1 ○ → 3	1 ○ ★ 3	1 0 3	1 0 3
2 0	2 ○ ◀	2 0	2 ○ ◀
MARKING:5H	MARKING:D6	MARKING:D5	MARKING:D4
5 H	D 6	D5	D4

Maximum Ratings @Ta=25℃

Parameter	Symbol	Limit	Unit
Non-Repetitive Peak Reverse Voltage	V_{RM}	100	V
Peak Repetitive Peak Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
RMS Reverse Voltage	$V_{R(RMS)}$	72	V
Forward Continuous Current	I _{FM}	300	mA
Average Rectified Output Current	Ιο	200	mA
Non-Repetitive Peak Forward Surge Current @t=8.3ms	I _{FSM}	2.0	А
Power Dissipation	P _D	350	mW
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	357	°C/W
Junction Temperature	Tj	150	${\mathfrak C}$
Storage Temperature	T _{STG}	-55~+150	$^{\circ}$

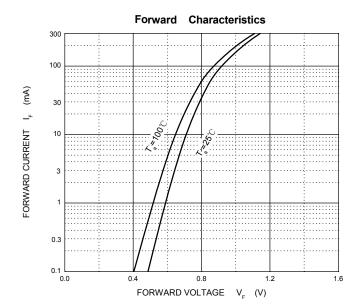
Electrical Ratings @Ta=25℃

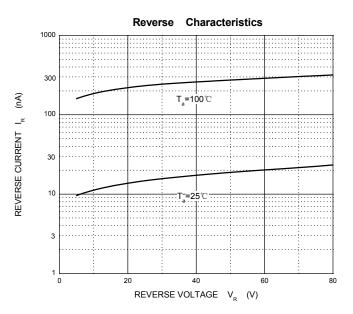
Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Reverse breakdown voltage	V _{(BR) 1}	100			V	I _R =100μA
	V (BR) 2	75			V	I _R =5μA
Forward voltage	V _F			1	V	I _F =10mA
Payara aurrent	I _{R1}			5	μΑ	V _R =75V
Reverse current	I _{R2}			25	nA	V _R =25V
Capacitance between terminals	Ст			4	pF	V _R =0V,f=1MHz
Doverno monovem timo	t _{rr}			4	ns	I _F =I _R =10mA, V _R =6V,
Reverse recovery time						Irr=0.1XI _R ,R _L =100 Ω

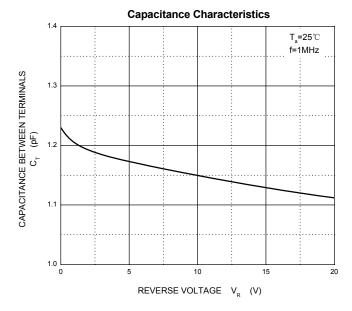


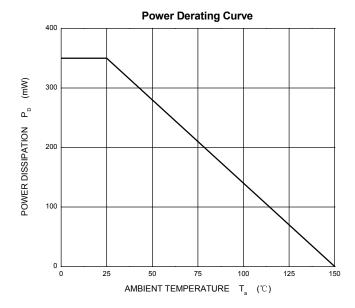
Semiconductor



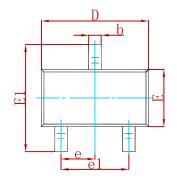


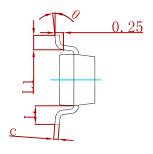


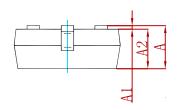




PACKAGE MECHANICAL DATA

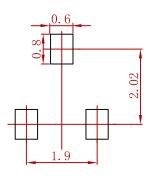






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min		Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022	2 REF	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

Suggested Pad Layout



- 1.Controlling dimension:in millimeters.2.General tolerance:± 0.05mm.3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MMBD4148A	SOT-23	3000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described orcontained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)