MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PIFD

74HC164D(MS)

产品规格手册





产品简介

74HC164D(MS) 是一款采用高速 CMOS 工艺技术设计的两线输入的 8 位移位寄存器。寄存器由主从 D 型触发 器构成,它具有很高的抗噪性和抗干扰性。在时钟 CLOCK 上升沿到来时 8 位二进制数据(Qa~Qg)向右移一位。带有一个清 0 输入端,可以轻松实现输出数据的清零。该移位寄存器也可根据需要实现多级芯片扩展输出。

产品特点

- 低输入电流: ≤1uA
- 传播延迟时间:典型值 20ns
- 低静态功耗: Icc≤5.0µ A,@ VCC=6V
- 复合使能输入,可轻松实现多级扩展
- 宽工作电压范围: 2.0V to 6.0V
- 封装形式: SOP14

产品用途

- 8位移位寄存器
- 自动化工程控制
- 其它应用领域

封装形式和管脚功能定义

封装图	脚位信息
	A 1 14 VCC B 2 13 Qh Qa 3 12 Qg Qb 4 11 Qf Qc 5 10 Qe Qd 6 9 CLEAR GND 7 8 CLOCK
SOP-14	

管脚序号	管脚定义	功能说明
1	A	数据输入端
2	В	数据输入端
3~6	Qa~Qd	Qa~Qd 数据输出端
7	GND	电源地
8	CLEAR	清0端,低电平有效
9	CLOCK	时钟控制端,上升沿有效
10~13	Qe~Qh	Qe~Qh 数据输出端
14	VCC	电源正

真值表

	Inputs				Outp	uts	
Clear	Clock	Α	В	Q_A	Q_{B}		Q_{H}
L	Х	Х	Χ	L	L		L
Н	L	Х	X	Q_{AO}	Q_{BO}		Q_{HO}
Н	1	Н	Н	Н	Q_{An}		Q_{Gn}
Н	1	L	X	L	Q_{An}		Q_{Gn}
Н	1	X	L	L	Q_{An}		Q_{Gn}

注:

$Q_A = AB$

- H 表示高电平;
- L 表示低电平;
- × 表示任意状态。
- ↑ 表示上升沿有效

Q_{AO}--Q_{HO} 表示保持原有状态

Q_{An}-Q_{Gn} 表示原有状态向右移位

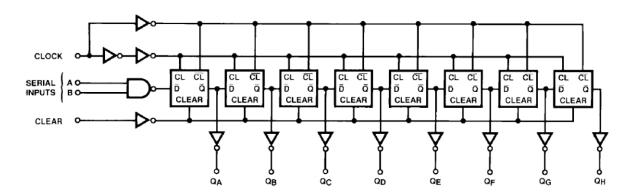


极限参数

参数	符号	极限值	单位
电源电压	V_{CC}	-0.5 to 6.5	V
输入/输出电压	V_{IN} , V_{OUT}	-0.5 to V _{CC} +0.5	V
输入/输出钳位电流	I_{IK} , I_{OK}	±20	mA
单个管脚输出电流	I_{OUT}	±25	mA
单个管脚接 VCC 或 GND 电流	I_{CC}	±50	mA
耗散功率	P_{D}	500	mW
工作温度	$T_{\mathbf{A}}$	0-70	${\mathbb C}$
存储温度	Ts	-65-150	${\mathbb C}$
引脚焊接温度	$T_{\mathbf{W}}$	260, 10s	$^{\circ}$ C

注:极限参数是指无论在任何条件下都不能超过的极限值。一旦超过此极限值,将有可能造成产品劣化等物理性损伤; 同时在接近极限参数下,不能保证芯片可以正常工作。

原理逻辑图



工作条件

项目		符号	最小值	典型值	最大值	单位
工作电压	V _{CC}		2	5	6	V
输入输出电压	VIN	, Vout	0	ı	VCC	V
<i>捻</i>) L 工 /	L	VCC=2.0V	0	1	1000	ns
输入上升/ 下降时间	L _r	VCC=4.5V	0	1	500	ns
1、14年11、1月	Lf	VCC=6. OV	0	_	400	ns



电学特性

直流电学特性: T_A=25℃

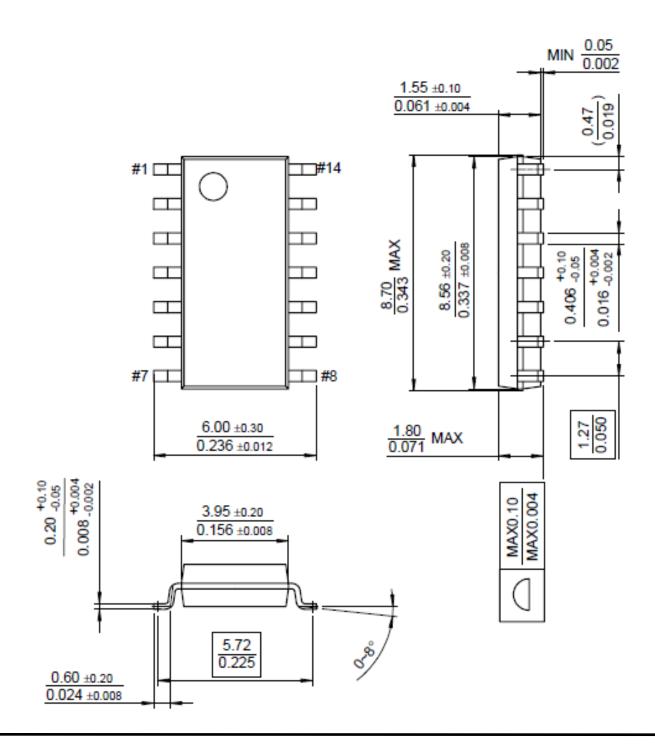
符号	项目	测试象	件	VCC(V)	最小值	典型值	最大值	单位					
				2.0	1.5	ı	-	V					
V_{IH}	高电平有效输入电压			4.5	3. 15	ı	-	V					
				6.0	4. 2	ı	-	V					
				2.0	_	ı	0.5	V					
$V_{\rm IL}$	低电平有效输入电压			4.5	_	ı	1.35	V					
				6.0	_	1	1.8	V					
				2.0	1.9	_	_	V					
		V_{I} = V_{IH} or V_{IL}		I _{OH} =20μA	4. 5	4. 4	I	-	V				
V_{OH}	高电平输出电压				6.0	5. 9	_	_	V				
				VIH OI VIL	VIH OI VIL	VIH OI VIL	VIH OI VIL	VIH OI VIL	VIH OI VIL	$I_{\text{OH}}{=}4.0\text{mA}$	4.5	3.9	4.3
			$I_{\text{OH}} = 5.2 \text{mA}$	6.0	5. 2	5. 7		V					
		V	, I	I _{OH} =20μA	2.0	_	_	0.1	V				
					W	W	\/	$I_{OH}=2$	I _{OH} =20μA	4.5	_	_	0.1
V_{OL}	低电平输出电压	V _I =		6.0	_	_	0.1	V					
		V _{IH} or V _{IL}	VIH OI VIL	VIH OI VIL	$I_{\text{OH}}{=}4.0\text{mA}$	4.5	_	0.2	0.5	V			
			$I_{\text{OH}} = 5.2 \text{mA}$	6.0	_	0.3	0.5	V					
I_{IN}	输入电流	V _I =V _{CC} or GND		6.0	_	_	1	uA					
I_{CC}	工作电流	$V_I = V_{CC}$ or GND, $I_{OUT} = 0\mu A$		6.0	_	_	5	uA					
V_{CC}	工作电压				2	_	6	V					

交流电学特性: Ta=25℃ V_{CC}=5.0V, C_L=16pF, tr=tf≤20ns。

符号	项目	测试条件	最小值	典型值	最大值	单位
$t_{\mathtt{PHL}}$	传输延迟时间	_	_	24	_	ns
t_{PLH}	Clock to Output	1	=	18	-	ns
$t_{\mathtt{PHL}}$	传输延迟时间	1	=	25	-	ns
t_{PLH}	Clear to Output	1	=	17	-	ns
f_{MAX}	传输延迟时间	1	=	22	-	ns
t _{REM}	最小清除时间 Clear to Clock	1	=	5	-	ns
ts	最小设置时间 Data to Clock	1	=	30	-	ns
t _H	最小保持时间 Clock to Data	_	=	10	_	ns
t _W	最小脉宽 Clock or Clear	=	_	18	_	ns



S0P14



订购信息

P/N	PKG	QTY
74HC164D(MS)	SOP-14	2500



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