MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

AONR21321-MS

Product specification





AONR21321-MS

Description

The AONR21321-MS uses advanced trench technology excellent RDS(ON), low gate charge and operation with gate

voltages as low as 4.5V. This device is suitable for use as aload switch or in PWM applications .

Features

 $V_{DS} = -30V, I_D = -50A$ RDS(ON) < 25m Ω @ VGS=-4.5V

 $RDS(ON) < 15m\Omega @ VGS=-10V$

High Power and current handing capability

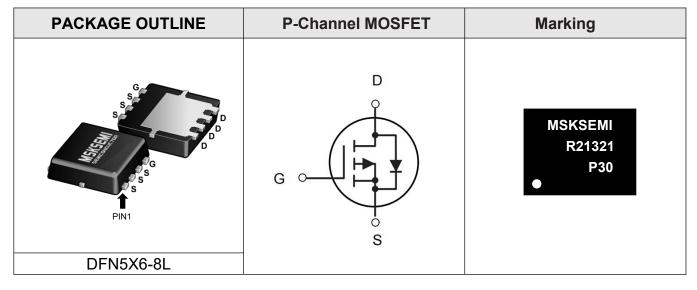
Lead free product is acquired

Application

- PWM applications
- Load switch
- Power management

Surface mount package

Reference News



Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit	
VDS	Drain-Source Voltage	-30	V	
VGS	Gate-Source Voltage	±20	V	
	Drain Current-Continuous (Tc=25 °C)			
D	Drain Current-Continuous (Tc=100 ℃)	-24	A	
IDM	Drain Current-Pulsed (Note 1)	-80	А	
	Maximum Power Dissipation (Tc=25 °C)	3	W	
PD	Maximum Power Dissipation (Tc=100 °C)	1.3		
EAS	Single pulse avalanche energy (Note 5)	231	mJ	
TJ, TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C	
RθJA	Thermal Resistance, Junction-to-Ambient (Note 2)	41.67	°C/ W	



Electrical Characteristics (TA=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V I⊵=-250µA	-30	-33	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-30V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	IGSS	Vgs=±20V,Vds=0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	Vos=Vgs,Io=-250µA	-1	-1.5	-3	V
	RDS(ON)	V _{GS} =-10V, I _D =-10A	-	9	15	mΩ
Drain-Source On-State Resistance		Vgs=-4.5V, Id=-7A	-	18	25	mΩ
Forward Transconductance	gFS	VDs=-10V,ID=-10A	-	20	-	S
Input Capacitance	Clss		-	1750	-	PF
Output Capacitance	Coss	V _D s=-15V,V _G s=0V, F=1.0MHz	-	215	-	PF
Reverse Transfer Capacitance	Crss	1 – 1.0ivii iz	-	180	-	PF
Turn-on Delay Time	td(on)		-	9	-	nS
Turn-on Rise Time	tr	V _{DD} =-15V, ID=-10A,	-	8	-	nS
Turn-Off Delay Time	td(off)	Vgs=-10V,Rgen=1 Ω	-	28	-	nS
Turn-Off Fall Time	tr		-	10	-	nS
Total Gate Charge	Qg		-	24	-	nC
Gate- Source Charge	Qgs	Vos=-15V,Io=-10A,Vgs=- 10V	-	3.5	-	nC
Gate-Drain Charge	Qgd		-	6	-	nC
Diode Forward Current (Note 2)	ls		-	-	-12	А
Diode Forward Voltage (Note 3)	VSD	Vgs=0V,Is=-12A	-	-	-1.2	V

Notes:

 $\ensuremath{\textbf{1. Repetitive Rating: Pulse width limited by maximum junction temperature}\ .$

- **2.** Surface Mounted on FR4 Board, t \leq 10 sec .
- 3. Pulse Test: Pulse Width ≤ 300 $\mu s,$ Duty Cycle ≤ 2% .
- 4. Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25C, V_{DD}=- 15V, V_G=10V, L=0 .5mH, Rg=25\Omega, I_{AS}=-34A



Typical Electrical and Thermal Characteristics

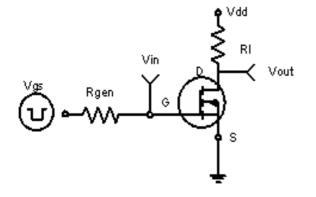
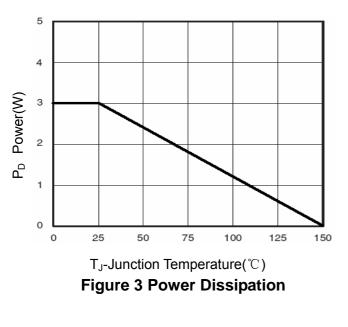
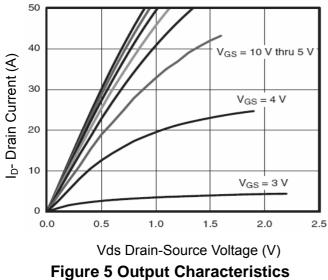
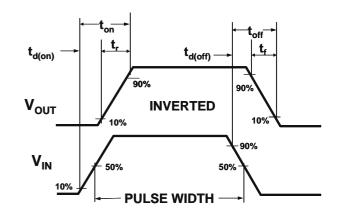


Figure 1:Switching Test Circuit









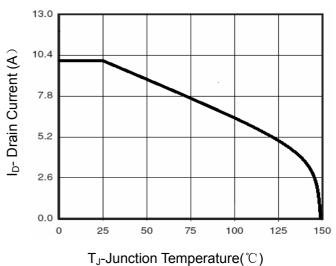


Figure 4 Drain Current

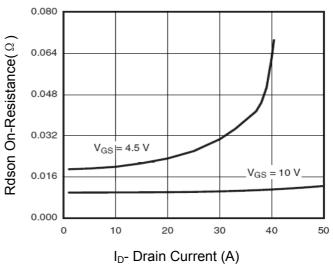
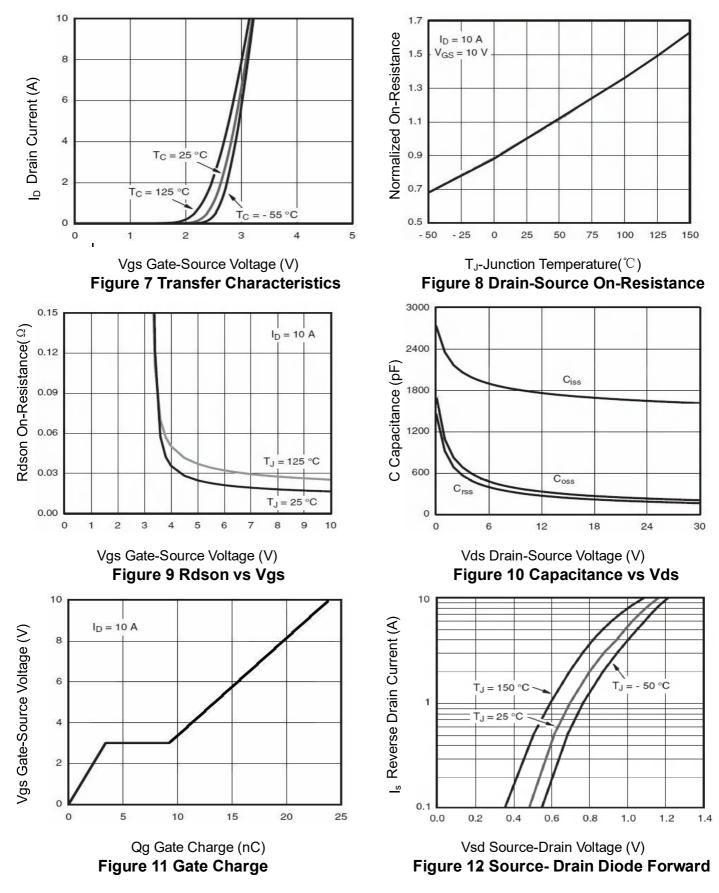


Figure 6 Drain-Source On-Resistance



Figure 5 Output Characteristics





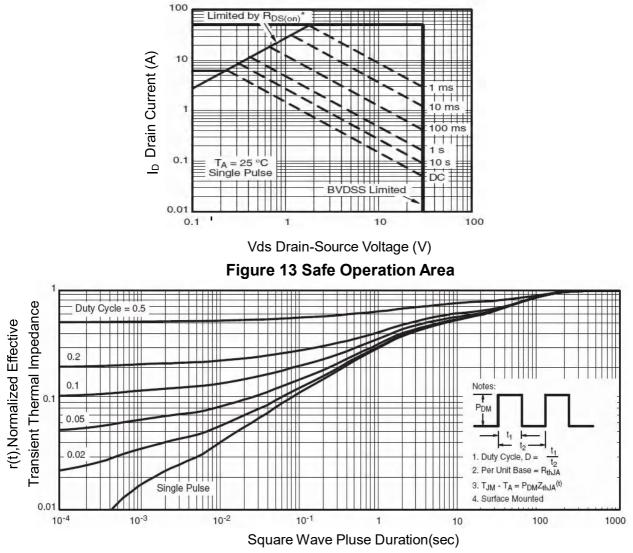
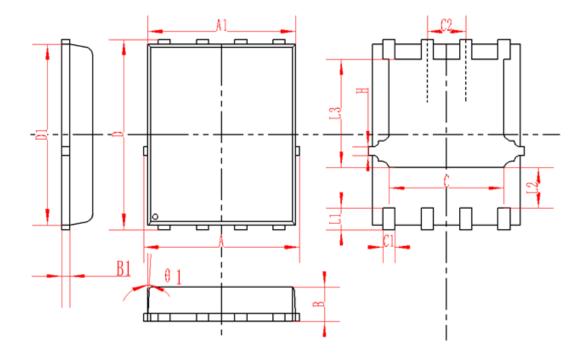


Figure 14 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



SYMBOL	MM		INCH			
STIVIDUL	MIN	NOM	MAX	MIN	NOM	MAX
A	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8.	10.	12 _°	8.	10.	12.
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

REEL SPECIFICATION

P/N	PKG	QTY
AONR21321-MS	DFN5X6-8L	5000



AONR21321-MS

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