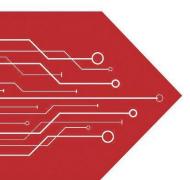
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Product data sheet

www.msksemi.com

Semiconductor

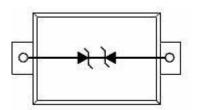


Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

Features

- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 200 Watts @ 8 x 20 _s
 Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection



SOD- 523

Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.VF = 0.9V at IF = 10mA

P/N	V _{RWM} (V)	I _R (uA) @ V _{RWM}	V _{BR} (V)@ I _T (Note 1)	Ιτ	V _C (V) @ I _{PP} =5 A*	V _C (V) @ Max I _{PP} *	I _{PP} (A)*	P _{PK} (W)*	C (pF)
	Max	Max	Min	mA	Тур	Max	Max	Max	Тур
MSESD5D3V3C	3.3	1	5.0	1.0	11.6	18.6	9.4	174	25

^{*}Surge current waveform per Figure 1.

Absolute Ratings (Tamb=25°C)

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (t _p = 8/20 μ s)	200	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +155	°C
T _{op}	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge contact discharge	±15 ±8	KV
	IEC61000-4-4 (EFT)	40	Α
	ESD Voltage Per Human Body Model	16	KV

^{1.} V_{BR} is measured with a pluse test current I_T at an ambient temperature of 25 $^{\circ}$ C.

Electrical Parameter

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
V_{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
Ι _Τ	Test Current
V_{BR}	Breakdown Voltage @ I _T

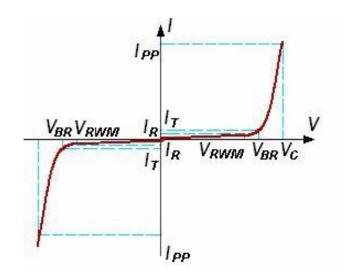


FIG1: Pulse Waveform

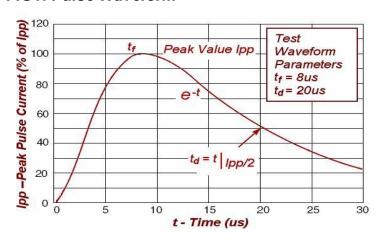
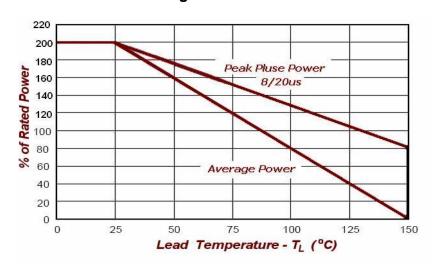
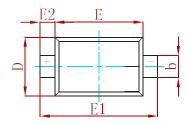


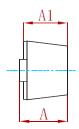
FIG2:Power Derating

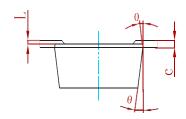




PACKAGE MECHANICAL DATA

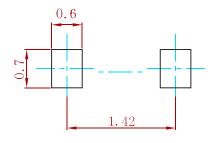






Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	0.510	0.770	0.020	0.031	
A1	0.500	0.700	0.020	0.028	
b	0.250	0.350	0.010	0.014	
С	0.080	0.150	0.003	0.006	
D	0.750	0.850	0.030	0.033	
E	1.100	1.300	0.043	0.051	
E1	1.500	1.700	0.059	0.067	
E2	0.200 REF		0.008 REF		
L	0.010	0.070	0.001	0.003	
0	7° ŘEF		7° REF		

Suggested Pad Layout



Note:

- 1. Controlling dimension:in millimeters.
- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MSESD5D3V3C	SOD-523	3000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)