

SFECF10M7DA0001-R0

Note: This datasheet may be out of date.

Please download the latest datasheet of SFECF10M7DA0001-R0 from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en-us/products/productdetail?partno=SFECF10M7DA0001-R0



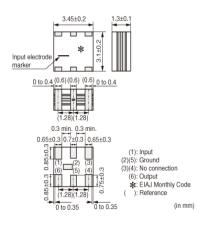






Appearance & Shape







Features

SFECF10M7 series for FM receivers are small, high-performance and super thin (1.4mm max.) filters. The piezoelectric element is sandwiched by the ceramics substrate. They have 1.4mm max. thickness and a small mounting area (3.45x3.1mm). SFECF series and CDSCB series (MHz Discriminator) enable customers to make VICS/RKE/TPMS set very thin and small.

Features

- 1. The filters are mountable by automatic placers.
- 2. They are slim, at only 1.4mm max. thickness, and have a small mounting area (3.45x3.1mm) enabling flexible PCB design.
- 3. Various bandwidths are available. Select a suitable type in accordance with the desired selectivity.
- 4. Operating Temperature Range: -20 to +80 (degrees C), Storage Temperature Range: -40 to +85 (degrees C)



Applications

Other Usage Communication Equipment/Audio Visual



Packaging Information

Packaging	Specifications	Minimum Order Quantity
R0	180mm Embossed Tape	2000

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Attentior

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2. This datasheet has only typical specifications because there is no space for detailed specifications

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering





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Specifications

Operating Temperature	T .	
Range	-20°C to 80°C	
Shape	SMD	
Elements	2	
Center Frequency	10.700MHz	
Center Frequency Tolerance	±30kHz	
Nominal Center Value	No	
3dB Bandwidth	420kHzmin.	
Stop Bandwidth	950kHz	
Area of Stop Bandwidth	[within 20dB]	
Spurious Attenuation	35dB[within 9MHz to fo]	
Insertion Loss	3.0dB±2.0dB(at minimum loss point)	
Ripple	3.0dBmax.(at minimum loss point)	
Input/Output Impedance	330Ω	
Mass	65mg	

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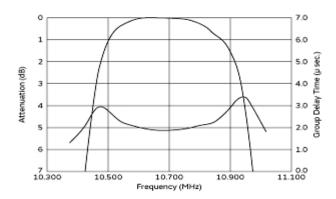
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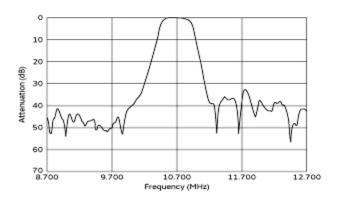
Manufacturing Co., Ltd.

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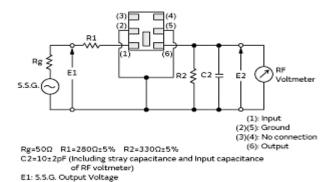
Product Data





Frequency Characteristics

Spurious Response



Measurement Circuit

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>>Murata(村田)