

Reference Specification

200°C Operation Leaded MLCC for Automotive with AEC-Q200 RHS Series

Product specifications in this catalog are as of Nov. 2020, and are subject to change or obsolescence without notice.

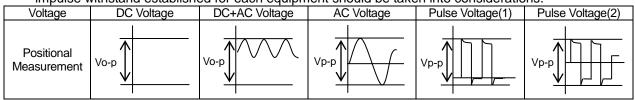
Please consult the approval sheet before ordering.Please read rating and Cautions first.

Δ CAUTION

1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for each equipment should be taken into considerations.



2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the selfgenerated heat due to dielectric-loss. In case of Class 2 capacitors (Temp.Char. : X7R,X7S,X8L, etc.), applied voltage should be the load such as self-generated heat is within 20 °C on <u>the condition of</u> <u>atmosphere temperature 25 °C</u>. Please contact us if self-generated heat is occurred with Class 1 capacitors (Temp.Char. : C0G,U2J,X8G, etc.). When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.

3. Fail-safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

4. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 °C and 20 to 70%. Use capacitors within 6 months.

5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

7. BONDING AND RESIN MOLDING, RESIN COAT

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of a bonded or molded product in the intended equipment. In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive or molding resin may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

8. TREATMENT AFTER BONDING AND RESIN MOLDING, RESIN COAT

When the outer coating is hot (over 100 $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause furning or partial dispersion when the product is used.

9. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- Undersea equipment
 Medical equipment
- 2. Aerospace equipment
- Power plant control equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)8. Disaster prevention / crime prevention equipment
- 7. Traffic signal equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions. Rinse bath capacity : Output of 20 watts per liter or less.

Rinsing time : 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

- 2. Soldering and Mounting
 - Insertion of the Lead Wire
 - When soldering, insert the lead wire into the PCB without mechanically stressing the lead wire.
 - Insert the lead wire into the PCB with a distance appropriate to the lead space.

3. CAPACITANCE CHANGE OF CAPACITORS

• Class 2 capacitors (Temp.Char. : X7R,X7S,X8L, etc.)

Class 2 capacitors an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit.

Please contact us if you need a detail information.

▲ NOTE

- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

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. Rat	-					e up to 200°C ative time to 2	C 200°C is within	2000 ł	nours.		
	• Pa			configura							
ex.)	RHS	S	7	G	2A	101	J	0	A2	H01	В
	Serie				Rated voltage	Capacitance	Capacitance tolerance	Dimens		Individual specification code	Packin style code
	• Se	ries									
		Co	de			Content					
		RF	IS		Ерох	y coated, 20	0°C max.				
	• To	mpor	oturo	abaraata	riatio						
	• Tel		Te	character mp. har.		p. Range	Temp. coeff.(ppm/	/°C)	Standard Temp.	Operatin Temp. Rar	•
			С	CG		5∼25°C	0+30/-72	2			
	7G	1)		a code)		∼125°C ∼200°C	0±30 0+72/-30	0	25°C	-55 ~ 200°	°C
-					•						
	• Do	tod v	- 14								
	• Ra		oltag			11					
	• Nd	Со	de		ated vo	-					
		Co 2	ode A	R	DC100	V	150°C please	a use th	is product		
	ge(%)	Co 2 Whei	ode A n the	R product te	DC100)V ture exceeds	150°C, please ted conditions				
	ge(%)	Co 2 When withir	ode A n the	R product te	DC100)V ture exceeds					
	,	Co 2 When withir	n the	R product te	DC100	DV ture exceeds erature derat	ted conditions	in the fi		200	

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Capacitance

The first two digits denote significant figures ; the last digit denotes the multiplier of 10 in pF. ex.) In case of 101.

Capacitance tolerance

Code	Capacitance tolerance
J	+/-5%

• Dimension code

Code	Dimensions (LxW) mm max.
0	3.9 x 3.5
1	4.2 x 3.5

• Lead code

Code	Lead style	Lead spacing (mm)
A2	Straight type	2.5+/-0.8
DG	Straight taping type	2.5+0.4/-0.2
K1	Inside crimp type	5.0+/-0.8
M2	Inside crimp taping type	5.0+0.6/-0.2

Lead wire is solder coated CP wire.

 Individual specification code Murata's control code
 Please refer to [Part number list].

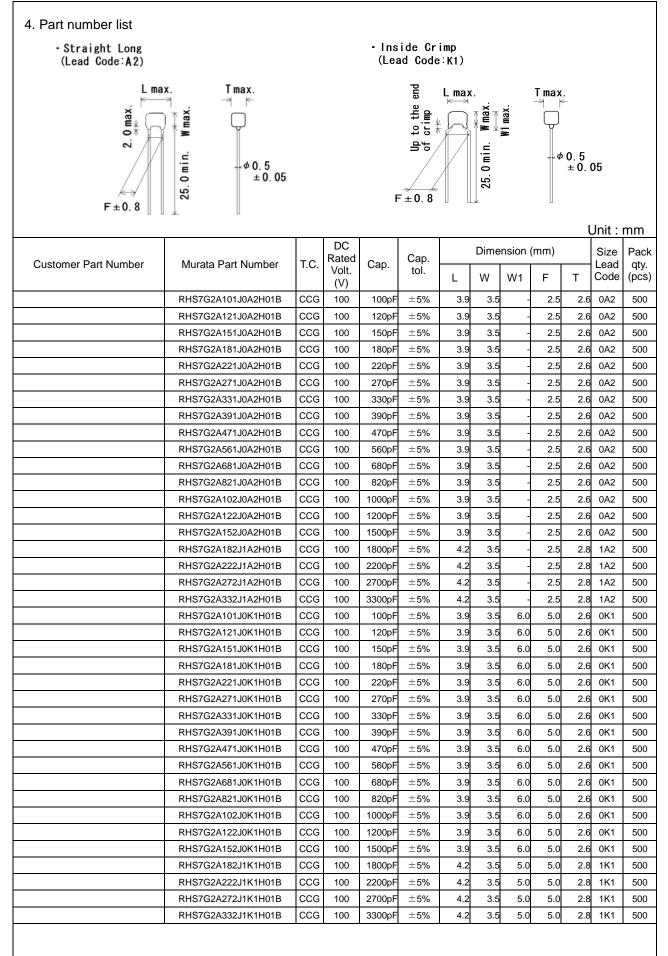
• Packing style code

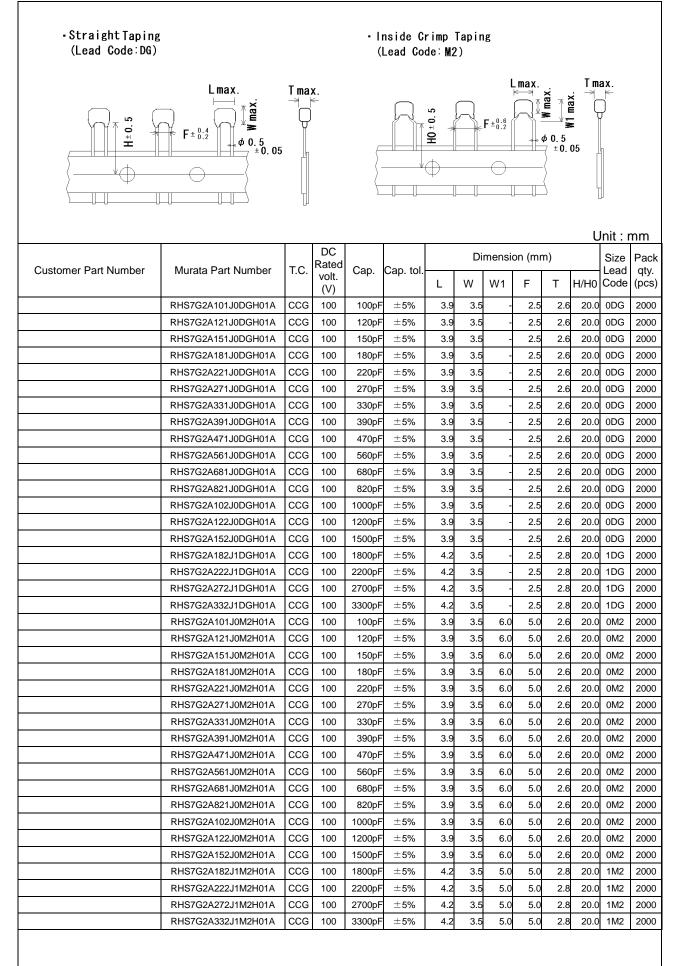
Code	Packing style
Α	Taping type of Ammo
В	Bulk type

3. Marking

Temp. char.	: Letter code : 4 (CCG char.)
Capacitance	: 3 digit numbers
Capacitance tolerance	e : Code

(Ex.)	
Rated voltage	100V
Dimension code	
0,1	4 101J



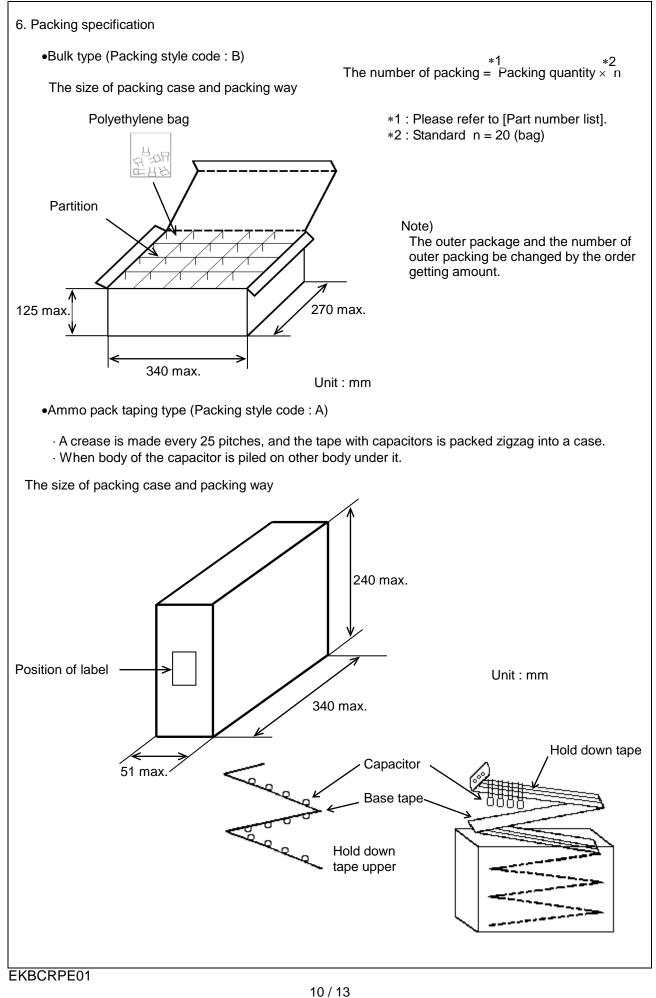


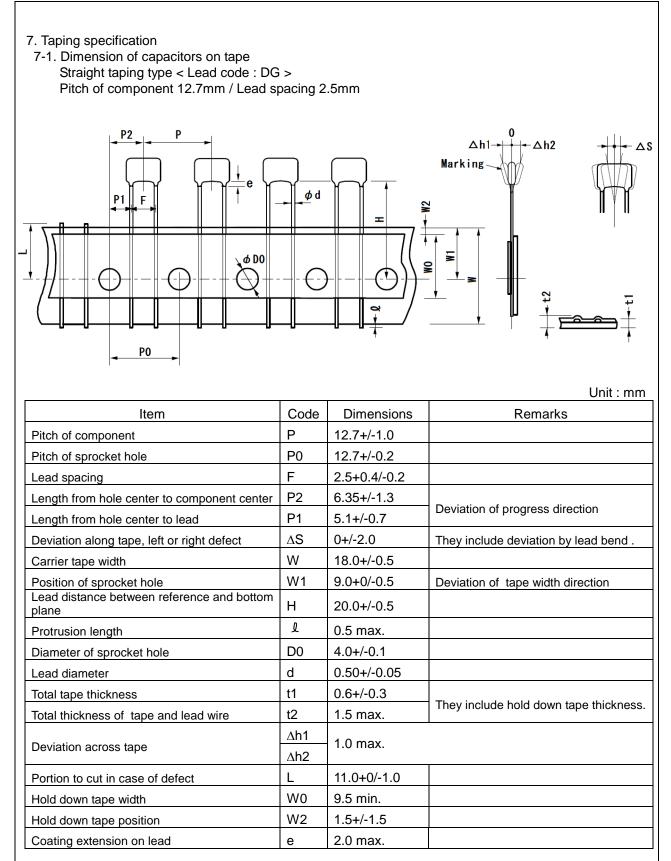
Temperature Exposure (Storage) Temperature Cycling Moisture	Stress	$\begin{tabular}{ c c c c } \hline Specification \\ \hline No defects or abnormalities except color change of outer coating. \\ \hline Within ±3% or ±0.3pF \\ (Whichever is larger) \\ \hline Q \ge 350 \\ 1,000M\Omega min. \\ \hline No defects or abnormalities except color \\ change of outer coating \\ \hline Within ±5% or ±0.5pF \\ (Whichever is larger) \\ \hline Q \ge 350 \\ 1,000M\Omega min. \\ \hline \end{tabular}$	Perform listed in then m S Te	m the n the neasu	citor for 1,000 tion, then mea 1,000 cycles following table	asure.	0±5°C. Let sit	t treatment
Electrical Test High Temperature Exposure (Storage) Temperature Cycling Moisture	Appearance Capacitance Change Q I.R. Appearance Capacitance Change Q	outer coating. Within $\pm 3\%$ or $\pm 0.3pF$ (Whichever is larger) $Q \ge 350$ 1,000M Ω min. No defects or abnormalities except color change of outer coating Within $\pm 5\%$ or $\pm 0.5pF$ (Whichever is larger) $Q \ge 350$	Perform listed in then m S Te	m the n the neasu	tion, then means the state of t	asure.	o the four hea	t treatment
High Temperature Exposure (Storage) Temperature Cycling Moisture	Appearance Capacitance Change Q I.R. Appearance Capacitance Change Q	outer coating. Within $\pm 3\%$ or $\pm 0.3pF$ (Whichever is larger) $Q \ge 350$ 1,000M Ω min. No defects or abnormalities except color change of outer coating Within $\pm 5\%$ or $\pm 0.5pF$ (Whichever is larger) $Q \ge 350$	Perform listed in then m S Te	m the n the neasu	tion, then means the state of t	asure.	o the four hea	t treatment
(Storage) Temperature Cycling Moisture	Change Q I.R. Appearance Capacitance Change Q	$\begin{array}{l} (Whichever is larger) \\ Q \geq 350 \\ 1,000M\Omega \mbox{ min.} \\ No defects or abnormalities except color \\ change of outer coating \\ Within \pm 5\% \mbox{ or } \pm 0.5pF \\ (Whichever is larger) \\ Q \geq 350 \end{array}$	listed in then m	n the neasur	following table			
Temperature Cycling Moisture	I.R. Appearance Capacitance Change Q	$\begin{array}{l} 1,000 M\Omega \text{ min.} \\ \text{No defects or abnormalities except color} \\ \text{change of outer coating} \\ \text{Within } 55\% \text{ or } \pm 0.5 p\text{F} \\ (\text{Whichever is larger}) \\ \text{Q} \geq 350 \end{array}$	listed in then m	n the neasur	following table			
Temperature Cycling Moisture	Appearance Capacitance Change Q	No defects or abnormalities except color change of outer coating Within $\pm 5\%$ or $\pm 0.5pF$ (Whichever is larger) $Q \ge 350$	listed in then m	n the neasur	following table			
Cycling Moisture	Capacitance Change Q	change of outer coating Within $\pm 5\%$ or $\pm 0.5pF$ (Whichever is larger) $Q \ge 350$	listed in then m	n the neasur	following table			
Docietonoo	Change Q	(Whichever is larger) Q ≥ 350	Те		с.			
Docietonoo			Те	ton	1	2	3	4
Docietonoo	I.R.	1,000MΩ min.		emp.	-	Room		4 Room
Docietonoo			Ťi	°C) ime nin.)	-55+0/-3 15±3	Temp. 1	200+5/-0 15±3	Temp.
Resistance	Appearance	No defects or abnormalities					humidity (80 to	o 98%)
	Capacitance				own below, 1		ive times. then measure	
	Change	(Whichever is larger)	Tempera			Humidity		, nidity
	Q I.R.	Q ≥ 200 500MΩ min.	(°C)	aure	Humidity	80~98%	Humidity 80~9	98% Humio ₩ 90~98
			70	1	90~98%	, , , , , , , , , , , , , , , , , , , , 	90~98%	y< 30~98
			55		+			++++
					+/++		A + + h	
			nter 1945		 	+++	/ 	+++
			840 E25					
			^{⊕35} [−] 30					Π
			25	÷Я <mark>⊢</mark> ┦				++++
			20		+		++++	++++
			15	1			++++	
				Initial	measurement			
			0					++++
			-5				++++	++++
			-10			One cycle	24 hours	
				0 1	234567	8 9 10 11 12	2 13 14 15 16 17 18	3 19 20 21 22
		No defects or abnormalities	Apply the rated voltage and DC1.3+0.2/-0 V (add $100k\Omega$ res					
Humidity		1						hon moasi
-								len meast
	7	500MΩ min.						
	Appearance	No defects or abnormalities except color						
		change of outer coating				,		•
			The ch	large/	•			
	-		-		Capacitance		Test Voltage	1
-		1,000MΩ min.	1					<u> </u>
				1	200pF-3300p	F 25%	of the rated v	oltage
External Visua	al	No defects or abnormalities	Visual	inspe	ction			
Physical Dime		Within the specified dimensions	Using	calipe	rs and micror	neters.		
Marking		To be easily legible.						
-							nronvi alcohol	
			3 parts (by volume) of mineral spirits Solvent 2 : Terpene defluxer					
				Solvent 3 : 42 parts (by volume) of water 1part (by volume) of propylene glycol				
							noethanolami	ne
condition" To	emperature:1	I 5 to 35°C, Relative humidity:45 to 75%, Atmosphere p	ressure:8				noethanoiann	
	-lumidity Deperational Life External Visua Physical Dime Marking Resistance o Solvents	Biased Humidity Provide a state of the sta	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{array}{ c c c c c c } \hline I.R. & SUUM2 min. & & & & \\ \hline & & & & \\ \hline & & & & \\ \hline & & & &$	$\begin{array}{ c c c c c c c } \hline R & \text{SUUME2 min.} & \begin{array}{c} & & & & & & & & & & & & & & & & & & &$	I.R. SUMM2 min. 90-8%, ♥ <td< td=""></td<>

No.	AEC-0 Test		Specification		AE	C-Q200 Test N	/lethod	
11	Mechanical	Appearance	No defects or abnormalities				be applied along	
	Shock	Capacitance	Within the specified tolerance	mutually perpendicular axes of the test specimen (18 shoc The specified test pulse should be Half-sine and should ha				d have a
		Q	Q ≥ 1,000	duration :0.5ms, peak value:1,500G and velocity change				
12	Vibration	Appearance	No defects or abnormalities	The capacitor should be subjected to a simple harmonic				
		Capacitance	Within the specified tolerance	having a total amplitude of 1.5mm, the frequency beir uniformly between the approximate limits of 10 and 2				
		Q	Q ≥ 1,000	The frequency range, from 10 to 2,000Hz and return to 10Hz, should be traversed in approximately 20 min. This motion should be applied for 12 items in each 3 mutually perpendicula directions (total of 36 times).			10Hz, on	
13-1	Resistance to Soldering Heat	Appearance	No defects or abnormalities				the melted sold 0±5°C for 10±1 s	
	(Non-Preheat)	Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	• Post tr	eatment			
		Dielectric Strength (Between terminals)	No defects			tored for 24±2 h	nours at *room c	ondition.
13-2	Resistance to	Appearance	No defects or abnormalities	First the	capacitor sho	uld be stored at	120+0/-5°C for	60+0/-5
	Soldering Heat	Capacitance	Within ±2.5% or ±0.25pF	seconds		hould be imm	sed in the melte	d 00/do-
	(On-Preheat)	Change	(Whichever is larger)	- , .			at 260±5°C for	
		Dielectric Strength	No defects	seconds				
		(Between						
		terminals)			eatment	stored for 24+2	hours at *room	condition
13-3	Resistance to	Appearance	No defects or abnormalities	Test cor				contantion
	Soldering Heat (soldering iron method)			Termpe				
		Capacitance	Within ±2.5% or ±0.25pF	Soldering time : 3.5±0.5 seconds Soldering position				
		Change Dielectric	(Whichever is larger) No defects	Straight Lead:1.5 to 2.0mm from the root of term			root of terminal.	
		Strength (Between terminals)		Crimp Lead:1.5 to 2.0mm from the end of lead bend. • Post-treatment				
		,				hours at *room		
14	Thermal Shock	Appearance	No defects or abnormalities	Perform the 300 cycles according to the two heat treatment in the following table(Maximum transfer time is 20s.). L				
		Capacitance Change	Within ±5% or ±0.5pF (Whichever is larger)	In the following table (Maximum transfer 24 ± 2 h at *room condition, then measur				
		Q	Q ≥ 350		Step	1	2	
		Q I.R.	Q ≥ 350 1,000MΩ min.		Temp. (°C)	-55+0/-3	200+5/-0	
					Time			_
					(min.)	15±3	15±3	
15	ESD	Appearance	No defects or abnormalities	Per AEC-	Q200-002			
		Capacitance	Within the specified tolerance					
		Q	Q ≥ 1,000					
		I.R.	10,000MΩ min.					
16	Solderability		Lead wire should be soldered with uniform coating on the axial direction over 95% of the circumferential direction.	(JIS-K-81 propotion In both ca the termin Temp. of 245±5°	01) and rosin and then into ases the depth hal body. solder : C Lead Free S	(JIS-K-5902) (2 molten solder	(JIS-Z-3282) for to about 1.5 to -0.5Cu)	ht 2±0.5 se
100111 (ononion temper	aure. 10 10 30 °C	, Relative humidity:45 to 75%, Atmosphere pre:	55UI€.00 [U I UUKFA			

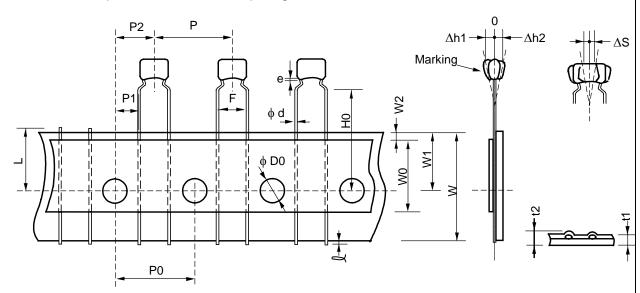
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lo.	AEC-Q200 Test Item		Specifications		AEC-Q200 Test Method				
7	Electrical	Apperance	No defects or	abnormalities	Visual inspection. The capacitance, Q should be measured at 25°C at the frequence and voltage shown in the table.				
	Characte- rization	Capacitance Q	Within the sp $Q \ge 1,000$	ecified tolerance					
						$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	Frequency 1±0.1MHz 1±0.1kHz	Voltage AC0.5 to 5V(ms) AC1±0.2V(ms)	
		Insulation Resistance (I.R.)	Room Temperature	10,000MΩ min.	DC vo and h		g the rated vo 2 min. of char	easured at 25±3 °C with oltage at normal temperat ging.	
			High Temperature	20MΩ min.	DC vo chargi	oltage not exceedin ing. ge/Discharge curre	g voltage in Tant \leq 50mA)	easured at 200±5 °C with able and within 2 min. of	
						Capacitance		est Voltage	
						100pF-1000pF 1200pF-3300pF		the rated voltage the rated voltage	
		Dielectric Strength	Between Terminals	No defects or abnormalities	applie	d between the tern ge/Discharge curre	ninations for 1		
						Rated voltage DC100V	300%	Test voltage of the rated voltage	
			Insulation		termin 2mm t and vo secon metal	balls of 1mm diam al, short-circuit, is from the balls as si oltage in table is im ds between capac balls. ge/Discharge curre	kept approxim nown in the fig pressed for 1 tor terminals	nately gure, Approv to 5 2r	
						Rated voltage DC100V	250%	Test voltage of the rated voltage	
18	Terminal Strength				to eac		direction of th	apply the force gradually ne capacitor until reachin or 10±1 seconds.	
		Bending Strength	Termination n	oot to be broken or loosened	be be then r	ent 90° at the point	of egress in o nal position a	a force of 2.5N and ther ne direction. Each wire is nd bent 90° in the oppos to 3 seconds.	
19	Capacitance Temperature Characteristics		mperature 0+30/-72ppm/°C (-55~25°C)		The c		should be me	easured after 5min. at ure(°C) 2 3 2 5	
					measu seque the ca tempe The ca	ured in step 3 as a ntially from step 1 pacitance should b rature coefficient a apacitance drift is o	reference. Wi through 5 (-55 be within the s and capacitanc caluculated by	nd using the capacitance hen cycling the temperat 5°C to +150°C) pecified tolerance for the ce change as Table A. r dividing the differences measured values in the s	



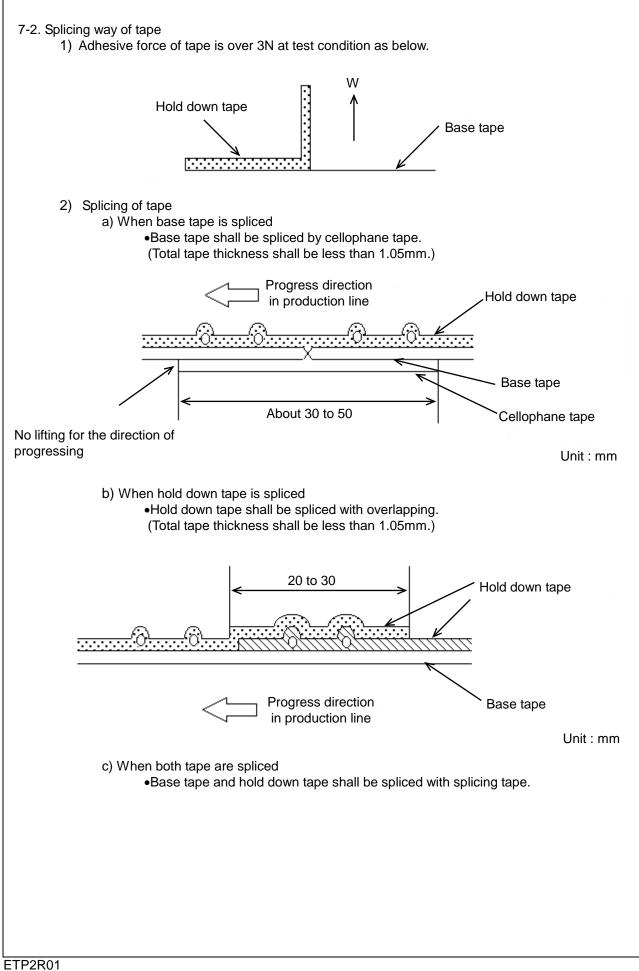


Inside crimp taping type < Lead code : M2 > Pitch of component 12.7mm / Lead spacing 5.0mm



Unit : mm

Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7+/-1.0	
Pitch of sprocket hole	P0	12.7+/-0.2	
Lead spacing	F	5.0+0.6/-0.2	
Length from hole center to component center	P2	6.35+/-1.3	Deviation of anomalo disastica
Length from hole center to lead	P1	3.85+/-0.7	Deviation of progress direction
Deviation along tape, left or right defect	ΔS	0+/-2.0	They include deviation by lead bend.
Carrier tape width	W	18.0+/-0.5	
Position of sprocket hole	W1	9.0+0/-0.5	Deviation of tape width direction
Lead distance between reference and bottom plane	HO	20.0+/-0.5	
Protrusion length	l	0.5 max.	
Diameter of sprocket hole	D0	4.0+/-0.1	
Lead diameter	φd	0.50+/-0.05	
Total tape thickness	t1	0.6+/-0.3	They include held down tone thickness
Total thickness of tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation corrections	∆h1	2.0 max. (Dime	ension code : W)
Deviation across tape	∆h2	1.0 max. (exce	pt as above)
Portion to cut in case of defect	L	11.0+0/-1.0	
Hold down tape width	W0	9.5 min.	
Hold down tape position	W2	1.5+/-1.5	
Coating extension on lead	е	Up to the end of c	rimp



单击下面可查看定价,库存,交付和生命周期等信息

>>Murata(村田)