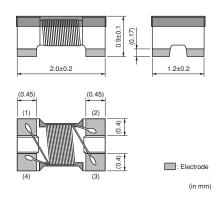
Data Sheet

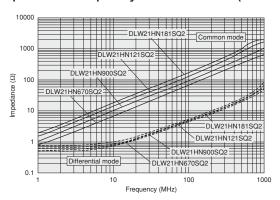
Common Mode Choke Coil Wire Wound Type

DLW21H Series (0805 Size)

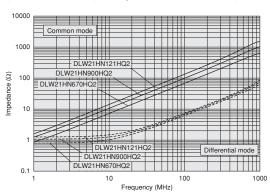
Dimensions



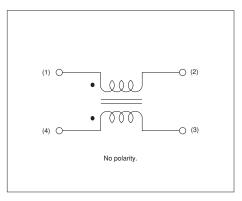
■ Impedance-Frequency Characteristics (Main Items)



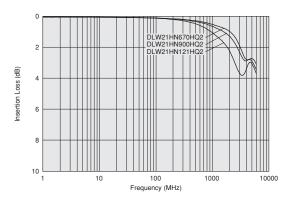
■ Impedance-Frequency Characteristics (Main Items)



■ Equivalent Circuit



■ Differential Mode Transmission Characteristics (Typ.)



Packaging

Code	Packaging	Minimum Quantity		
L	180mm Embossed Tape	3000		
В	Bulk(Bag)	500		

Continued on the following page.



This data sheet is applied for CHIP COMMON MODE CHOKE COIL used for General Electronics equipment for your design.

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- 2. This datasheet has only typical specifications because there is no space for detailed specifications. Therefore, please approve our product specifications or transact the approval sheet for product specifications before ordering.

Data Sheet

Ontinued from the preceding page.

■ Rated Value (□: packaging code)

Part Number	Common Mode Impedance (at 100MHz/20°C)	Rated Current	Rated Voltage	Insulation Resistance (min.)	Withstand Voltage	DC Resistance	Operating Temperature Range
DLW21HN670SQ2□	67ohm ±25%	330mA	50Vdc	10M ohm	125Vdc	0.35ohm max.	-40 to +85°C
DLW21HN900SQ2□	90ohm ±25%	330mA	50Vdc	10M ohm	125Vdc	0.35ohm max.	-40 to +85°C
DLW21HN121SQ2	120ohm ±25%	280mA	50Vdc	10M ohm	125Vdc	0.45ohm max.	-40 to +85°C
DLW21HN181SQ2□	180ohm ±25%	250mA	50Vdc	10M ohm	125Vdc	0.50ohm max.	-40 to +85°C
DLW21HN670HQ2	67ohm ±25%	240mA	20Vdc	10M ohm	50Vdc	0.49ohm max.	-40 to +85°C
DLW21HN900HQ2	90ohm ±25%	220mA	20Vdc	10M ohm	50Vdc	0.59ohm max.	-40 to +85°C
DLW21HN121HQ2	120ohm ±25%	200mA	20Vdc	10M ohm	50Vdc	0.68ohm max.	-40 to +85°C

Number of Circuit: 1

■ ①Caution/Notice

Do not use products beyond the rated current and rated voltage as this may create excessive heat and deteriorate the insulation resistance.

Notice

Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.

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>>Murata(村田)