

## CHIP COIL (CHIP INDUCTORS) LQP03TN□□□□02D REFERENCE SPECIFICATION

## 1.Scope

This reference specification applies to LQP03TN\_02 series, Chip coil (Chip Inductors).

#### 2.Part Numbering

(ex)	LQ	<u> </u>	03	T	N	0N6	В	0	2	D
	Product ID	Structure	e Dimensio	on Applications	Category	Inductance	Tolerance	Features	Electrode	Packaging
			$(L \times W)$	and					1	D:Taping
				Characteristics	:					*B:Bulk

\*Bulk packing also available. (A product is put in the plastic bag under the taping conditions.)

#### 3.Rating

•Operating Temperature Range. −55°C to +125°C

(Ambient temperature: Rated current can be handled in this temperature range.)

•Storage Temperature Range. -55°C to +125°C

Customer Part Number			Inductance		DC Resistance	Self Resonant Frequency		Rated Current						
Fait Number	Fait Number	(nH)	Tolerance	(min)	(Ω max)	(MHz)		(mA)						
	LQP03TN0N6B02D					Min.	*Тур.							
	LQP03TN0N6C02D	0.6			0.07			850						
	LQP03TN0N7B02D		-			20000								
	LQP03TN0N7C02D	0.7												
	LQP03TN0N8B02D		-		0.08			800						
	LQP03TN0N8C02D	0.8												
	LQP03TN0N9B02D					18000								
	LQP03TN0N9C02D	0.9												
	LQP03TN1N0B02D		1				20000							
	LQP03TN1N0C02D	1.0												
	LQP03TN1N1B02D		1		0.10			750						
	LQP03TN1N1C02D	1.1				17000	17000	17000	47000	47000	47000	47000		
	LQP03TN1N2B02D		1		17000									
	LQP03TN1N2C02D	1.2												
	LQP03TN1N3B02D													
	LQP03TN1N3C02D	1.3												
	LQP03TN1N4B02D					10000	40000							
	LQP03TN1N4C02D	1.4				16000	19600							
	LQP03TN1N5B02D		B:±0.1nH	14		15000	47000							
	LQP03TN1N5C02D	1.5	C:±0.2nH				17900							
	LQP03TN1N6B02D						20000							
	LQP03TN1N6C02D	1.6					20000							
	LQP03TN1N7B02D				0.15									
	LQP03TN1N7C02D	1.7			1		19100	19100						
	LQP03TN1N8B02D													
	LQP03TN1N8C02D	1.8	]				17700							
	LQP03TN1N9B02D						15100							
	LQP03TN1N9C02D	1.9	]			12500	15100							
	LQP03TN2N0B02D	_				12300	14800							
	LQP03TN2N0C02D	2.0					17000							
	LQP03TN2N1B02D	QP03TN2N1C02D 2.1 QP03TN2N2B02D 2.2 QP03TN2N3B02D 2.3					13900							
	LQP03TN2N1C02D				11000	10000	]							
	LQP03TN2N2B02D				11000	13400								
	LQP03TN2N2C02D													
	LQP03TN2N3B02D						12900	500						
	LQP03TN2N3C02D				0.20	10000								
	LQP03TN2N4B02D			0.20	10000	12200	500							
	LQP03TN2N4C02D	2.4												

Customer Part Number	MURATA Part Number	Ind	luctance	Q (min)	DC Resistance	Self Resor Frequ	ency	Rated Current	
		(nH)	Tolerance	(111111)	(Ω max)	Min.	⊣z) *Typ.	(mA)	
	LQP03TN2N5B02D					IVIII I.			
	LQP03TN2N5C02D	2.5					12200		
	LQP03TN2N6B02D								
	LQP03TN2N6C02D	2.6				10000	13300		
	LQP03TN2N7B02D				0.00		40000		
	LQP03TN2N7C02D	2.7			0.20		13000	500	
	LQP03TN2N8B02D						11800		
	LQP03TN2N8C02D	2.8					11600		
	LQP03TN2N9B02D					9500	12400		
	LQP03TN2N9C02D	2.9				3300	12400		
	LQP03TN3N0B02D						11900		
	LQP03TN3N0C02D	3.0	_				11300		
	LQP03TN3N1B02D						11300		
	LQP03TN3N1C02D	3.1					11000		
	LQP03TN3N2B02D					8000	10600		
	LQP03TN3N2C02D	3.2			0.05	0000	10000	450	
	LQP03TN3N3B02D				0.25		10900	450	
	LQP03TN3N3C02D	3.3	B:±0.1nH				10000		
	LQP03TN3N4B02D	0.4	C:±0.2nH				9400		
	LQP03TN3N4C02D	3.4				7000	0 100		
	LQP03TN3N5B02D					7000	9600		
	LQP03TN3N5C02D	3.5					0000		
	LQP03TN3N6B02D	3.6					9500		
	LQP03TN3N6C02D						0000		
	LQP03TN3N7B02D	0.7				6000	8200	400	
	LQP03TN3N7C02D	3.7					0200		
	LQP03TN3N8B02D	0.0			0.30		8100		
	LQP03TN3N8C02D	3.8		14			0.00		
	LQP03TN3N9B02D	0.0				5700	7900		
	LQP03TN3N9C02D	3.9							
	LQP03TN4N0B02D	4.0					8600	350	
	LQP03TN4N0C02D	4.0							
	LQP03TN4N1B02D	4.1					8400		
	LQP03TN4N1C02D	4.1	1			5300			
	LQP03TN4N2B02D	4.2					8600		
	LQP03TN4N2C02D	4.2							
	LQP03TN4N3H02D	4.3			0.40		9800		
	LQP03TN4N3J02D	7.0	1		0.40			550	
	LQP03TN4N7H02D	4.7				4400	8800	-	
	LQP03TN4N7J02D LQP03TN5N1H02D		1						
	· ·	5.1				4200	8600		
	LQP03TN5N1J02D LQP03TN5N6H02D	0.1	1						
							8000		
	20.00110100022	H:±3%			4000				
	LQP03TN6N2H02D LQP03TN6N2J02D	6.2	⊓.±3% J:±5%				7900		
	LQP03TN6N8H02D		J.±J70						
	LQP03TN6N8J02D	J02D 6.8 H02D	1		0.60	3900	8000	300	
	LQP03TN6N6J02D		†		3.00			300	
	LQP03TN7N5J02D		1			3700	6700		
	LQP03TN8N2H02D		†						
		8.2	1			3600	6600	250	
	LQP03TN8N2J02D LQP03TN9N1H02D		1		0.70				
		9.1	1		5.75	3300	5900	200	
	LQP03TN9N1J02D 9.1								

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Customer Part Number	MURATA Part Number	Ind	uctance	Q (min)	DC Resistance	Self Resor Frequ	iency	Rated Current
		(nH)	Tolerance	(111111)	(Ω max)	Min.	Hz) *Typ.	(mA)
	LQP03TN10NH02D					IVIII I.	τyp.	
	LQP03TN10NJ02D	10			0.70	3200	5800	
	LQP03TN11NH02D			14				
	LQP03TN11NJ02D	11			0.80		5400	
	LQP03TN12NH02D				0.00	2900		
	LQP03TN12NJ02D	12			0.70			250
	LQP03TN13NH02D				0.7 0		4300	200
	LQP03TN13NJ02D	13			0.80			
	LQP03TN15NH02D				0.00	2600		
	LQP03TN15NJ02D	15			0.70		3800	
	LQP03TN16NH02D				0.7 0			
		16			0.95		3700	
	LQP03TN16NJ02D		4		0.00			200
	LQP03TN18NH02D	18		12	0.80		3400	200
	LQP03TN18NJ02D			12	0.00	2200		
	LQP03TN20NH02D LQP03TN20NJ02D	20			2.30		3600	150
					2.00			
	LQP03TN22NH02D LQP03TN22NJ02D	22			1.90		3300	
					1.50			
	LQP03TN24NH02D	24					3200	
	LQP03TN24NJ02D				2.30	2000		140
		LQP03TN27NH02D 27					2900	
	LQP03TN27NJ02D							
	LQP03TN30NH02D	30					2700	
	LQP03TN30NJ02D	- 33	4		2.95	1700		
	LQP03TN33NH02D						2600	
	LQP03TN33NJ02D		H:±3%					120
	LQP03TN36NH02D	36	J:±5%		3.00		2400	
	LQP03TN36NJ02D					1500		
	LQP03TN39NH02D	39						
	LQP03TN39NJ02D			9			2200	
	LQP03TN43NH02D	43		9	3.60	60 1300		
	LQP03TN43NJ02D							
	LQP03TN47NH02D	47						
	LQP03TN47NJ02D							
	LQP03TN51NH02D	51			0.00	1200		
	LQP03TN51NJ02D							
	LQP03TN56NH02D	56			3.90			
	LQP03TN56NJ02D							400
	LQP03TN62NH02D	62					1800	100
	LQP03TN62NJ02D				0	1100		
	LQP03TN68NH02D	68			8		1500	
	LQP03TN68NJ02D							
	LQP03TN75NJ02D	75						
	LQP03TN75NH02D	7.0				1000	1400	
	LQP03TN82NH02D	91 100				1000	1100	
	LQP03TN82NJ02D				4.0			
	LQP03TN91NH02D			8	10			
	LQP03TN91NJ02D					900	1300	
	LQP03TNR10H02D					500	1300	
LQF	LQP03TNR10J02D	100						
	LQP03TNR11H02D	110						80
	LQP03TNR11J02D	110				800	1100	
	LQP03TNR12H02D	120	1		12	000	1100	
	LQP03TNR12J02D	120					l	

Customer Part Number	MURATA Part Number	Inductance		Q (min)	DC Resistance	Self Resonant Frequency (MHz)		Rated Current (mA)
		(nH)	Tolerance	(111111)	(Ω max)	Min.	*Typ.	(IIIA)
	LQP03TNR13H02D	130			9		960	
	LQP03TNR13J02D	130				650	900	
	LQP03TNR15H02D	LQP03TNR15J02D 150 LQP03TNR16H02D 160 LQP03TNR16J02D 160				650	880	80
	LQP03TNR15J02D						000	
	LQP03TNR16H02D						840	
	LQP03TNR16J02D				600	0+0	l	
	LQP03TNR18H02D		H:±3%		11	000	790	70
	LQP03TNR18J02D	100					790	
	LQP03TNR20H02D	200	J:±5%	5			750	
	LQP03TNR20J02D	200				500	730	
	LQP03TNR22H02D	220			13	300	710	
	LQP03TNR22J02D	240					710	
	LQP03TNR24H02D						630	60
	LQP03TNR24J02D					15 450	030	
	LQP03TNR27H02D	270			15		580	
	LQP03TNR27J02D	270					560	

<sup>\*</sup> Typical value is actual performance.

## 4. Testing Conditions

《Unless otherwise specified》

Temperature : Ordinary Temperature / 15°C to 35°C

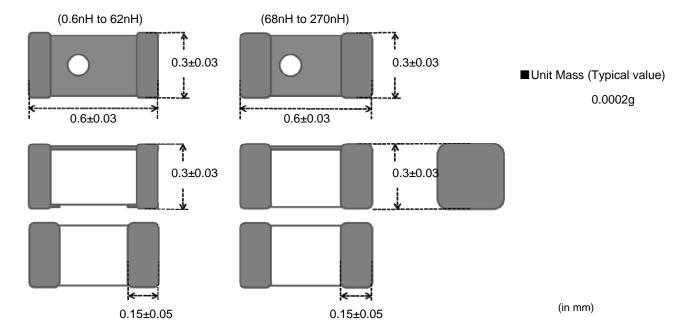
Humidity: Ordinary Humidity / 25%(RH) to 85 %(RH)

《In case of doubt》

Temperature : 20°C ± 2°C

Humidity : 60%(RH) to 70 %(RH)
Atmospheric Pressure : 86kPa to 106 kPa

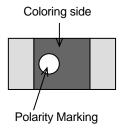
## 5. Appearance and Dimensions





## 6. Marking

Polarity Marking :white



## 7. Electrical Performance

No.	Item	Specification	Test Method
7.1	Inductance	Inductance shall meet item 3.	Measuring Equipment: KEYSIGHT E4991A or equivalent Measuring Frequency: (0.6nH~30nH) 500MHz (33nH~120nH) 300MHz (130nH~270nH) 100MHz Measuring Condition: Test signal level / about 0dBm Electrical length / 10mm Weight / about 1N to 5N Measuring Fixture: KEYSIGHT 16197A Position coil under test as shown in below and contact coil with each terminal by adding weight. Coloring side should be a topside, and should be in the direction of
7.2	Q	Q shall meet item 3.	the fixture for position of chip coil.  The polarity Marking  Measuring Method: See P.12 <electrical inductance="" measuring="" method="" of="" performance:="" q=""></electrical>
7.3	DC Resistance	DC Resistance shall meet item 3.	Measuring Equipment:Digital multi meter
7.4	Self Resonant Frequency(S.R.F)	S.R.F shall meet item 3.	Measuring Equipment: KEYSIGHT N5230A or equivalent
7.5	Rated Current	Self temperature rise shall be limited to 25°C max.	The rated current is applied.



## **8.Mechanical Performance**

No.	Item	Specification	Test Method
8.1	Shear Test	Chip coil shall not be damaged	Substrate:Glass-epoxy substrate
		after tested as test method.	Force:2N Hold Duration:5 s±1 s Applied Direction: Parallel to PCB. Chip coil
8.2	Bending Test	Chip coil shall not be damaged	Substrate:Glass-epoxy substrate
0.2	bending rest	after tested as test method.	(100mm × 40mm × 0.8mm)
		and tooled do tool motiled.	Speed of Applying Force:1mm /s
			Deflection:1mm
			Hold Duration:30 s
			Pressure jig
			R340 F Deflection
			45 45 Product (in mm)
8.3	Vibration	Appearance:No damage Inductance Change: within ±10%	Substrate:Glass-epoxy substrate Oscillation Frequency: 10Hz to 2000Hz to 10Hz for 20 min Total amplitude 1.5 mm or Acceleration amplitude 196 m/s² whichever is smaller. Testing Time: A period of 2h in each of 3 mutually perpendicular directions.
8.4	Solderability	The electrode shall be at least 90%	Flux: Ethanol solution of rosin 25(wt)%
	-	covered with new solder coating.	(Immersed for 5s to 10s)
			Solder:Sn-3.0Ag-0.5Cu
			Pre-Heating:150°C±10°C / 60s to 90s
			Solder Temperature:240°C±5°C
8.5	Resistance to	Appearance:No damage	Immersion Time:3s±1s Flux: Ethanol solution of rosin 25(wt)%
0.5	Soldering Heat	Inductance Change: within ±10%	(Immersed for 5s to 10s)
	2 5 Gotting Float		Solder:Sn-3.0Ag-0.5Cu
			Pre-Heating:150°C±10°C / 60s to 90s
			Solder Temperature:260°C±5°C
			Immersion Time:5s±1s
			Then measured after exposure in the room condition for 24h±2h.



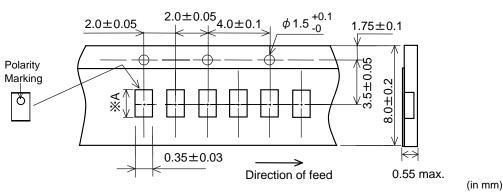
#### 9. Environmental Performance

It shall be soldered on the substrate.

No.	Item	Specification	Test Method
9.1	Heat Resistance	Appearance:No damage	Substrate:Glass-epoxy substrate
		Inductance Change: within ±10%	Temperature:125°C
			Time:1000h (+48h,-0h)
			Then measured after exposure in the
			room condition for 24h±2h.
9.2	Cold Resistance		Substrate:Glass-epoxy substrate
			Temperature:-55°C
			Time:1000 h (+48h,-0h)
			Then measured after exposure in the
			room condition for 24h±2h.
9.3	Humidity		Substrate:Glass-epoxy substrate
			Temperature:40°C±2°C
			Humidity:90%(RH) to 95%(RH)
			Time:1000 h(+48h,-0h)
			Then measured after exposure in the
			room condition for 24h±2h.
9.4	Temperature		Substrate:Glass-epoxy substrate
	Cycle		1 cycle:
			1 step: -55°C / 30min±3 min
			2 step:Ordinary temp. / 10~15 min
			3 step: 125°C / 30min±3 min
			4 step: Ordinary temp. / 10~15 min
			Total of 10 cycles
			Then measured after exposure in the
			room condition for 24h±2h.

## 10. Specification of Packaging

### 10.1 Appearance and Dimensions of paper tape (8mm-wide)



\*A 0N6~62N、R13~R27; 0.67±0.03 68N~R12; 0.65±0.03

## 10.2 Specification of Taping

- (1) Packing quantity (standard quantity)
  - 15,000 pcs. / reel
- (2) Packing Method

Products shall be packed in the cavity of the base tape and sealed by cover tape.

- (3) Sprocket hole
  - The sprocket holes are to the right as the tape is pulled toward the user.
- (4) Spliced point
  - Base tape and Cover tape has no spliced point.
- (5) Missing components number

Missing components number within 0.1 % of the number per reel or 1 pc., whichever is greater, and are not continuous. The Specified quantity per reel is kept.

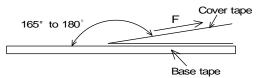


#### 10.3 Pull Strength

Cover tape	5N min
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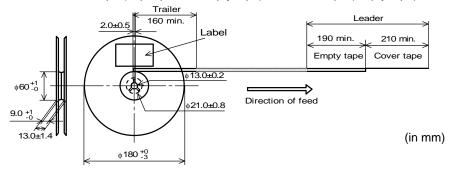
#### 10.4 Peeling off force of cover tape

Speed of Peeling off	300mm/min
Dealing off force	0.1N to 0.6N
Peeling off force	(minimum value is typical)



### 10.5 Dimensions of Leader-tape, Trailer and Reel

There shall be leader-tape (top tape and empty tape) and trailer-tape (empty tape) as follows.



#### 10.6 Marking for reel

Customer part number, MURATA part number, Inspection number(\*1) , RoHS Marking (\*2), Quantity etc  $\cdots$ 

\*1) < Expression of Inspection No.>

 $\frac{\square \square}{(1)} \quad \frac{OOOO}{(2)} \quad \frac{\times \times \times}{(3)}$ 

- (1) Factory Code
- (2) Date First digit : Year / Last digit of year

Second digit : Month / Jan. to Sep.  $\rightarrow$  1 to 9, Oct. to Dec.  $\rightarrow$  O,N,D

Third, Fourth digit: Day

- (3) Serial No.
- \*2) <Expression of RoHS Marking>

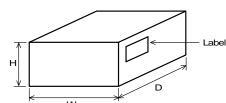
ROHS – 
$$\underline{Y}$$
 ( $\underline{\Delta}$ )

- (1) RoHS regulation conformity parts.
- (2) MURATA classification number

#### 10.7 Marking for Outside package (corrugated paper box)

Customer name, Purchasing order number, Customer part number, MURATA part number, RoHS Marking (\*2) ,Quantity, etc  $\cdots$ 

#### 10.8 Specification of Outer Case



Outer	Case Dim (mm)	ensions	Standard Reel Quantity in Outer Case (Reel)
W	D	Н	III Outer Case (Neer)
186	186	93	5

\* Above Outer Case size is typical. It depends on a quantity of an order.



## 11. / Caution

Limitation of Applications

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- (1) Aircraft equipment
- (2) Aerospace equipment
- (3) Undersea equipment
- (4) Power plant control equipment
- (5) Medical equipment
- (6) Transportation equipment (vehicles, trains, ships, etc.)
- (7) Traffic signal equipment
- (8) Disaster prevention / crime prevention equipment
- (9) Data-processing equipment
- (10) Applications of similar complexity and /or reliability requirements to the applications listed in the above

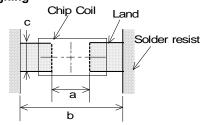
#### 12. Notice

Products can only be soldered with reflow.

This product is designed for solder mounting.

Please consult us in advance for applying other mounting method such as conductive adhesive.

#### 12.1 Land pattern designing



	•
0.2~0.3	
0.8~0.9	
0.2~0.3	
	(in mm)
	0.8~0.9

#### 12.2 Flux, Solder

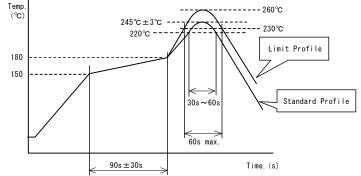
· Use rosin-based flux.

Don't use highly acidic flux with halide content exceeding 0.2(wt)% (chlorine conversion value). Don't use water-soluble flux.

- Use Sn-3.0Ag-0.5Cu solder.
- Standard thickness of solder paste :  $100 \,\mu$  m~ $150 \,\mu$  m.

#### 12.3 Reflow soldering conditions

- Pre-heating should be in such a way that the temperature difference between solder and product surface is limited to 150°C max. Cooling into solvent after soldering also should be in such a way that the temperature difference is limited to 100°C max.
   Insufficient pre-heating may cause cracks on the product, resulting in the deterioration of products quality.
- Standard soldering profile and the limit soldering profile is as follows.
   The excessive limit soldering conditions may cause leaching of the electrode and / or resulting in the deterioration of product quality.
- · Reflow soldering profile



	Standard Profile	Limit Profile
Pre-heating	150°C~180°C 、90s±30s	
Heating	above 220°C, 30s∼60s	above 230°C, 60s max.
Peak temperature	245°C±3°C	260°C,10s
Cycle of reflow	2 times	2 times



#### 12.4 Reworking with soldering iron

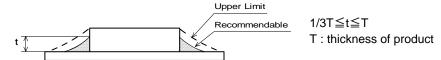
The following conditions must be strictly followed when using a soldering iron.

Pre-heating	150°C,1 min
Tip temperature	350°C max.
Soldering iron output	80W max.
Tip diameter	$\phi$ 3mm max.
Soldering time	3(+1,-0)s
Time	2 times

Note: Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the products due to the thermal shock.

#### 12.5 Solder Volume

· Solder shall be used not to be exceeded the upper limits as shown below.

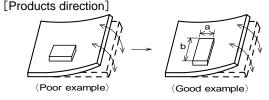


Accordingly increasing the solder volume, the mechanical stress to Chip is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.

#### 12.6 Attention regarding P.C.B. bending

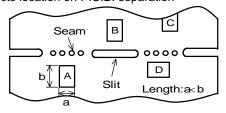
The following shall be considered when designing and laying out P.C.B.'s.

(1) P.C.B. shall be designed so that products are not subject to the mechanical stress due to warping the board.



Products shall be located in the sideways direction (Length:a < b) to the mechanical stress.

(2) Products location on P.C.B. separation



Products (A,B,C,D) shall be located carefully so that products are not subject to the mechanical stress due to warping the board. Because they may be subjected the mechanical stress in order of  $A>C>B \cong D$ .

#### 12.7 Cleaning Conditions

Products shall be cleaned on the following conditions.

- (1) Cleaning temperature shall be limited to 60°C max.(40°C max for IPA)
- (2) Ultrasonic cleaning shall comply with the following conditions with avoiding the resonance phenomenon at the mounted products and P.C.B.

Power: 20 W / I max. Frequency: 28kHz to 40kHz Time: 5 min max.

- (3) Cleaner
  - Alcohol type cleaner Isopropyl alcohol (IPA)
  - 2. Aqueous agent PINE ALPHA ST-100S
- (4) There shall be no residual flux and residual cleaner after cleaning. In the case of using aqueous agent, products shall be dried completely after rinse with de-ionized water in order to remove the cleaner.
- (5) Other cleaning Please contact us.



#### 12.8 Resin coating

When products are coated with resin, please contact us in advance.

#### 12.9 Handling of a substrate

(1)There is a possibility of chip cracking caused by PCBexpansion/contraction with heat, because stress on a chip is different depending on PCB material and structure.

When the thermal expansion coefficient greatly differs between the board used for mounting and the chip, it will cause cracking of the chip due to the thermal expansion and contraction.

The chip is assumed to be mounted on the PCB of glass-epoxy material, and we don't test with other PCB material which has different thermal expansion coefficient from Glass-epoxy.

When other PCB materials are considered, please be sure to evaluate by yourself.

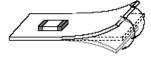
(2) After mounting products on a substrate, do not apply any stress to the product caused by bending or twisting to the substrate when cropping the substrate, inserting and removing a connector from the substrate or tightening screw to the substrate.

Excessive mechanical stress may cause cracking in the product.

In case of the mounting on flexible PCB, there is a possibility of chip cracking caused by mechanical stress even from small bending or twisting.

When the flexible PCB is considered, please be sure to evaluate by yourself.

Bending Twisting





#### 12.10 Storage and Handing Requirements

(1) Storage period

Use the products within 12 months after delivered. Solderability should be checked if this period is exceeded.

- (2) Storage conditions
  - Products should be stored in the warehouse on the following conditions.

Temperature : -10°C ~ 40°C

Humidity : 15% to 85% relative humidity No rapid change on temperature and humidity.

- Products should not be stored on bulk packaging condition to prevent the chipping of the core and the breaking of winding wire caused by the collision between the products.
- Products should be stored on the palette for the prevention of the influence from humidity, dust and so on.
- Products should be stored in the warehouse without heat shock, vibration, direct sunlight and so on.
- (3) Handling Condition

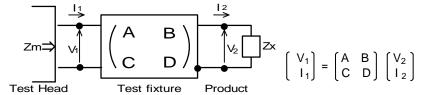
Care should be taken when transporting or handling product to avoid excessive vibration or mechanical shock.

## 13. 1 Note

- (1)Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- (2)You are requested not to use our product deviating from the reference specifications.
- (3)The contents of this reference specification are subject to change without advance notice. Please approve our product specifications or transact the approval sheet for product specifications before ordering.

## -<Electrical Performance:Measuring Method of Inductance/Q>

(1) Residual elements and stray elements of test fixture can be described by F-parameter shown in following.



(2) The impedance of chip coil Zx and measured value Zm can be described by input/output current/voltage.

$$Zm = \frac{V_1}{I_1}$$
 ,  $Zx = \frac{V_2}{I_2}$ 

(3) Thus, the relation between Zx and Zm is following;

$$Zx = \alpha \frac{Zm - \beta}{1 - Zm \Gamma}$$
 where,  $\alpha = D / A = 1$   
  $\beta = B / D = Zsm - (1 - Yom Zsm)Zss$   
  $\Gamma = C / A = Yom$ 

Zsm:measured impedance of short chip
Zss:residual impedance of short chip (0.480nH)
Yom:measured admittance when opening the fixture

(4) Lx and Qx shall be calculated with the following equation.

$$x = \frac{Im(Zx)}{2 \pi f}$$
,  $Qx = \frac{Im(Zx)}{Re(Zx)}$  Lx :Inductance of chip coil Qx:Q of chip coil f :Measuring frequency

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