



# NCE6005AS

## NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE6005AS uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub>=60V,I<sub>D</sub>=5A

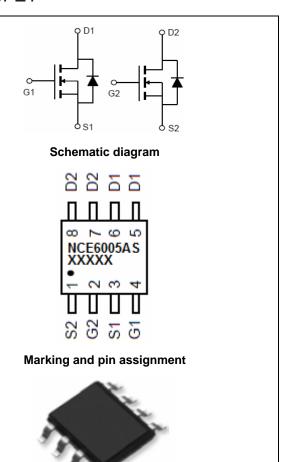
 $R_{DS(ON)}$  <35m $\Omega$  @  $V_{GS}$ =10V (Typ.26m $\Omega$ )

 $R_{DS(ON)}$  <45m $\Omega$  @  $V_{GS}$ =4.5V (Typ.32m $\Omega$ )

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



SOP-8 top view

#### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6005AS	NCE6005AS	SOP-8	Ø330mm	12mm	4000 units

### Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

<u> </u>			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	5	А
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	3.5	Α
Pulsed Drain Current	I <sub>DM</sub>	24	Α
Maximum Power Dissipation	P <sub>D</sub>	2	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	62.5	°C/W



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# Electrical Characteristics (T<sub>A</sub>=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)			•	•		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.2	1.6	2.5	V
Dunin Course On State Begintance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	-	mΩ	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	32	45	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =5A	11	-	-	S
Dynamic Characteristics (Note4)			•	•		
Input Capacitance	C <sub>lss</sub>	\\ -20\\\\ -0\\	-	979	-	PF
Output Capacitance	Coss		-	120	-	PF
Reverse Transfer Capacitance	$C_{rss}$	F=1.UMHZ	-	100	_	PF
Switching Characteristics (Note 4)			•	•		•
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=30V, V_{GS}=0V,$ $F=1.0MHz$ $V_{DD}=30V, R_{L}=6.7\Omega$ $V_{GS}=10V, R_{G}=3\Omega$	-	5.2	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	3	_	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =3 $\Omega$	-	17	_	nS
Turn-Off Fall Time	t <sub>f</sub>		-	2.5	-	nS
Total Gate Charge	Qg	V 20V/I 5A	-	22		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =30V,I <sub>D</sub> =5A,	-	3.3		nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	5.2		nC
Drain-Source Diode Characteristics			•			•
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =5A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	5	Α
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is neg	ligible (turi	n-on is do	ominated b	y LS+LD)

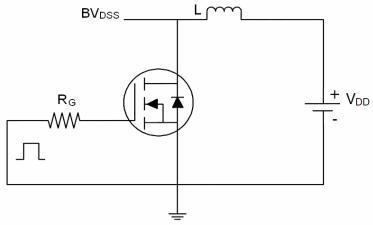
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition:Tj=25  $^{\circ}\text{C}$  ,VDD=30V,VG=10V,L=0.5mH,Rg=25 $\Omega$

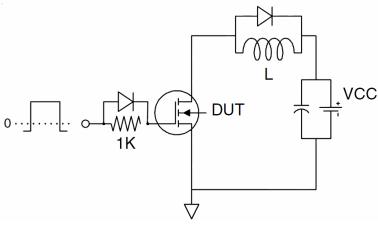


## **Test Circuit**

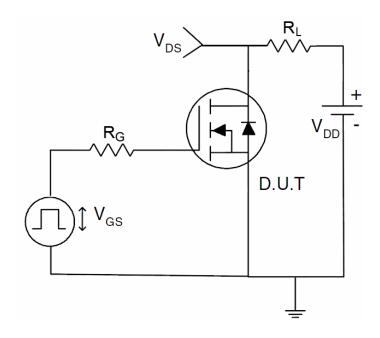
# 1) E<sub>AS</sub> test Circuit



## 2) Gate charge test Circuit



# 3) Switch Time Test Circuit

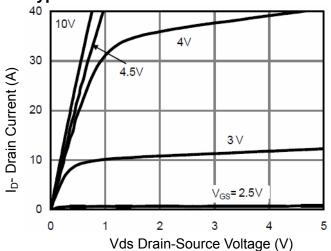


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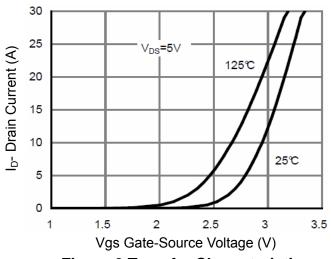


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# Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

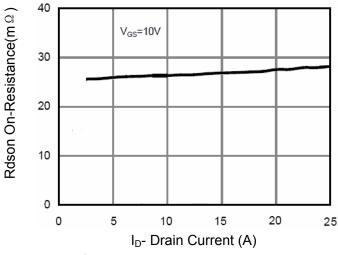
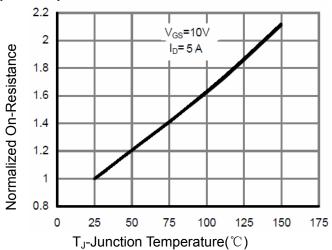


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 

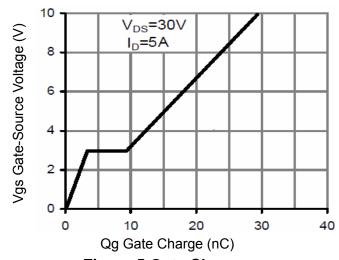


Figure 5 Gate Charge

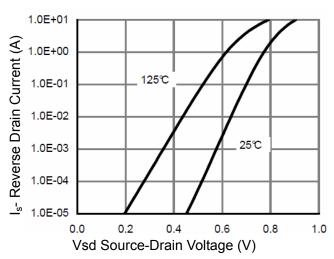


Figure 6 Source- Drain Diode Forward

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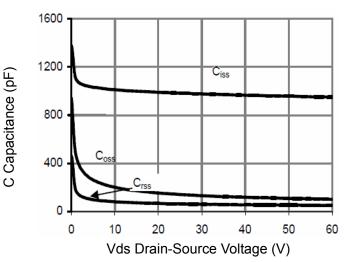


Figure 7 Capacitance vs Vds

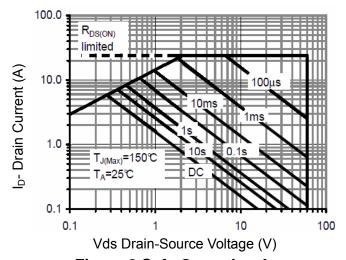


Figure 8 Safe Operation Area

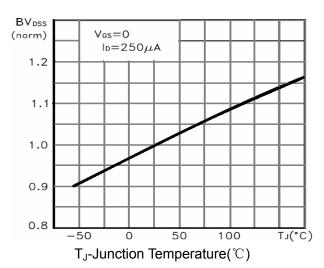


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

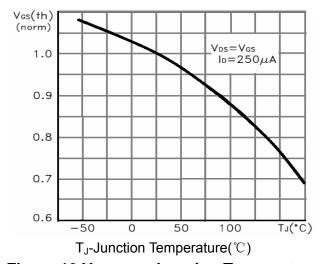


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

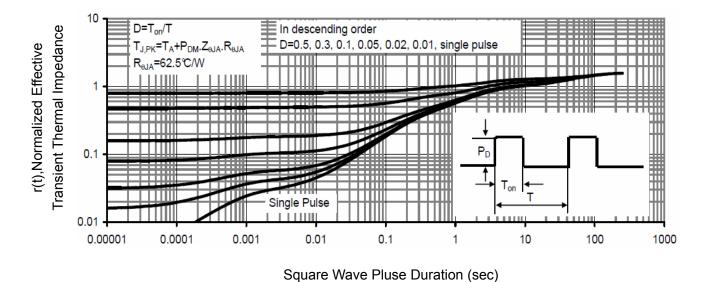
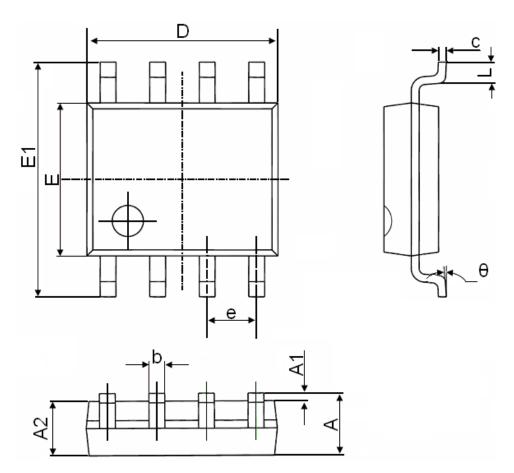


Figure 11 Normalized Maximum Transient Thermal Impedance

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# **SOP-8 Package Information**



Cumah al	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
Е	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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