

NCE N-Channel Super Trench II Power MOSFET

Description

The NCEP030N85GU uses Super Trench II technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS}(\text{ON})}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

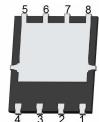
General Features

- V_{DS} =85V,I_D =140A $R_{DS(ON)}$ =2.65m Ω (typical) @ V_{GS} =10V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% AVds TESTED!

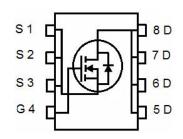
PDFN 5X6-8L





Top View

Bottom View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P030N85GU	NCEP030N85GU	PDFN5X6-8L	Ø330mm	12mm	5000units

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	85	V	
Gate-Source Voltage	V _G s	±20	V	
Drain Current-Continuous	I _D	140	Α	
Drain Current-Continuous(T _C =100 °C)	I _D (100°C)	105	Α	
Pulsed Drain Current	I _{DM}	560	А	
Maximum Power Dissipation	P _D	170	W	
Derating factor		1.36	W/℃	
Single pulse avalanche energy (Note 1)	E _{AS}	920	mJ	
Operating Junction and Storage Temperature Range	T_{J},T_{STG}	-55 To 150	°C	

Thermal Characteristic

Thermal Resistance,Junction-to-Case	R _{eJC}	0.74	°C/W

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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			<u> </u>			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	85	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =85V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =70A	-	2.65	3.05	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =70A		65	-	S
Dynamic Characteristics (Note2)	1		,			•
Input Capacitance	Clss	1/ 401/11 01/	-	5860	-	PF
Output Capacitance	Coss	V_{DS} =40V, V_{GS} =0V,	-	980	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	63	-	PF
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}		-	19	-	nS
Turn-on Rise Time	t _r	V_{DD} =40 V , I_D =70 A	-	12	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	44	-	nS
Turn-Off Fall Time	t _f		-	11	-	nS
Total Gate Charge	Qg		-	96	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=40V, I_{D}=70A,$	-	31	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	23	-	nC
Drain-Source Diode Characteristics			<u> </u>			
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =70A	-	-	1.2	V
Diode Forward Current	Is		-	-	140	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =70A	-	76	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/μs	-	130	-	nC

Notes:

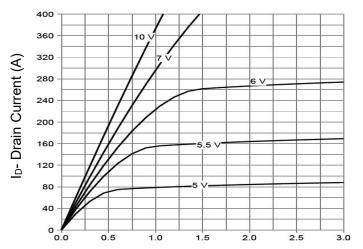
^{1.} EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=40V,VG=10V,L=0.5mH,Rg=25 Ω

^{2.} Guaranteed by design, not subject to production

^{3.} These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=150°C. The SOA curve provides a single pulse rating.

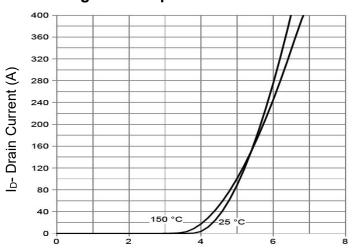


Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

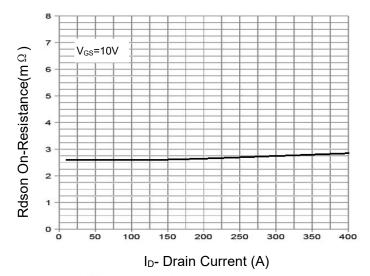
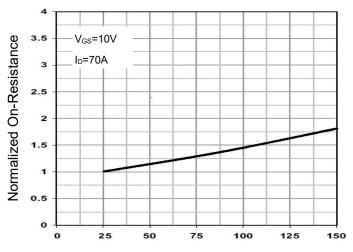
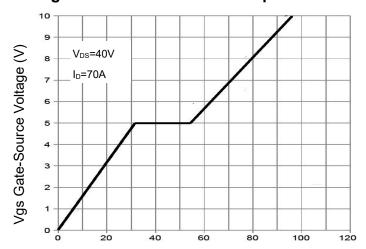


Figure 3 Rdson- Drain Current



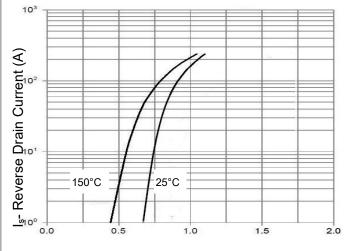
T_J-Junction Temperature(°C)

Figure 4 Rdson-Junction Temperature



Qg Gate Charge (nC)

Figure 5 Gate Charge

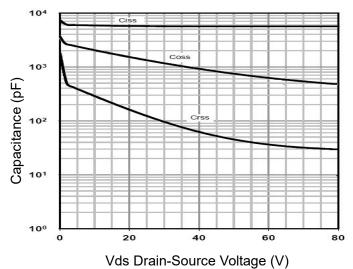


Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward

200



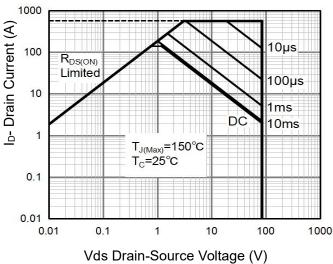


M) 160 Hower Dissipation 120 Hower Dissipati

Figure 7 Capacitance vs Vds

T_C-Case Temperature(°C)

Figure 9 Power De-rating



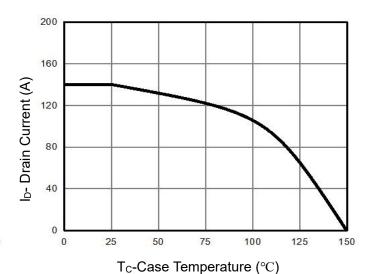


Figure 8 Safe Operation Area (Note3)

Figure 10 Current De-rating

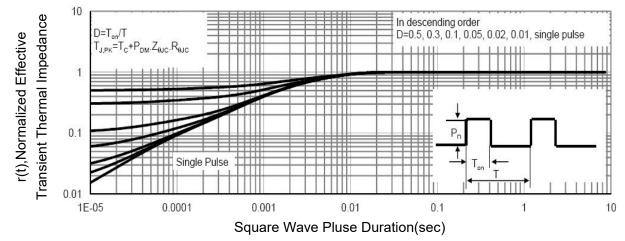
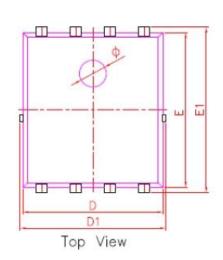
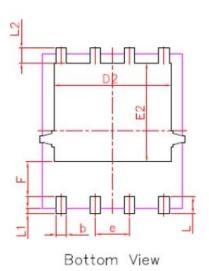


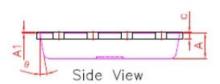
Figure 11 Normalized Maximum Transient Thermal Impedance



PDFN5X6-8L Package Information







DIM.	MIN.	NOM.	MAX.	
Α	0.90	0.95	1.00	
A1	0.00	0.02	0.05	
b	0.35	0.40	0.50	
С	0.20	0.25	0.30	
D	5.10	5.20	5.30	
D1	5.10	5.40	5.50	
D2	4.25	4.35	4.45	
е	1.27 BSC			
Е	5.70	5.75	5.80	
E1	6.00	6.15	6.30	
E2	3.57	3.67	3.77	
F	1.18	1.28	1.38	
L	0.55	0.65	0.75	
L1	0.15	0.20	0.25	
L2	0.45	0.55	0.65	
ø	0.90	1.00	1.10	
Θ	8.	10*	12*	

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NCEP030N85GU

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