

# NCE N-Channel Enhancement Mode Power MOSFET

# **Description**

The NCE6003M uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other switching application.

#### **General Feature**

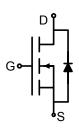
V<sub>DS</sub> =60V,I<sub>D</sub> =3.0A

$$\begin{split} R_{DS(ON)} < &100 m\Omega \text{ @ } V_{GS} = &10V \\ R_{DS(ON)} < &120 m\Omega \text{ @ } V_{GS} = &4.5V \end{split}$$

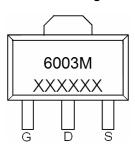
- High Power and current handing capability
- Lead free product is acquired
- Surface mount package

# **Application**

- Battery switch
- ●DC/DC converter



Schematic diagram



SOT-89 -3L top view

# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
6003M	NCE6003M	SOT-89-3L	Ø180mm	12mm	1000units

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

The contact manner than go (12 to a more care and the care)					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	60	V		
Gate-Source Voltage	V <sub>G</sub> s	±20	V		
Drain Current-Continuous	I <sub>D</sub>	3	Α		
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	10	Α		
Maximum Power Dissipation	P <sub>D</sub>	1.7	W		
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	16.2	mJ		
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$		

# **Thermal Characteristic**

Thermal Resistance,Junction-to-Case (Note 2)	$R_{ heta JC}$	10	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	73.5	°C/W

# **Electrical Characteristics (T<sub>A</sub>=25**°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						



# http://www.ncepower.com

# NCE6003M

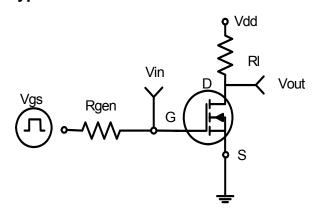
Drain-Source Breakdown Voltage	rce Breakdown Voltage BV <sub>DSS</sub> V <sub>GS</sub> =0V I <sub>D</sub> =250		60	65	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	1		II.	<u>I</u>		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.3	2.0	V
Duit Ou and Ou Old Duit In		V <sub>GS</sub> =10V, I <sub>D</sub> =3A	-	73	100	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	90	120	mΩ
Forward Transconductance	<b>g</b> Fs	$V_{DS}$ =5 $V$ , $I_{D}$ =3 $A$	4	-	-	S
Dynamic Characteristics (Note4)			'			
Input Capacitance	C <sub>lss</sub>	V 20V/V 0V	-	510	-	PF
Output Capacitance	$V_{DS}$ =30V, $V_{GS}$ =0V,		-	34	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	26	-	PF
Switching Characteristics (Note 4)	1		II.	<u>I</u>		
Turn-on Delay Time	t <sub>d(on)</sub>		-	6	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =30 $V$ , $I_{D}$ =3 $A$	-	15	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =10V, $R_{GEN}$ =1 $\Omega$	-	15	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Qg	\/ 00\/ L 0A	-	14.6	-	nC
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =30V,I <sub>D</sub> =3A,		1.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub> V <sub>GS</sub> =10V		-	3	-	nC
Drain-Source Diode Characteristics	·					
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	$V_{GS}$ =0 $V$ , $I_{S}$ =3 $A$	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	3	Α

### Notes:

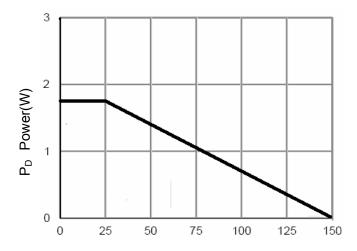
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- **4.** Guaranteed by design, not subject to production
- 5.EAS condition : Tj=25  $^{\circ}\text{C}$  ,VDD=-30V,VG=-10V,L=0.5mH,Rg=25 $\Omega$



# **Typical Electrical and Thermal Characteristics**



**Figure 1 Switching Test Circuit** 



 $T_J$ -Junction Temperature(°C) Figure 3 Power Dissipation

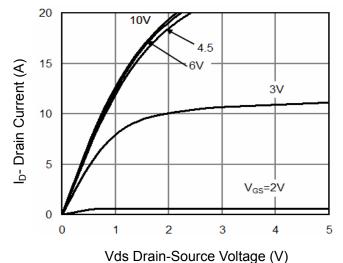
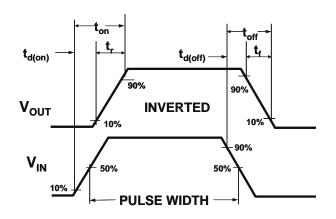
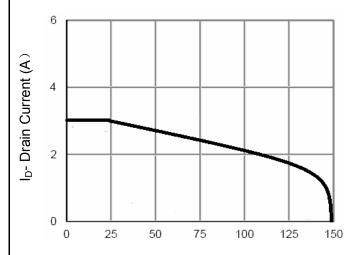


Figure 5 Output Characteristics



**Figure 2 Switching Waveforms** 



T<sub>J</sub>-Junction Temperature(°C)

Figure 4 Drain Current

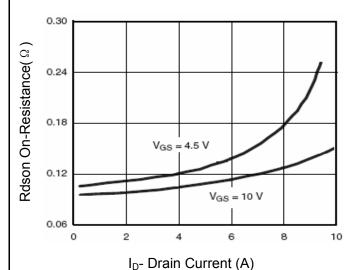
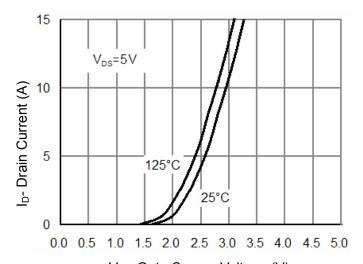
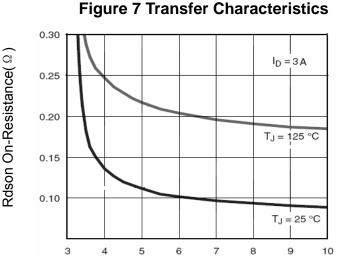


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)



Vgs Gate-Source Voltage (V)

# Figure 9 Rdson vs Vgs

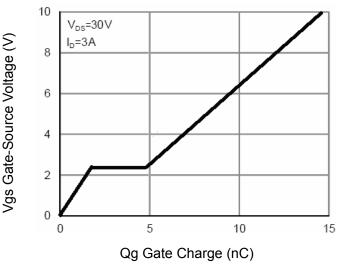


Figure 11 Gate Charge

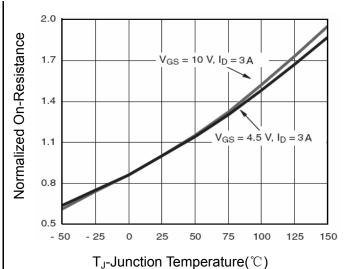
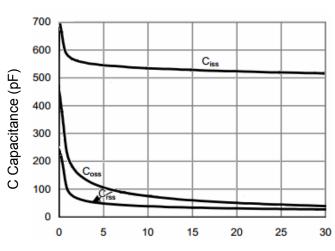


Figure 8 Drain-Source On-Resistance



Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds

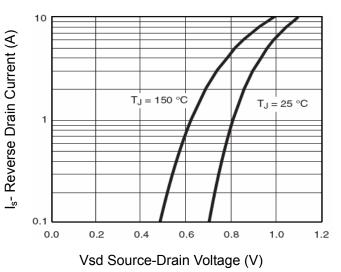
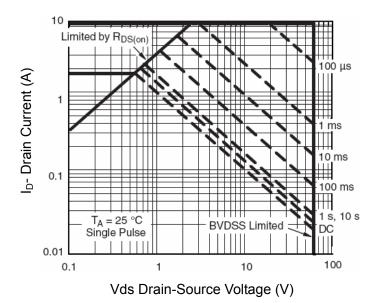
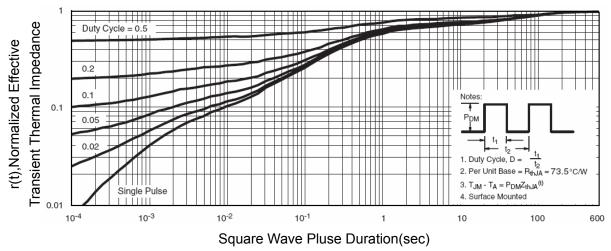


Figure 12 Source- Drain Diode Forward





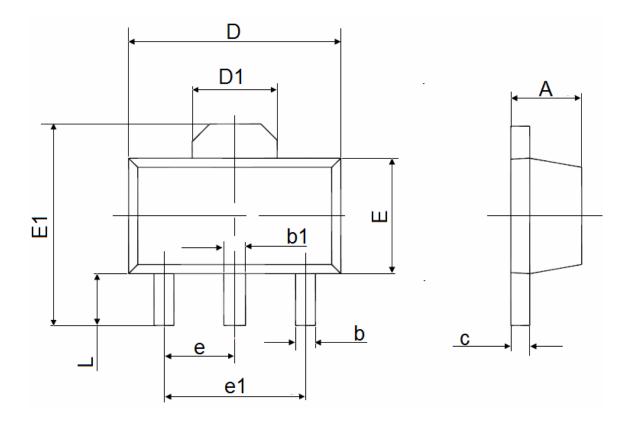
**Figure 13 Safe Operation Area** 



**Figure 14 Normalized Maximum Transient Thermal Impedance** 



# **SOT-89-3L Package Information**



Cymbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.020	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	D1 1.550 REF.		0.061 REF.		
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP.		0.060 TYP.		
e1	3.000 TYP.		0.118 TYP.		
L	0.900	1.200	0.035	0.047	

### **Notes**

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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