

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE01P30D uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. It is ESD protested.

General Features

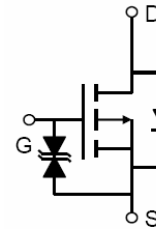
- $V_{DS} = -100V, I_D = -30A$
 $R_{DS(ON)} < 58m\Omega @ V_{GS} = -10V$ (Typ:44m Ω)
 $R_{DS(ON)} < 65m\Omega @ V_{GS} = -4.5V$ (Typ:48m Ω)
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low On-Resistance

Application

- Portable equipment and battery powered systems

100% UIS TESTED!

100% ΔV_{ds} TESTED!



Schematic diagram



Marking and pin assignment



TO-263-2L top view

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|-----------|----------------|-----------|------------|----------|
| NCE01P30D | NCE01P30D | TO-263-2L | - | - | - |

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|---|--------------------|------------|---------------|
| Drain-Source Voltage | V_{DS} | -100 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous | I_D | -30 | A |
| Drain Current-Continuous($T_C = 100^\circ C$) | $I_D(100^\circ C)$ | -21 | A |
| Pulsed Drain Current | I_{DM} | -120 | A |
| Maximum Power Dissipation | P_D | 120 | W |
| Single pulse avalanche energy ^(Note 5) | E_{AS} | 420 | mJ |
| Derating factor | | 0.8 | W/ $^\circ C$ |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 175 | $^\circ C$ |

Thermal Characteristic

| | | | |
|--|-----------------|------|--------------|
| Thermal Resistance, Junction-to-Case ^(Note 2) | $R_{\theta Jc}$ | 1.25 | $^\circ C/W$ |
|--|-----------------|------|--------------|

Electrical Characteristics (T_C=25°C unless otherwise noted)

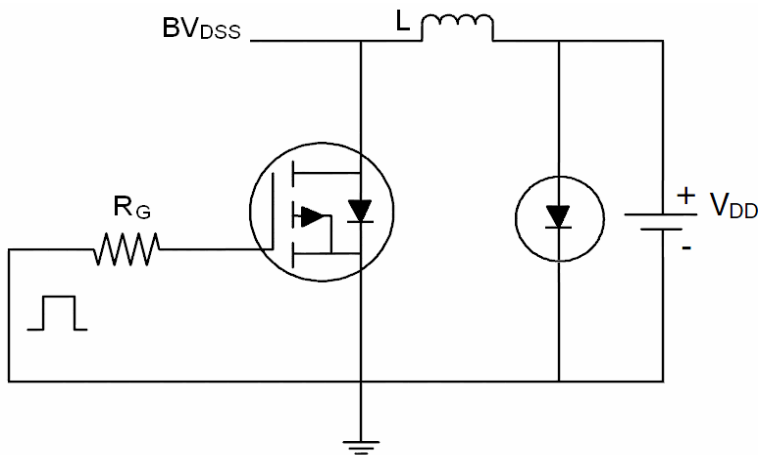
| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|---------------------|--|------|-------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =-250μA | -100 | - | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =-100V, V _{GS} =0V | - | - | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±10 | μA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =-250μA | -1.5 | -1.9 | -2.5 | V |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =-10V, I _D =-15A | - | 44 | 58 | mΩ |
| | | V _{GS} =-4.5V, I _D =-15A | - | 48 | 65 | |
| Forward Transconductance | g _{FS} | V _{DS} =-50V, I _D =-10A | 5 | - | - | S |
| Dynamic Characteristics (Note 4) | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =-50V, V _{GS} =0V, F=1.0MHz | - | 8049 | - | PF |
| Output Capacitance | C _{oss} | | - | 184.5 | - | PF |
| Reverse Transfer Capacitance | C _{rss} | | - | 179 | - | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =-50V, I _D =-15A V _{GS} =-10V, R _{GEN} =9.1Ω | - | 17 | - | nS |
| Turn-on Rise Time | t _r | | - | 80 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | | - | 45 | - | nS |
| Turn-Off Fall Time | t _f | | - | 65 | - | nS |
| Total Gate Charge | Q _g | V _{DS} =-50V, I _D =-15A, V _{GS} =-10V | - | 120 | - | nC |
| Gate-Source Charge | Q _{gs} | | - | 22 | - | nC |
| Gate-Drain Charge | Q _{gd} | | - | 26.4 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | V _{SD} | V _{GS} =0V, I _S =-10A | - | - | -1.2 | V |
| Diode Forward Current (Note 2) | I _S | - | - | - | -30 | A |
| Reverse Recovery Time | t _{rr} | T _J = 25°C, I _F = -15A di/dt = 100A/μs (Note 3) | - | 90 | - | nS |
| Reverse Recovery Charge | Q _{rr} | | - | 150 | - | nC |
| Forward Turn-On Time | t _{on} | Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD) | | | | |

Notes:

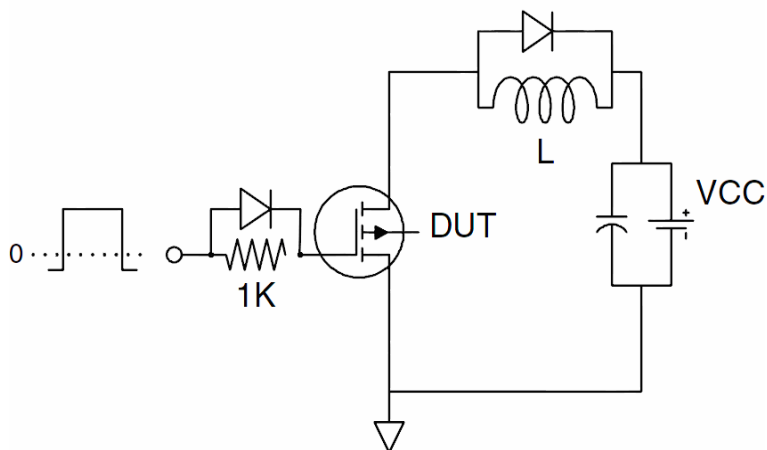
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition: T_J=25°C, V_{DD}=-50V, V_G=-10V, L=0.5mH, R_g=25Ω

Test Circuit

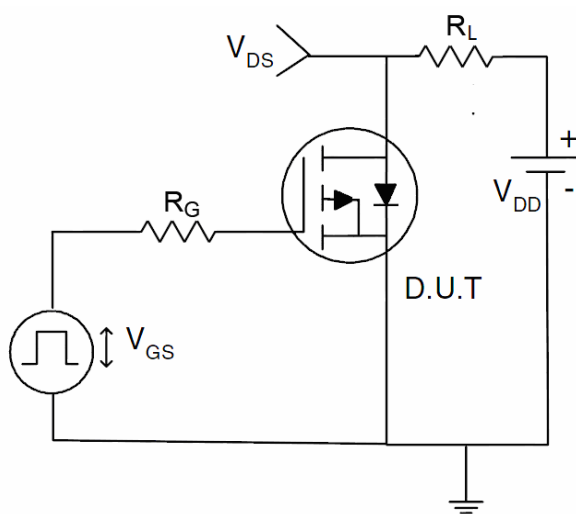
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

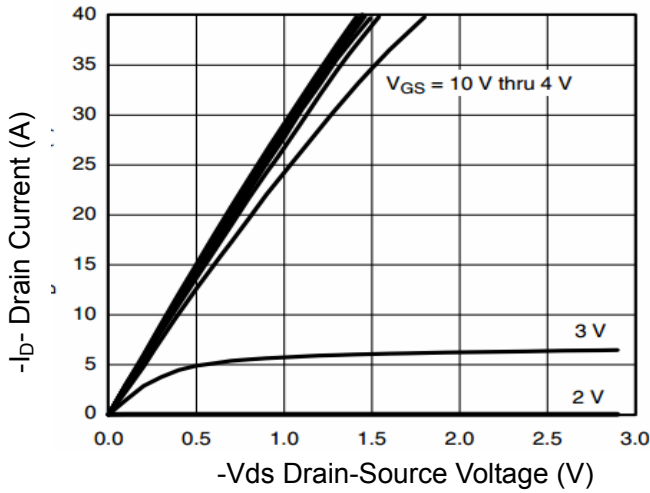


Figure 1 Output Characteristics

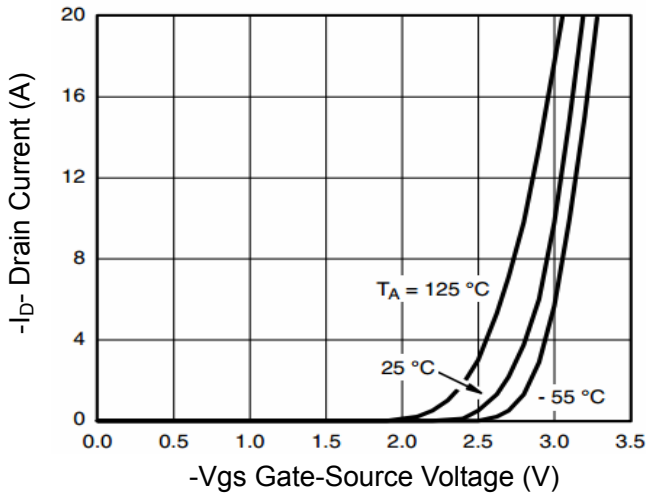


Figure 2 Transfer Characteristics

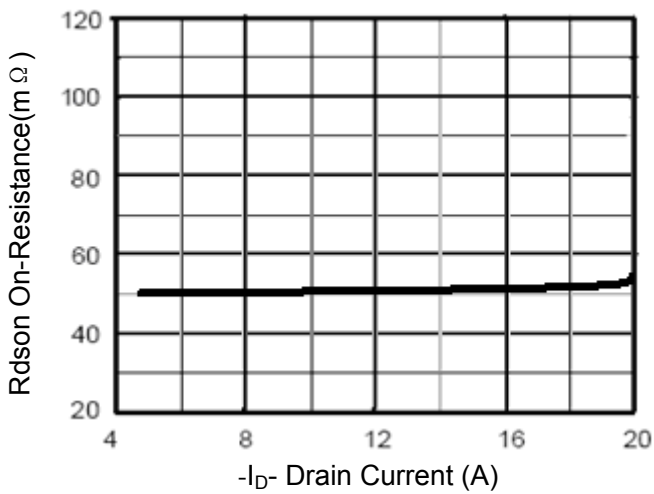


Figure 3 Rdson- Drain Current

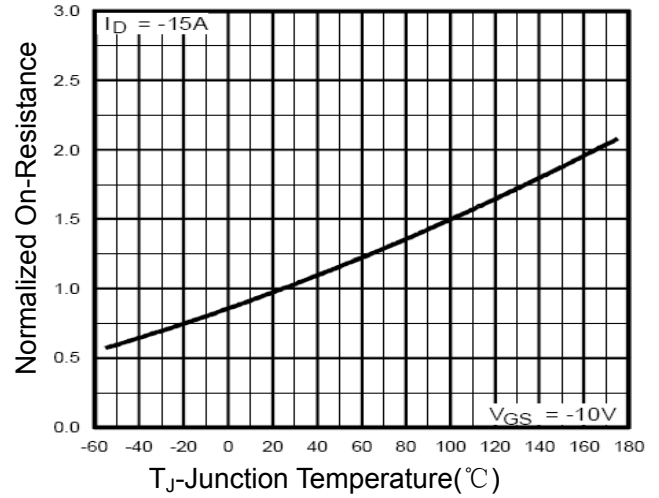


Figure 4 Rdson-Junction Temperature

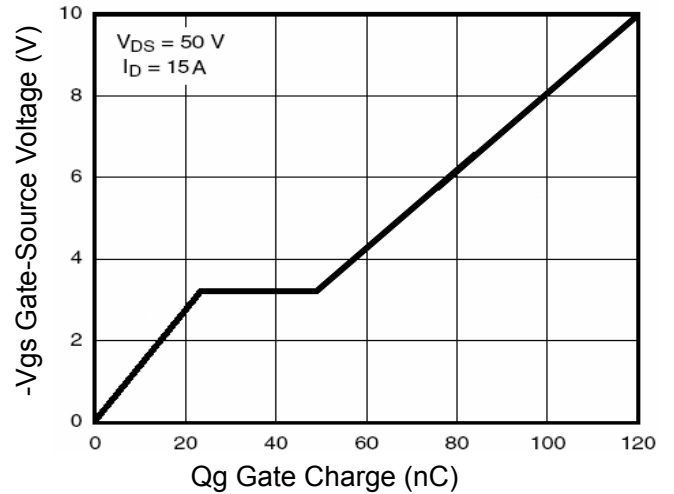


Figure 5 Gate Charge

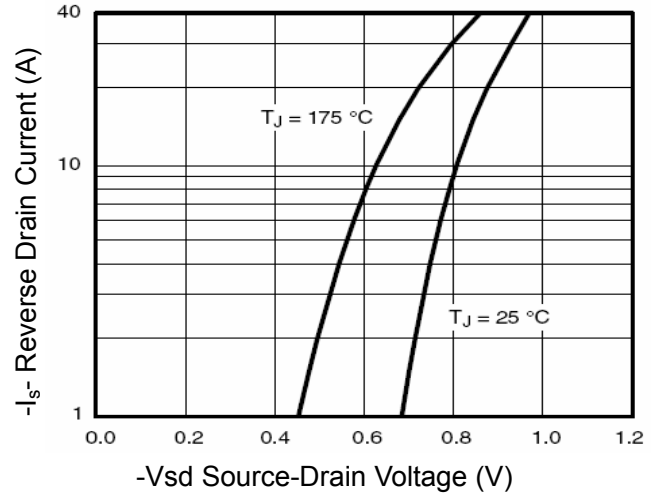


Figure 6 Source- Drain Diode Forward

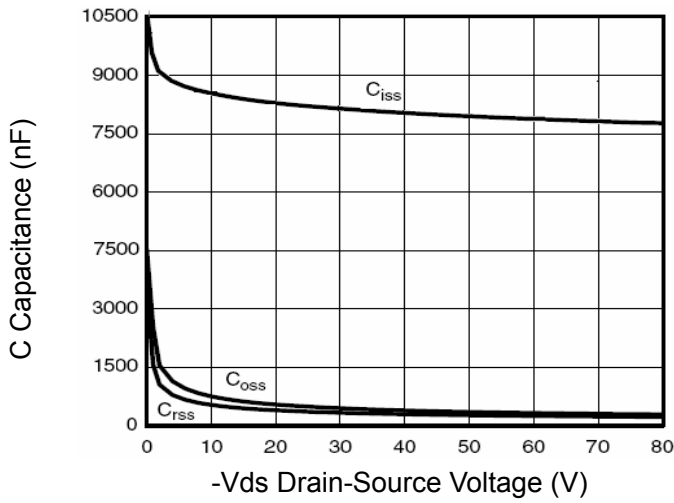


Figure 7 Capacitance vs Vds

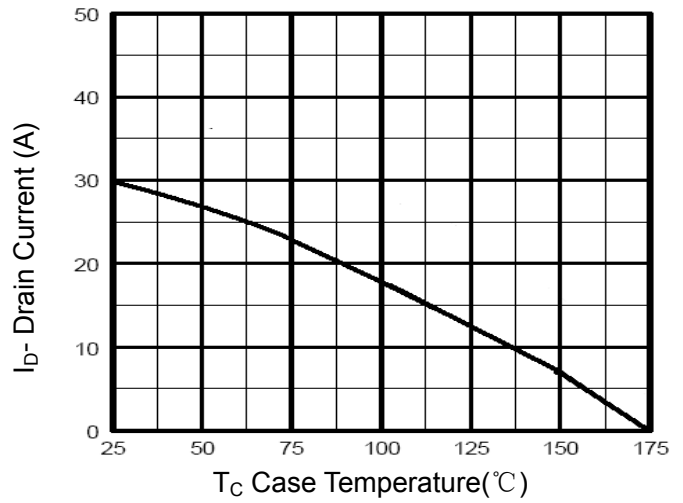


Figure 9 Drain Current vs Case Temperature

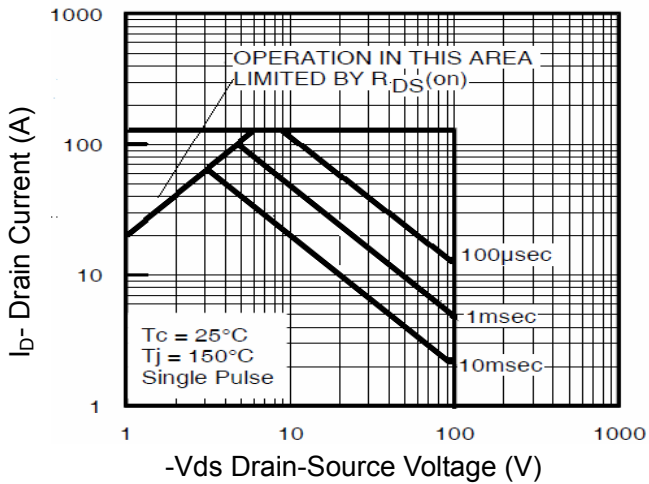


Figure 8 Safe Operation Area

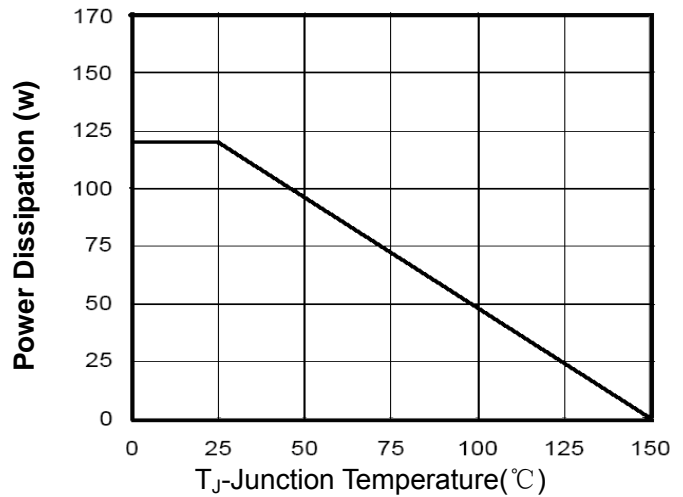


Figure 10 Power De-rating

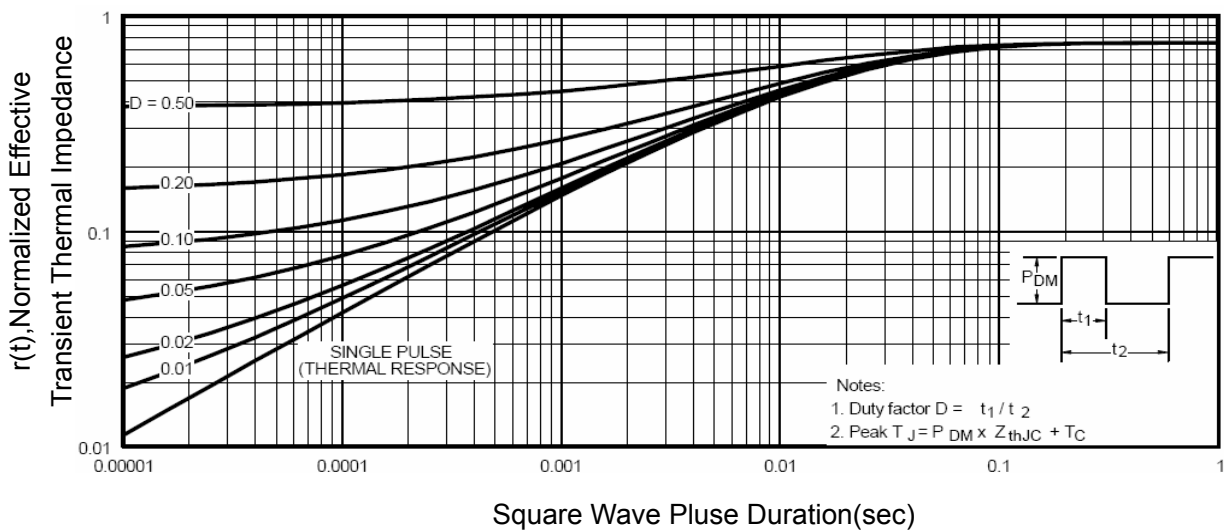
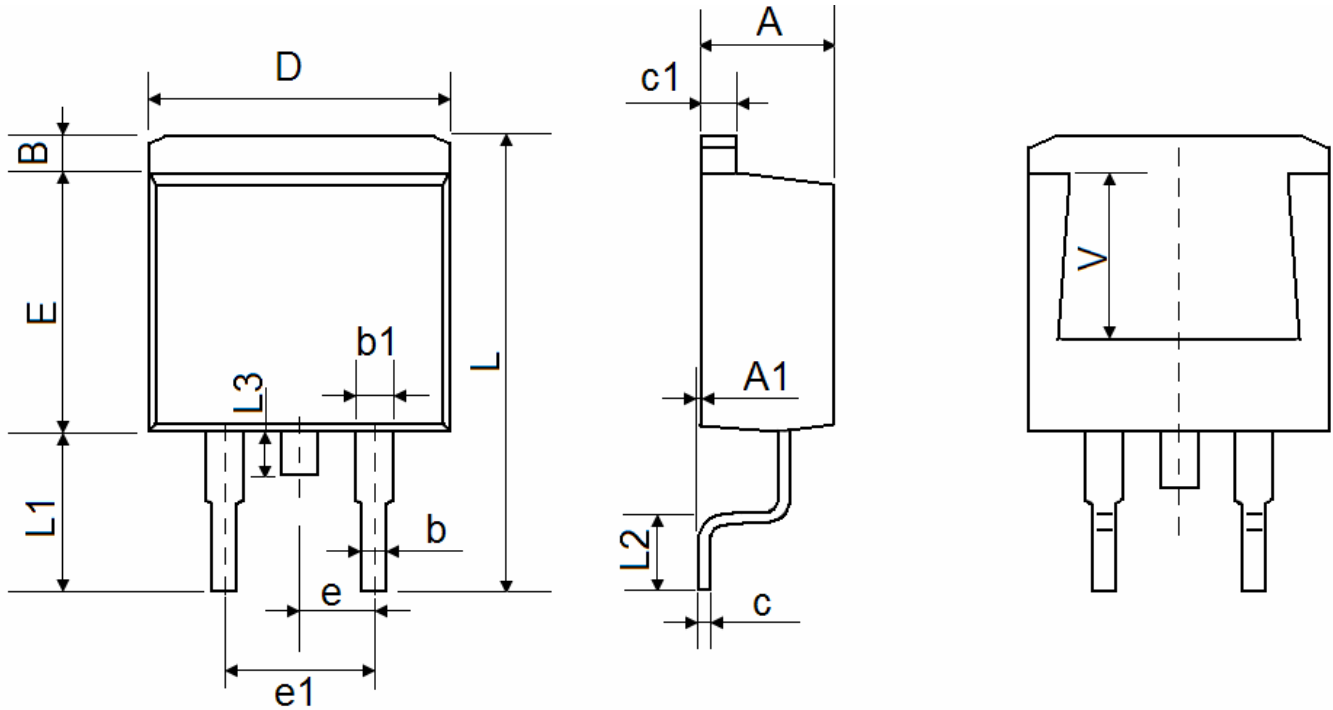


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-263-2L Package Information



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 4.470 | 4.670 | 0.176 | 0.184 |
| A1 | 0.000 | 0.150 | 0.000 | 0.006 |
| B | 1.170 | 1.370 | 0.046 | 0.054 |
| b | 0.710 | 0.910 | 0.028 | 0.036 |
| b1 | 1.170 | 1.370 | 0.046 | 0.054 |
| c | 0.310 | 0.530 | 0.012 | 0.021 |
| c1 | 1.170 | 1.370 | 0.046 | 0.054 |
| D | 10.010 | 10.310 | 0.394 | 0.406 |
| E | 8.500 | 8.900 | 0.335 | 0.350 |
| e | 2.540 TYP. | | 0.100 TYP. | |
| e1 | 4.980 | 5.180 | 0.196 | 0.204 |
| L | 15.050 | 15.450 | 0.593 | 0.608 |
| L1 | 5.080 | 5.480 | 0.200 | 0.216 |
| L2 | 2.340 | 2.740 | 0.092 | 0.108 |
| L3 | 1.300 | 1.700 | 0.051 | 0.067 |
| V | 5.600 REF | | 0.220 REF | |

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