





## NCE P-Channel Enhancement Mode Power MOSFET

## **Description**

The NCE55P15K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

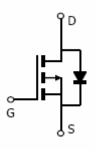
- $V_{DS}$  =-55V, $I_{D}$  =-15A  $R_{DS(ON)}$  <75m $\Omega$  @  $V_{GS}$ =-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

### **Application**

- Power switching application
- Hard switched and high frequency circuits
- DC-DC Converter

100% UIS TESTED!

100% ΔVds TESTED!



#### Schematic diagram



Marking and pin assignment



TO-252-2L top view

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE55P15K	NCE55P15K	TO-252-2L	-	-	-

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-55	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	-15	А
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	-10	Α
Pulsed Drain Current	I <sub>DM</sub>	-50	Α
Maximum Power Dissipation	P <sub>D</sub>	35	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 175	$^{\circ}\!\mathbb{C}$

#### **Thermal Characteristic**

Thermal Resistance ,Junction-to-Case <sup>(Note 2)</sup>	$R_{ heta JC}$	4.3	°C/W
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# NCE55P15K

## Electrical Characteristics (T<sub>C</sub>=25 <sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-55V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-1.5	-2.6	-3.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	-	60	75	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-15V,I <sub>D</sub> =-5A	16	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>Iss</sub>	V 00V/V 0V	-	1450	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V,	-	145	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	110	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-30V, , $R_L$ =30 $\Omega$	-	9	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =6 $\Omega$	-	65	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	30	-	nS
Total Gate Charge	Qg	\/ 20\/ L 5A	-	26	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-30V, $I_{D}$ =-5A, $V_{GS}$ =-10V	-	4.5	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =-10V	-	7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =-15A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	-15	Α

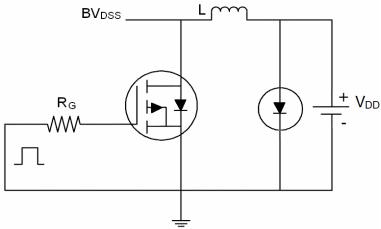
#### Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

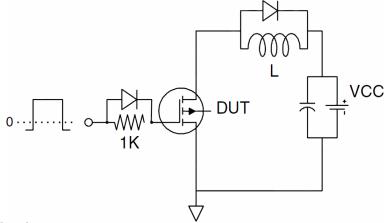


## **Test Circuit**

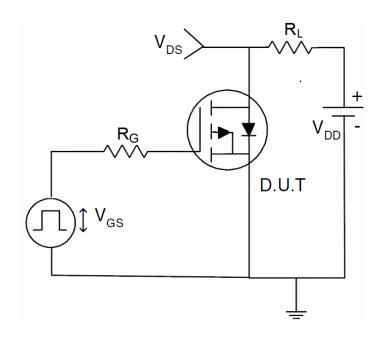
## 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit

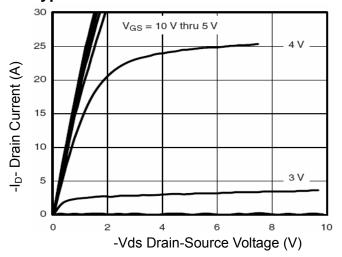


## 3) Switch Time Test Circuit

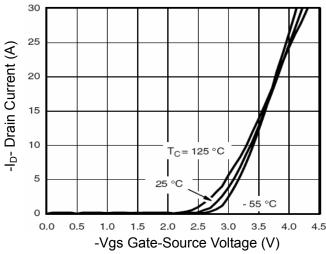




## Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

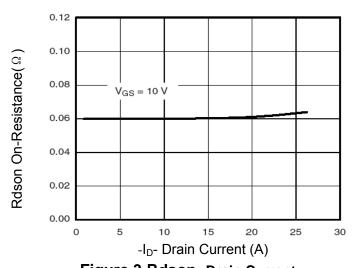
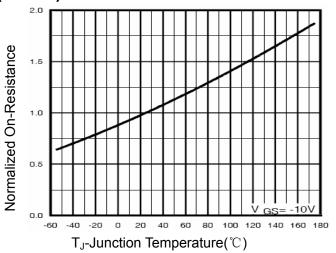


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 

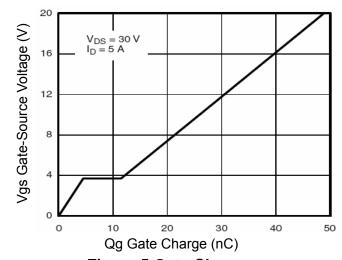


Figure 5 Gate Charge

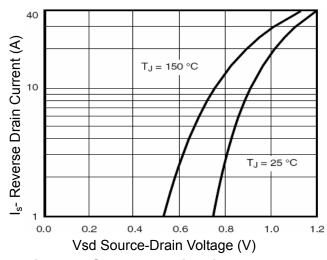


Figure 6 Source- Drain Diode Forward



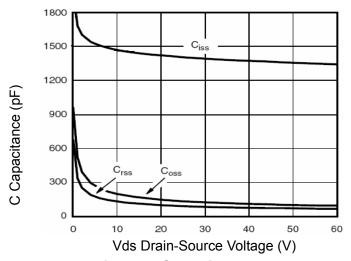


Figure 7 Capacitance vs Vds

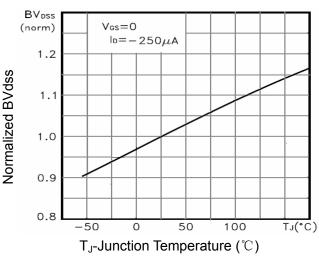
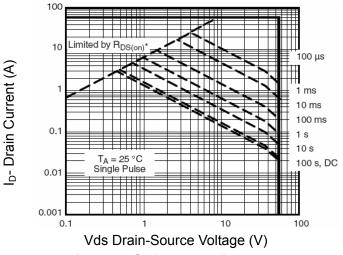


Figure 9 BV<sub>DSS</sub> vs Junction Temperature



**Figure 8 Safe Operation Area** 

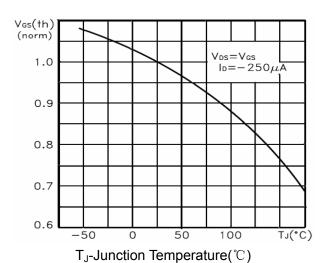


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

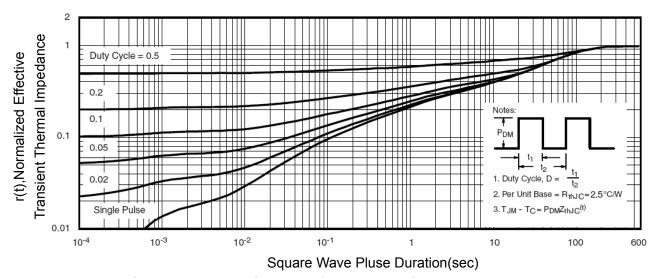


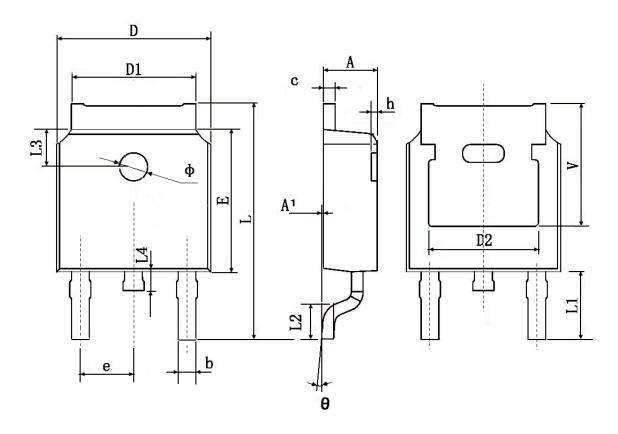
Figure 11 Normalized Maximum Transient Thermal Impedance

**Pb Free Product** 



# NCE55P15K

# **TO-252 Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.483	TYP.	0.190	TYP.		
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900	TYP.	0.114	TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600	1.600 TYP.		TYP.		
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350	TYP.	0.211	0.211 TYP.		



## http://www.ncepower.com

# NCE55P15K

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