

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE01P30 uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. It can be used in a wide variety of applications. It is ESD protested.

General Features

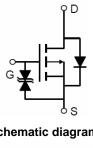
- V_{DS} =-100V,I_D =-30A $R_{DS(ON)}$ <58m Ω @ V_{GS} =-10V (Typ:50m Ω)
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low On-Resistance

Application

Portable equipment and battery powered systems

100% UIS TESTED!

100% AVds TESTED!



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE01P30	NCE01P30	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	-30	Α
Drain Current-Continuous(T _C =100°C)	I _D (100°C)	-21	Α
Pulsed Drain Current	I _{DM}	-120	Α
Single pulse avalanche energy (Note 5)	E _{AS}	360	mJ
Maximum Power Dissipation	P _D	120	W
Derating factor		0.8	W/℃
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$ C



Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	R _{eJc}	1.25	°C/W	Ì
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Electrical Characteristics (T_C=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	•					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	· · · · · · · · · · · · · · · · · · ·		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-100V,V _{GS} =0V	V,V _{GS} =0V -		1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±10	μA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.5	-1.9	-2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-15A	-	50	58	mΩ
Forward Transconductance	g FS	V _{DS} =-50V,I _D =-10A	5	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}	\/ 50\/\/ 0\/	-	8049	-	PF
Output Capacitance	Coss	V _{DS} =-50V,V _{GS} =0V,	-	184.5	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	179	-	PF
Switching Characteristics (Note 4)	1		•			
Turn-on Delay Time	t _{d(on)}		-	17	-	nS
Turn-on Rise Time	t _r	V _{DD} =-50V,I _D =-15A	-	80	-	nS
Turn-Off Delay Time	$t_{d(off)}$	V_{GS} =-10V, R_{GEN} =9.1 Ω	-	45	-	nS
Turn-Off Fall Time	t _f		-	65	-	nS
Total Gate Charge	Qg	\/ - 50\/ - 454	-	120	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-50V, I_{D} =-15A, V_{GS} =-10V	-	22	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =-10V	-	26.4	-	nC
Drain-Source Diode Characteristics	1		•			•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-10A	-	-	-1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	-30	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =-15A	-	90	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	150	-	nC
	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD				•

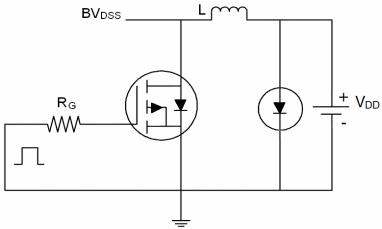
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production
- 5. EAS condition: Tj=25 $^{\circ}\text{C}\,\text{,V}_{DD}\text{=-}50\text{V},\text{V}_{G}\text{=-}10\text{V},\text{L=}0.5\text{mH},\text{Rg=}25\Omega$

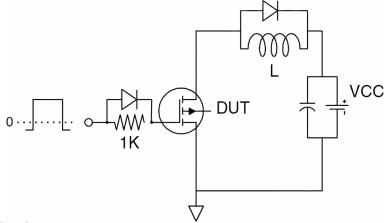


Test Circuit

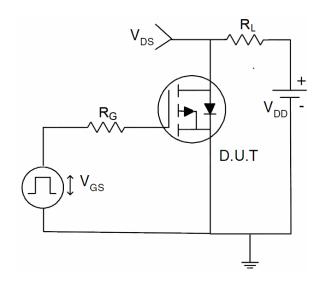
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

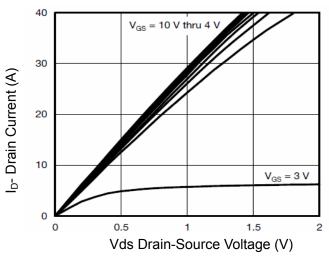


Figure 1 Output Characteristics

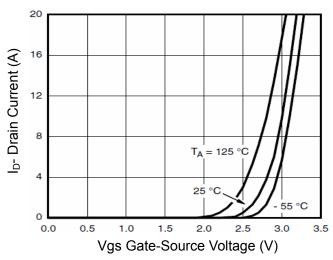


Figure 2 Transfer Characteristics

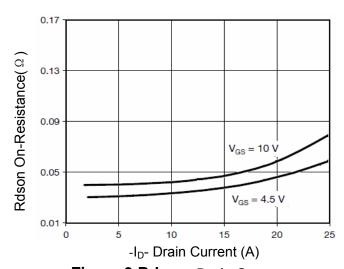


Figure 3 Rdson- Drain Current

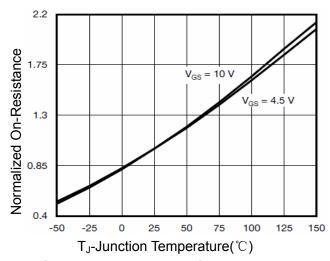


Figure 4 Rdson-JunctionTemperature

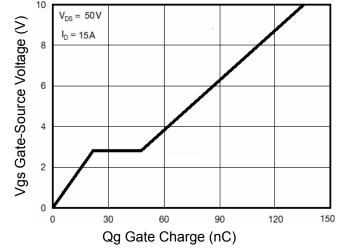


Figure 5 Gate Charge

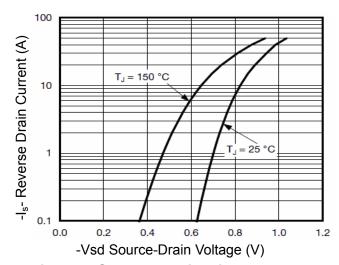
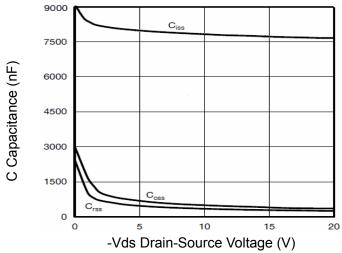
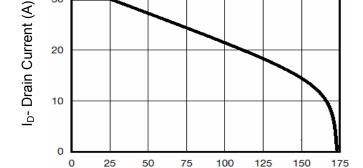


Figure 6 Source- Drain Diode Forward





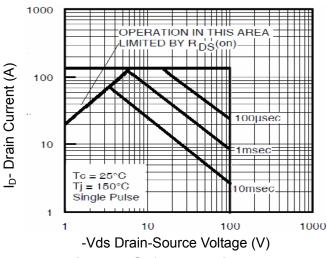


 T_C Case Temperature($^{\circ}C$)

Figure 7 Capacitance vs Vds Figure 9 Drain Current vs Case Temperature

40

30



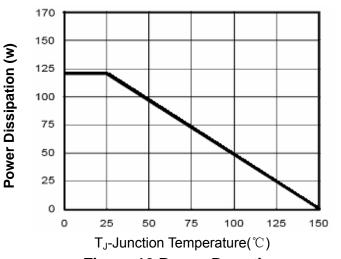


Figure 8 Safe Operation Area

Figure 10 Power De-rating

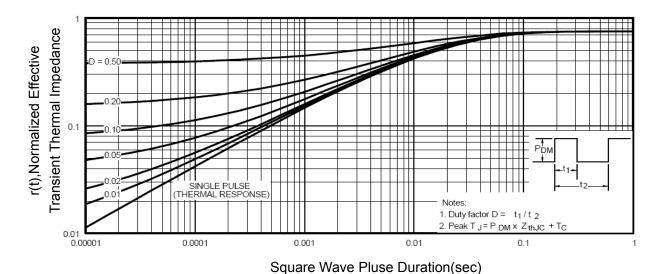
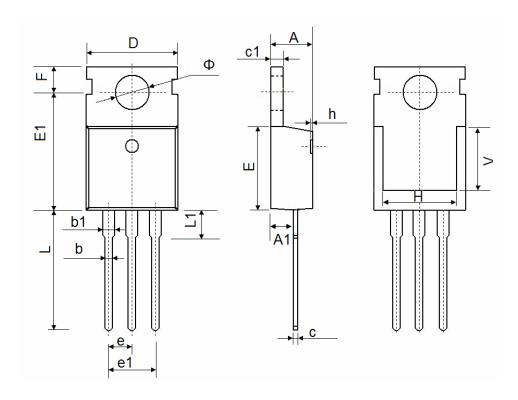


Figure 11 Normalized Maximum Transient Thermal Impedance



TO-220-3L Package Information



Complete	Dimension	s In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
Е	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.54	10 TYP.	0.100	TYP.	
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.50	7.500 REF.		REF.	
Ф	3.400	3.800	0.134	0.150	

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