

### NCE N-Channel Enhancement Mode Power MOSFET

### Description

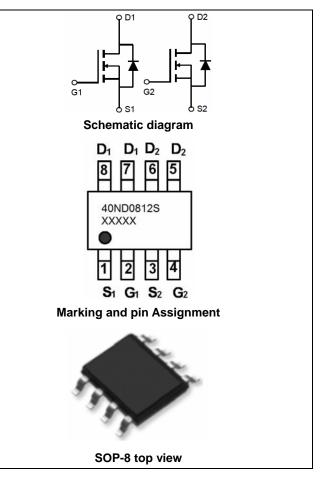
The NCE40ND0812S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **General Features**

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

### Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
40ND0812S	NCE40ND0812S	SOP-8	Ø330mm	12mm	4000 units

### Absolute Maximum Ratings (T<sub>A</sub>=25℃ unless otherwise noted)

Parame	Symbol	N1-Channel	N2-Channel	Unit		
Drain-Source Voltage	V <sub>DS</sub>	40	40	V		
Gate-Source Voltage	V <sub>GS</sub>	±20	±20	V		
Continuous Drain Current	T <sub>A</sub> =25℃	1	8	12	A	
Continuous Drain Current	T <sub>A</sub> =100℃	I <sub>D</sub>	5.7	8.5		
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	32	60	А	
Maximum Power Dissipation $T_A=25^{\circ}C$		PD	2	2.5	W	
Operating Junction and Storage T	T <sub>J</sub> ,T <sub>STG</sub>	-55	-55 To 150			

### **Thermal Characteristic**

Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 2) (N1-Channel)	R <sub>0JA</sub>	62.5	85	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2) (N2-Channel)	R <sub>0JA</sub>	50	75	°C/W



### N1-CH Electrical Characteristics (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.5	2.0	V
Drain Courses On State Desistance	D	$V_{GS}$ =10V, $I_{D}$ =8A	-	15.8	18	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	22	28	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =8A	33	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	Clss	<u>)/ -20)/)/ -0)/</u>	-	964	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V, F=1.0MHz	-	109	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	96	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	5.5	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =20V, R <sub>L</sub> =2.5 $\Omega$	-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{GEN}$ =3 $\Omega$	-	24	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Qg	N/ 00\/ L 0A	-	22.9	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=20V, I_{D}=8A,$	-	3.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	5.3	-	nC
Drain-Source Diode Characteristics					•	·
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =8A	-	0.8	1.2	V

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

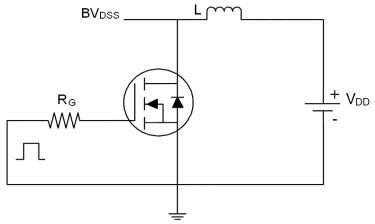
2. The value of R<sub>BJA</sub> is measured with the device mounted on 1in <sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user's specific board design. Surface Mounted on FR4 Board, t  $\leq$  10 sec. The current rating is based on the t  $\leq$  10s thermal resistance rating.

**3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

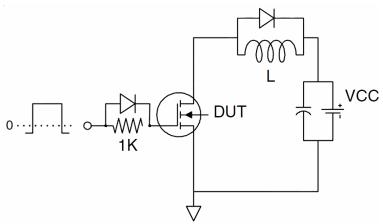
4. Guaranteed by design, not subject to production.



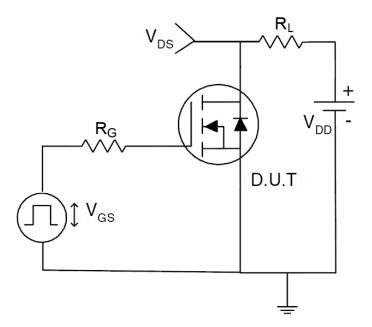
### Test Circuit 1) E<sub>AS</sub> Test Circuits



### 2) Gate Charge Test Circuit:



### 3) Switch Time Test Circuit:





### NCE40ND0812S

90%

10%

90%

50%

t<sub>d(off)</sub>

**INVERTED** 

**PULSE WIDTH** 

Figure 2:Switching Waveforms

on

10%

50%

90%

t<sub>d(on)</sub>

V<sub>OUT</sub>

V<sub>IN</sub>

10%

### N1- Channel Typical Electrical and Thermal Characteristics (Curves)

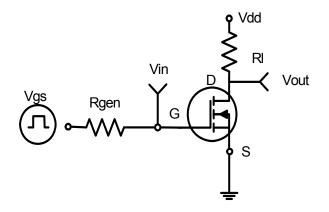
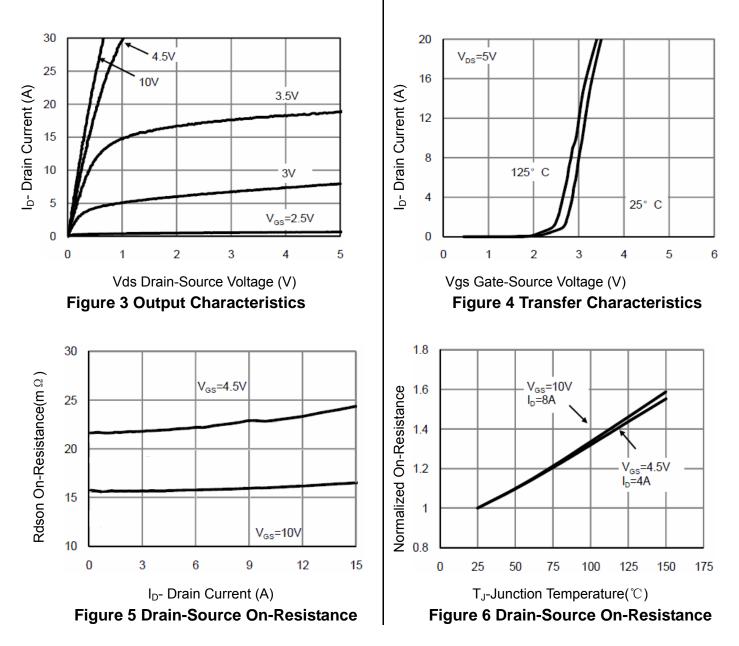
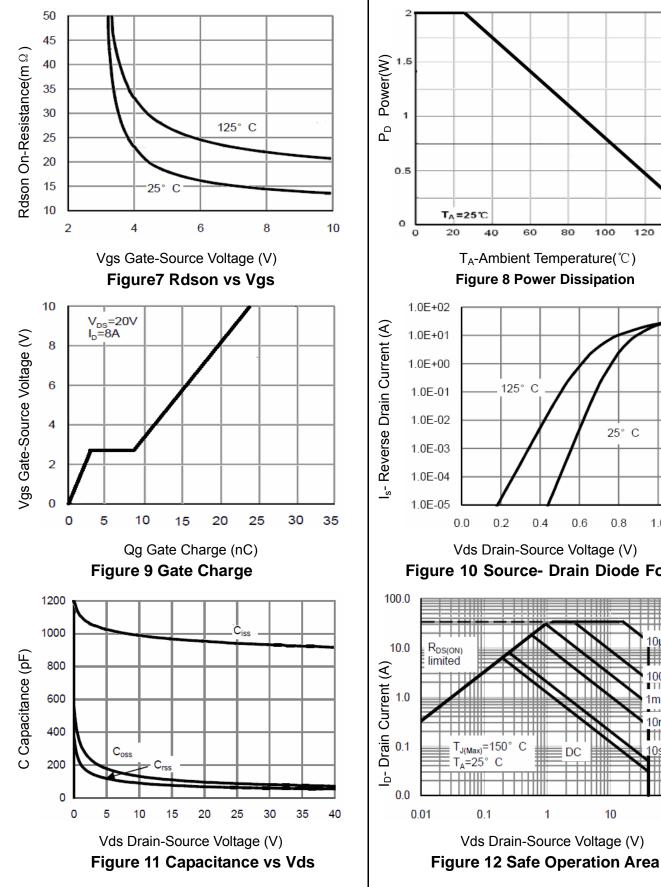


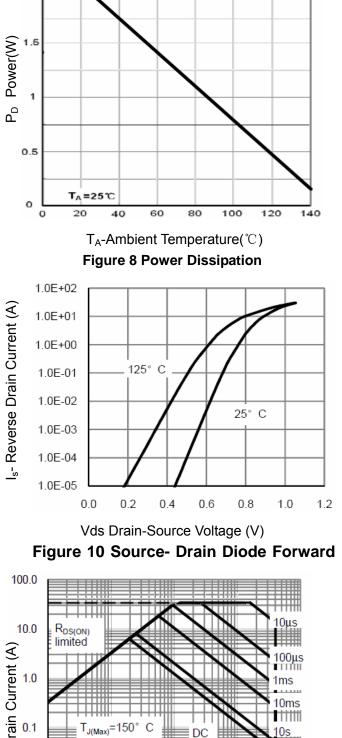
Figure 1:Switching Test Circuit





# NCE40ND0812S





С

1

100

10



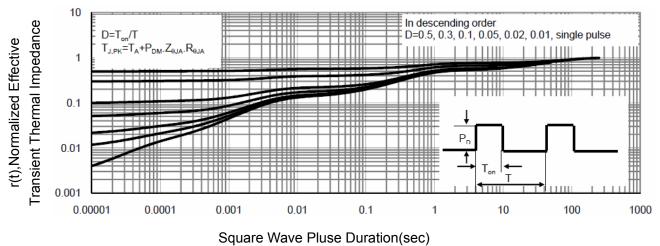


Figure 13 Normalized Maximum Transient Thermal Impedance



### N2-CH Electrical Characteristics (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			•
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	40	45	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.2	1.6	2.5	V
Drain October On Otata Desistance		$V_{GS}$ =10V, $I_{D}$ =10A	-	11.7	14	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =4.5V, $I_D$ =8A	-	15.6	20	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =10A		75	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V,	-	1780	-	PF
Output Capacitance	C <sub>oss</sub>		-	209	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	160	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	6.4	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =20V, R <sub>L</sub> =2 $\Omega$	-	17.2	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =3 $\Omega$	-	29.6	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	16.8	-	nS
Total Gate Charge	Qg	V/ 00)/1 40A	-	38.2		nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=20V, I_{D}=10A,$	-	5.6		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	7.4		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	12	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 10A	-	29	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	26	-	nC
				1		ı

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

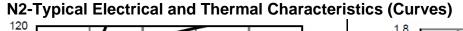


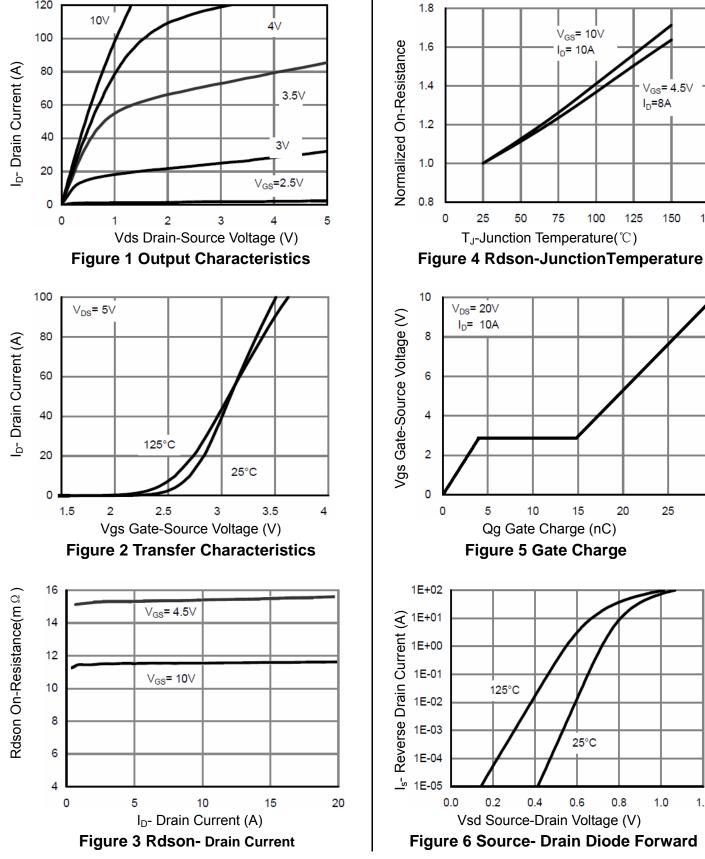
150

25

30

175

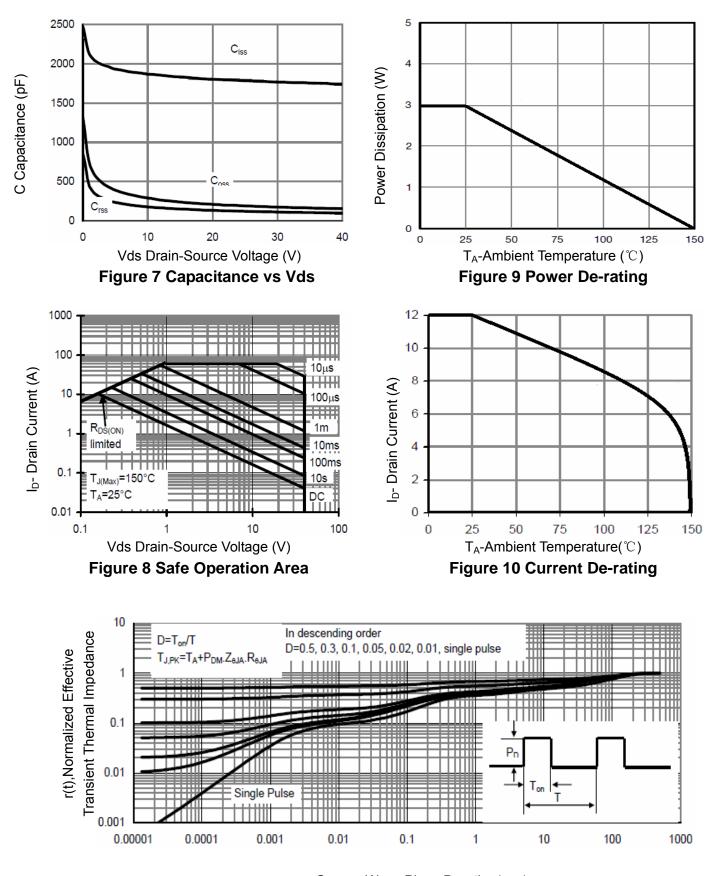




1.2



## NCE40ND0812S

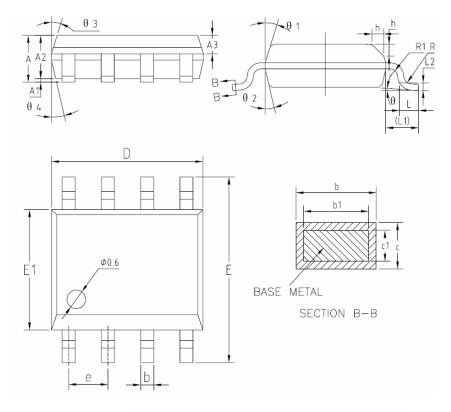


Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance



# NCE40ND0812S

### **SOP-8** Package Information



# COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.10	0.15	0.25
A2	1.25	1.40	1.65
A3	0.50	0.60	0.70
b	0.38	1	0.51
b1	0.37	0.42	0.47
с	0.18	-	0.25
c1	0.17	0.20	0.23
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
е	1.17	1.27	1.37
L	0.45	0.60	0.80
L1		1.04REF	
L2		0.25BSC	
R	0.07	1	_
R1	0.07	-	_
h	0.30	0.40	0.50
θ	0°	-	8°
θ1	15°	17°	19 <b>°</b>
θ2	11 <b>°</b>	13°	15 <b>°</b>
θ3	15 <b>°</b>	17°	19 <b>°</b>
θ4	11°	13°	15°



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