

NCE P-Channel Enhancement Mode Power MOSFET

Description

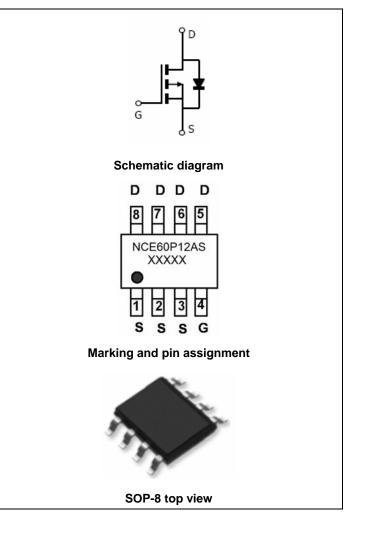
The NCE60P12AS uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- V_{DS} =-60V,I_D =-12A
 R_{DS(ON)} <14mΩ @ V_{GS}=-10V
 R_{DS(ON)} <17mΩ @ V_{GS}=-4.5V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- DC-DC Converter



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE60P12AS	NCE60P12AS	SOP-8	Ø330mm	12mm	4000 units

Absolute Maximum Ratings (T_A=25[°]Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	-12	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	-8.5	А
Pulsed Drain Current	I _{DM}	-50	А
Maximum Power Dissipation	PD	3.5	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance .Junction-to-Ambient ^(Note 2)	В	25	°C ////	I
I hermal Resistance , Junction-to-Ambient	rθja		C/VV	1

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Electrical Characteristics (T_A=25 $^\circ\!\!\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA -60		-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	·		•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.2	-1.8	-2.5	V
Desir Osumo Or Otata Desistante		V _{GS} =-10V, I _D =-12A	-	11	14	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-12A	-	13	17	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-12A	-	40	-	S
Dynamic Characteristics (Note4)			L			
Input Capacitance	C _{lss}	V _{DS} =-30V,V _{GS} =0V, F=1.0MHz	-	5604	-	PF
Output Capacitance	C _{oss}		-	356	-	PF
Reverse Transfer Capacitance	C _{rss}		-	265	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	16	-	nS
Turn-on Rise Time	tr	V_{DD} =-30V, ,RL=2.5 Ω	-	18	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =6 Ω	-	50	-	nS
Turn-Off Fall Time	t _f		-	33	-	nS
Total Gate Charge	Qg	V - 20V/I - 12A	-	62.1	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-30V,I _D =-12A, V _{GS} =-10V	-	9.3	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-10V	-	16.8	-	nC
Drain-Source Diode Characteristics			•	•		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-12A	-	-	-1.2	V
Diode Forward Current (Note 2)	ls		-	-	-12	А

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

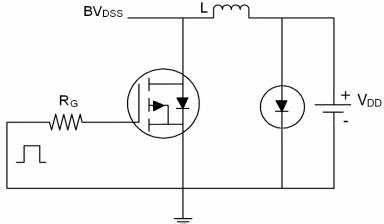
2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

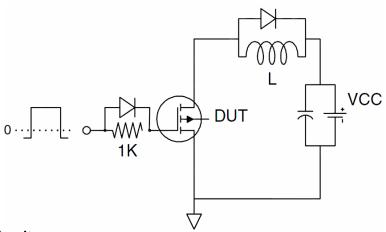
4. Guaranteed by design, not subject to production



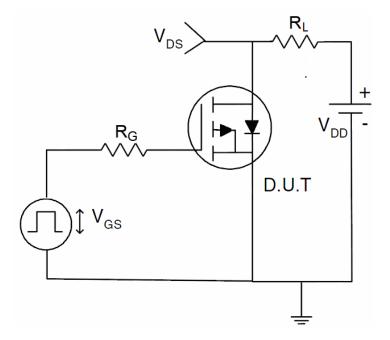
Test Circuit 1) E_{AS} Test Circuit



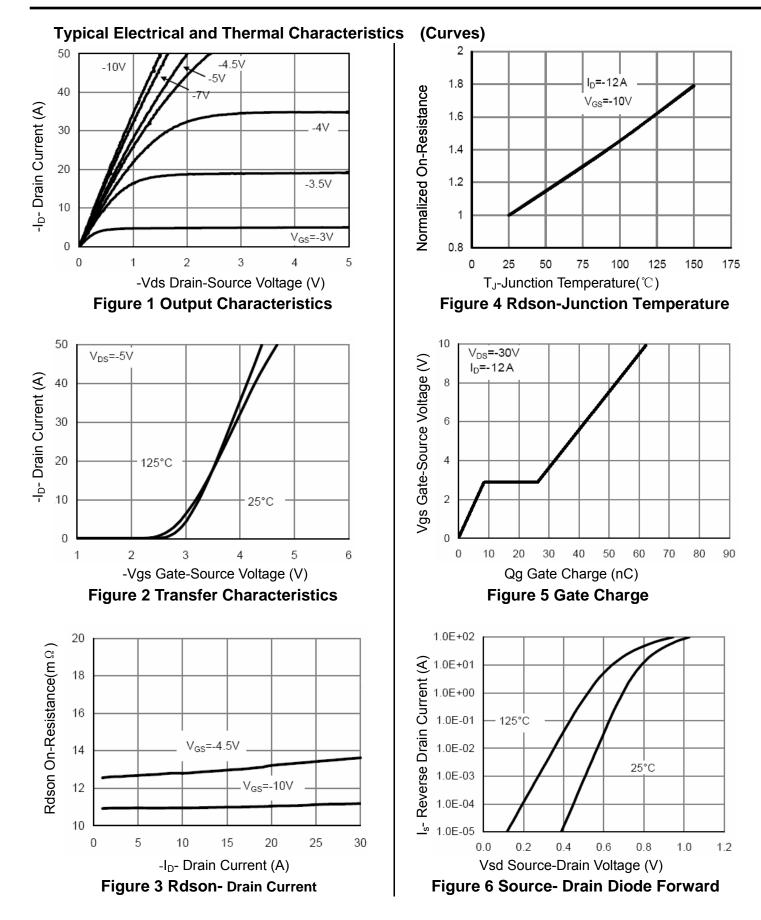
2) Gate Charge Test Circuit



3) Switch Time Test Circuit



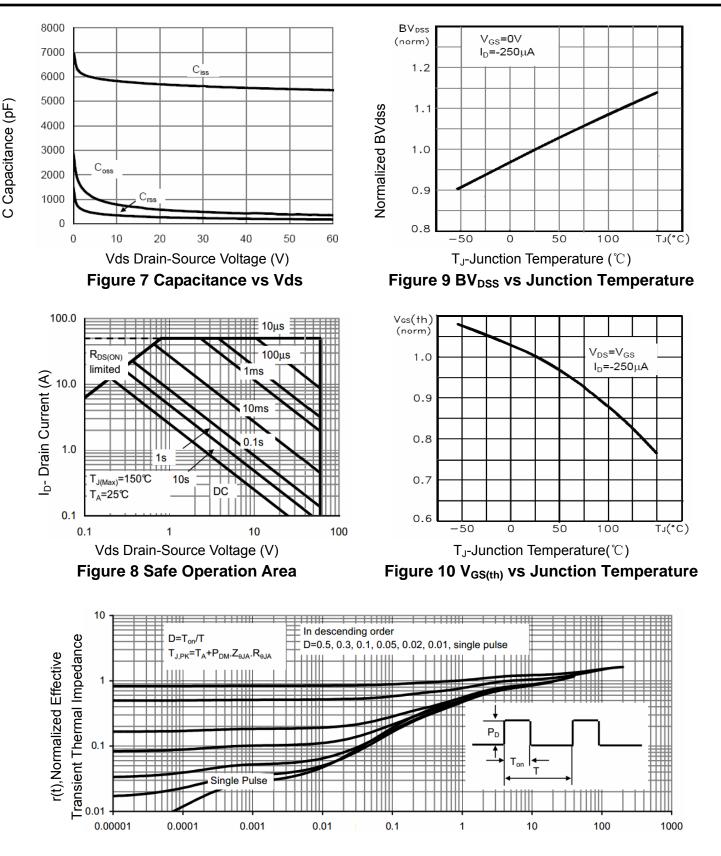






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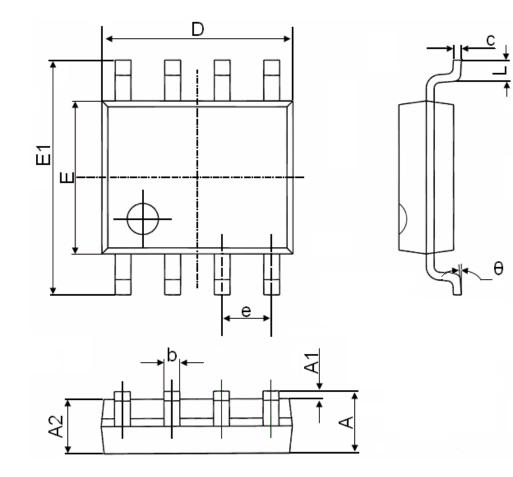
NCE60P12AS



Square Wave Pluse Duration(sec)
Figure 11 Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information



Symbol	Dimensions	n Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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