

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE60ND45AG uses advanced trench technology and design to provide excellent $R_{\text{DS}(\text{ON})}$ with low gate charge. It can be used in a wide variety of applications.

Application

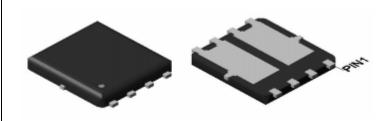
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED! 100% ΔVds TESTED!

General Features

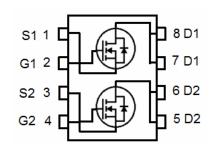
- V_{DS} =60V, I_{D} =45A $R_{DS(ON)}$ =9.4m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =13.4m Ω (typical) @ V_{GS} =4.5V
- High density cell design for ultra low Rdson
- Very low on-resistance R_{DS(on)}
- Good stability and uniformity with high E_{AS}
- 150 °C operating temperature
- Pb-free lead plating

DFN 5X6



Top View

Bottom View



Schematic Diagram

Package Marking and Ordering Information

	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
Ī	60ND45AG	NCE60ND45AG	DFN5X6-8L	Ø330mm	12mm	5000

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	45	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	32	Α
Pulsed Drain Current	I _{DM}	140	А
Single pulse avalanche energy (Note 5)	E _{AS}	260	mJ
Maximum Power Dissipation	P _D	60	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	${\mathbb C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rejc	2.08	°C/W

NCE60ND45AG

Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =60V, V_{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics (Note 3)						•
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2	1.9	2.5	V
Danier Courses Our Otata Basistanas		V _{GS} =10V, I _D =20A	-	9.4	11	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	-	13.4	17	mΩ
Forward Transconductance	G FS	V_{DS} =5 V , I_D =20 A	25	-	-	S
Dynamic Characteristics (Note4)						•
Input Capacitance	C _{lss}	V _{DS} =30V,V _{GS} =0V,	-	2750	-	PF
Output Capacitance	Coss		-	170	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	152	-	PF
Switching Characteristics (Note 4)						•
Turn-on Delay Time	t _{d(on)}		-	9	-	nS
Turn-on Rise Time	t _r	V_{DD} =30V, R_L =1.5 Ω	-	7	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =3 Ω	-	32	-	nS
Turn-Off Fall Time	t _f		-	6	-	nS
Total Gate Charge	Qg	\/ 20\/ L 00 A	-	60	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =30V, I_D =20A,	-	10	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	14	-	nC
Drain-Source Diode Characteristics	<u> </u>		•			•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	45	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF=20A	-	31	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	45	-	nC

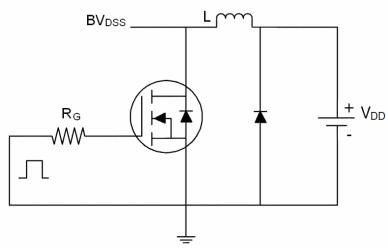
Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- **5.**E_{AS} condition: Tj=25 $^{\circ}$ C,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25 Ω

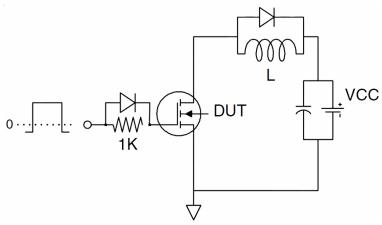


Test Circuit

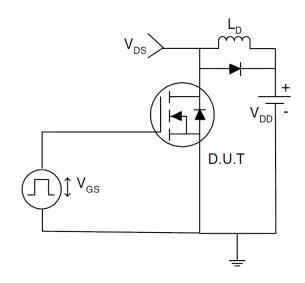
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

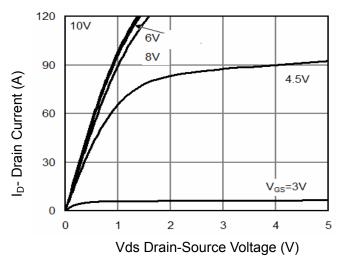


Figure 1 Output Characteristics

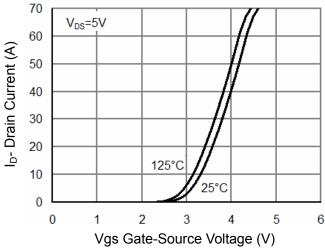


Figure 2 Transfer Characteristics

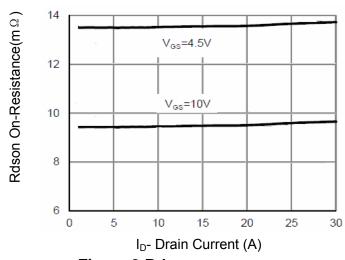


Figure 3 Rdson- Drain Current

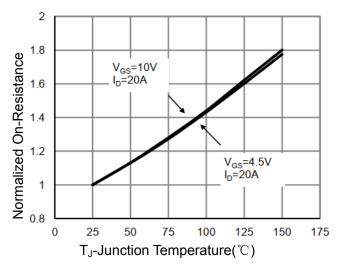


Figure 4 Rdson-JunctionTemperature

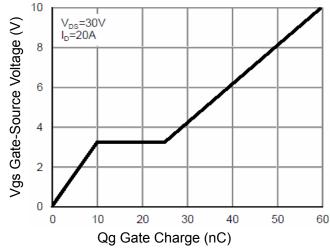


Figure 5 Gate Charge

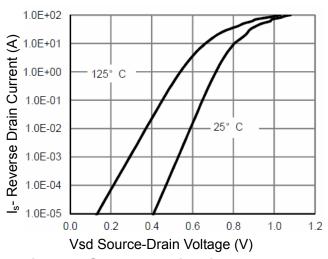
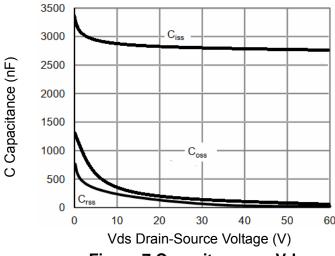


Figure 6 Source- Drain Diode Forward

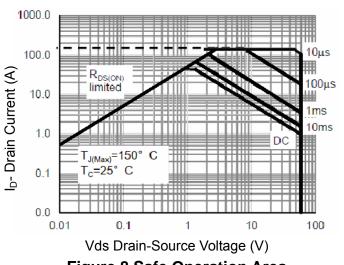




60 50 ID- Drain Current (A) 40 30 20 10 0 25 75 100 125 0 150 175 T_C -Case Temperature($^{\circ}C$)

Figure 7 Capacitance vs Vds

Figure 9 Current De-rating



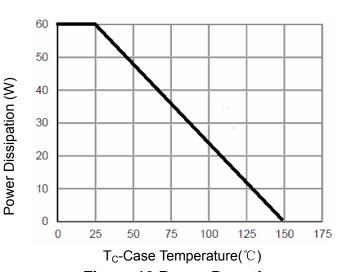
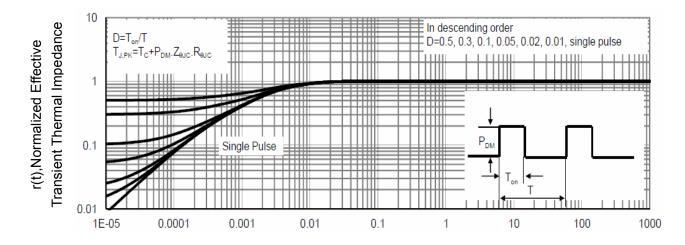


Figure 8 Safe Operation Area

Figure 10 Power De-rating

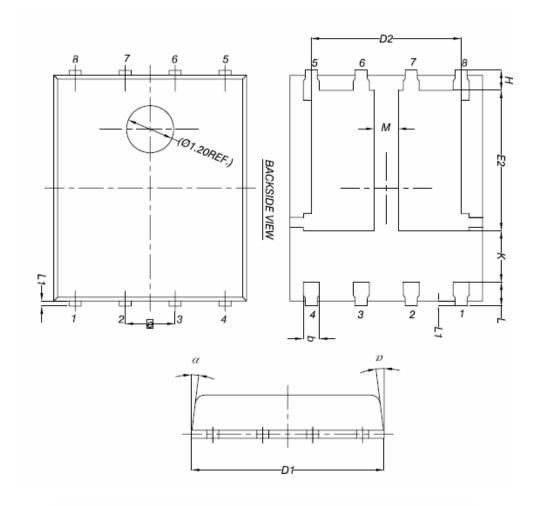


Square Wave Pluse Duration(sec)

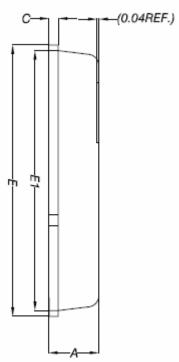
Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



544	MILLIMETERS			
DIM.	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Ε	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	1.27 BSC			
Н	0.41	0.51	0.61	
K	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
М	0.50	-	-	
α	0°	-	12°	



NCE60ND45AG

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