

## NCE N-Channel Enhancement Mode Power MOSFET

### Description

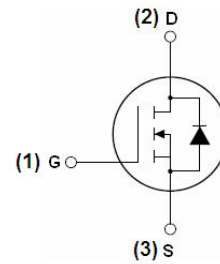
The NCE0275T uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in automotive applications and a wide variety of other applications.

### General Features

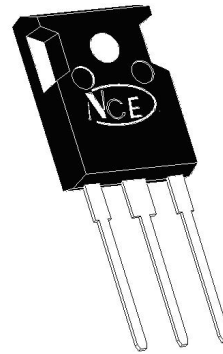
- $V_{DSS} = 200V, I_D = 75A$   
 $R_{DS(ON)} < 22m\Omega @ V_{GS} = 10V$
- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high  $E_{AS}$
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

### Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



TO-247-3L top view

**100% UIS TESTED!**  
**100%  $\Delta V_{ds}$  TESTED!**

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE0275T	NCE0275T	TO-247-3L	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DSS}$	200	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	75	A
Drain Current-Continuous( $T_C = 100^\circ C$ )	$I_D(100^\circ C)$	53	A
Pulsed Drain Current (Note 1)	$I_{DM}$	300	A
Maximum Power Dissipation	$P_D$	360	W
Derating factor		2.4	W/ $^\circ C$
Single pulse avalanche energy (Note 3)	$E_{AS}$	600	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 1)	$R_{\theta JC}$	0.42	$^\circ C/W$
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## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

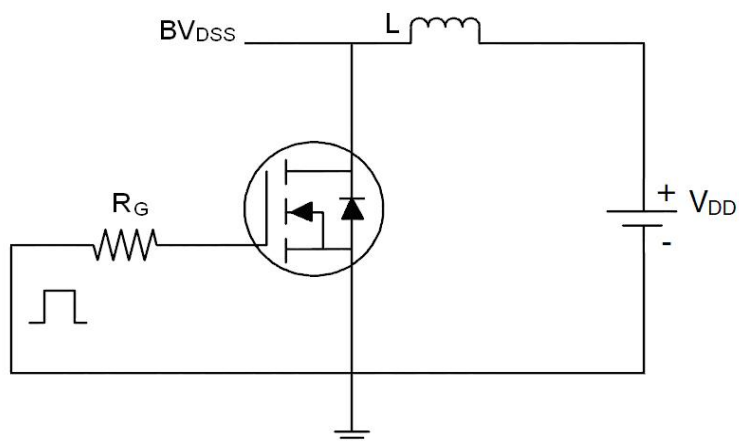
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	200	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	-	19	22	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =40A	-	115	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, F=1.0MHz	-	8238	-	PF
Output Capacitance	C <sub>oss</sub>		-	275	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	112	-	PF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =100V, I <sub>D</sub> =40A, V <sub>GS</sub> =10V, R <sub>G</sub> =2.7Ω	-	17	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	18	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	56	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	22	-	nS
Total Gate Charge	Q <sub>g</sub>	I <sub>D</sub> =40A, V <sub>DD</sub> =100V, V <sub>GS</sub> =10V	-	152.7	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	44.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	47.9	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =75A	-	-	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 40A	-	136	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs <sup>(Note2)</sup>	-	458	-	nC

### Notes:

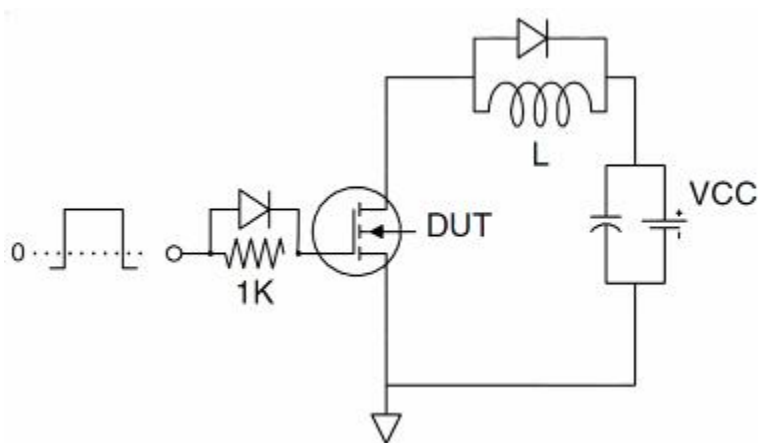
1. Surface Mounted on FR4 Board, t ≤ 10 sec.
2. Pulse Test: Pulse Width ≤ 400μs, Duty Cycle ≤ 2%.
3. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, L=1mH, R<sub>G</sub>=25Ω

**Test Circuit**

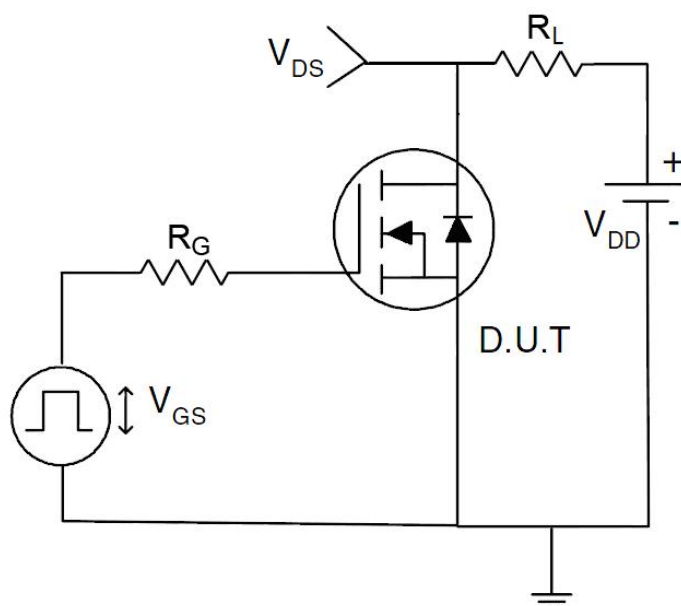
**1) E<sub>AS</sub> test Circuit**



**2) Gate charge test Circuit**



**3) Switch Time Test Circuit**



Typical Electrical and Thermal Characteristics(Curves)

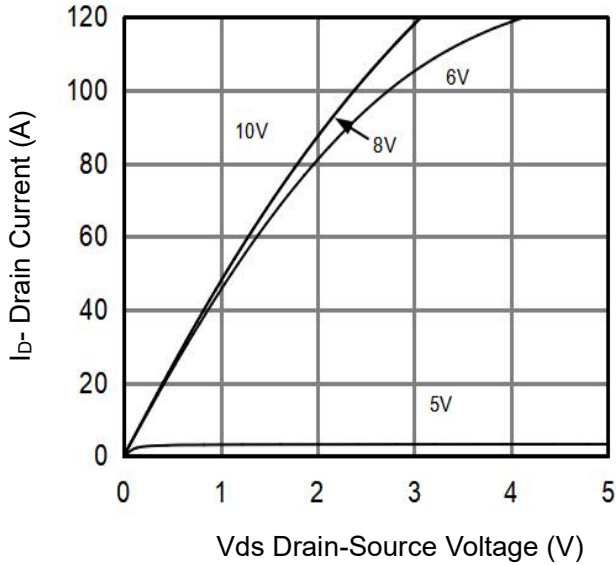


Figure 1 Output Characteristics

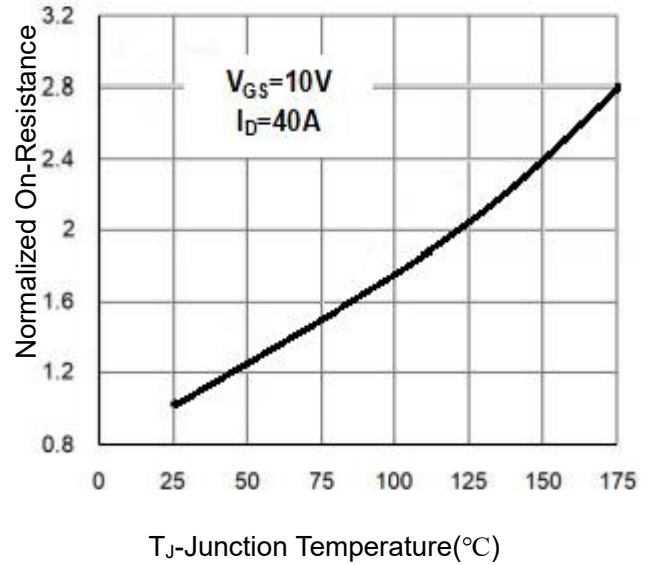


Figure 4 Rds(on)-Junction Temperature

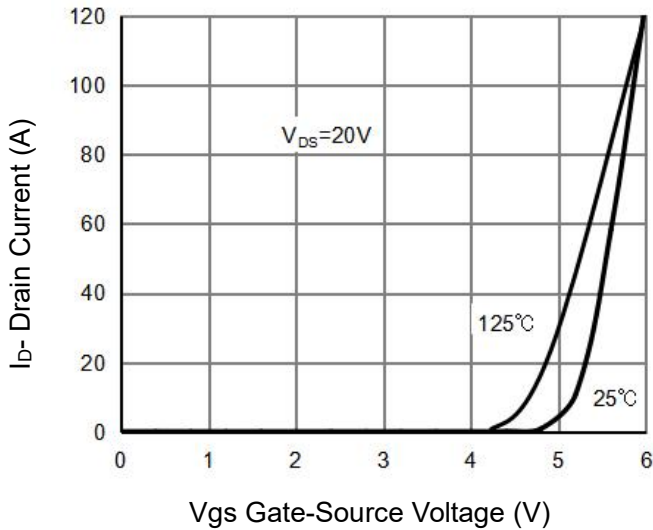


Figure 2 Transfer Characteristics

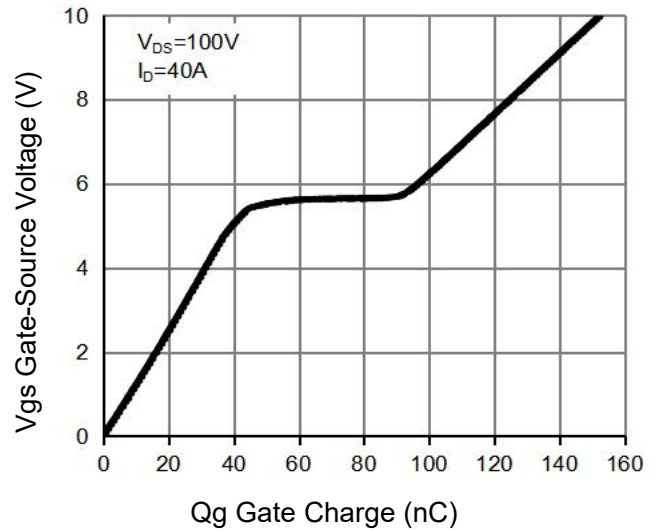


Figure 5 Gate Charge

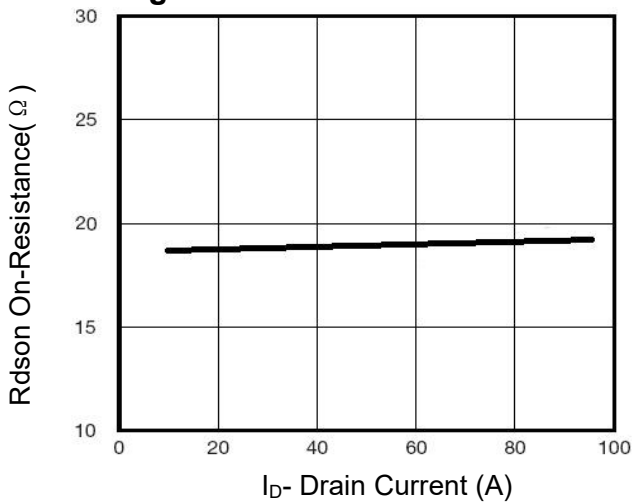


Figure 3 Rds(on)- Drain Current

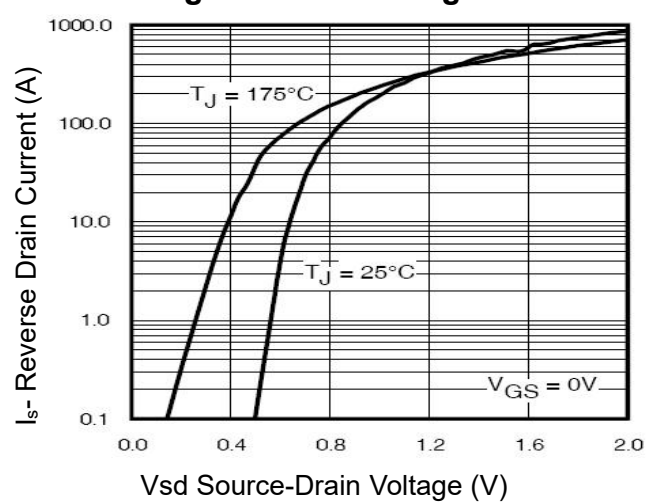


Figure 6 Source- Drain Diode Forward

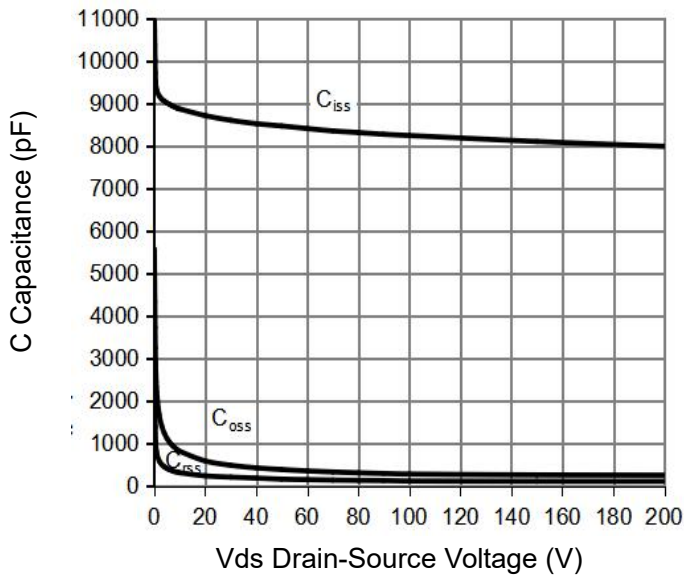


Figure 7 Capacitance vs Vds

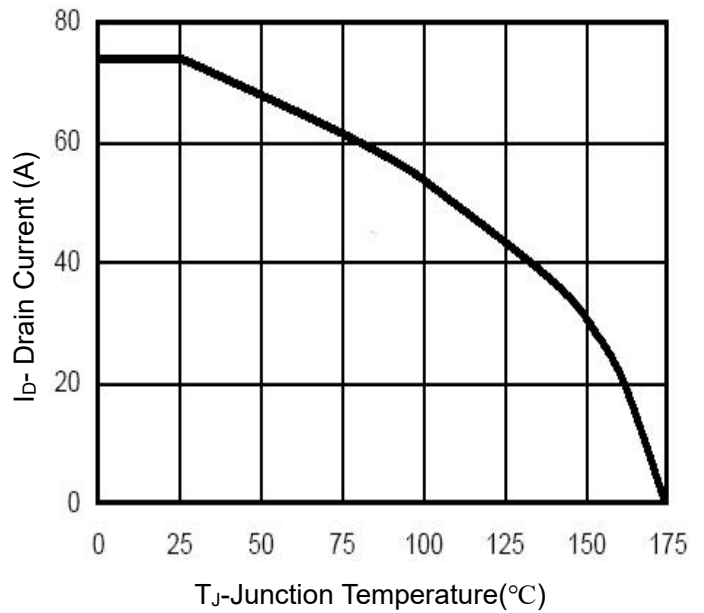


Figure 9 Current De-rating

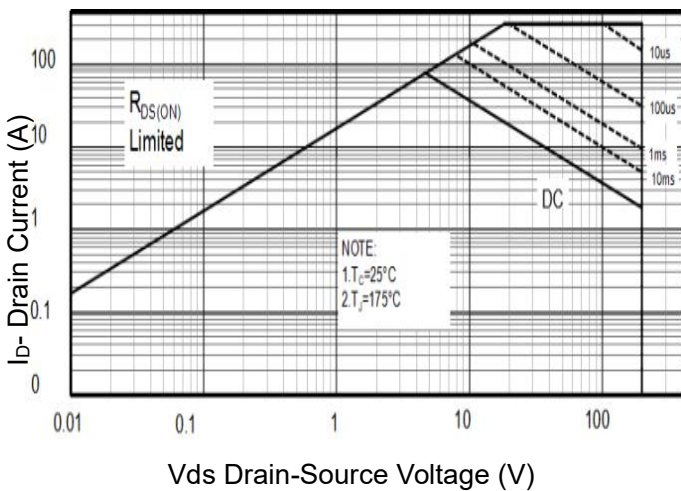


Figure 8 Safe Operation Area

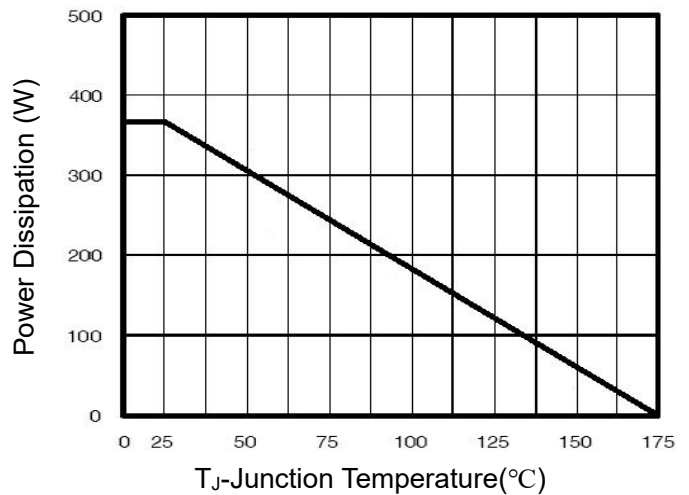


Figure 10 Power De-rating

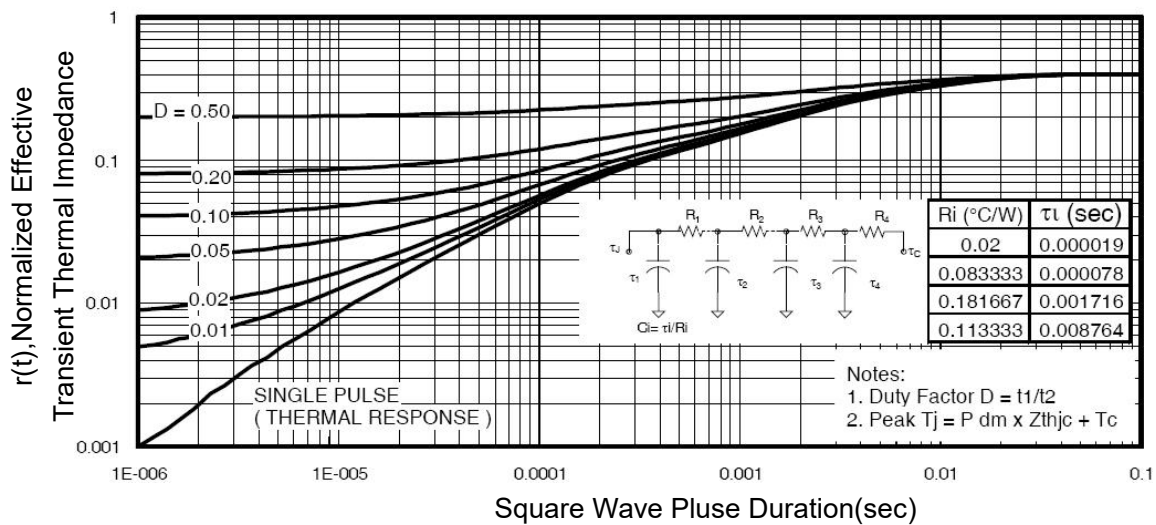
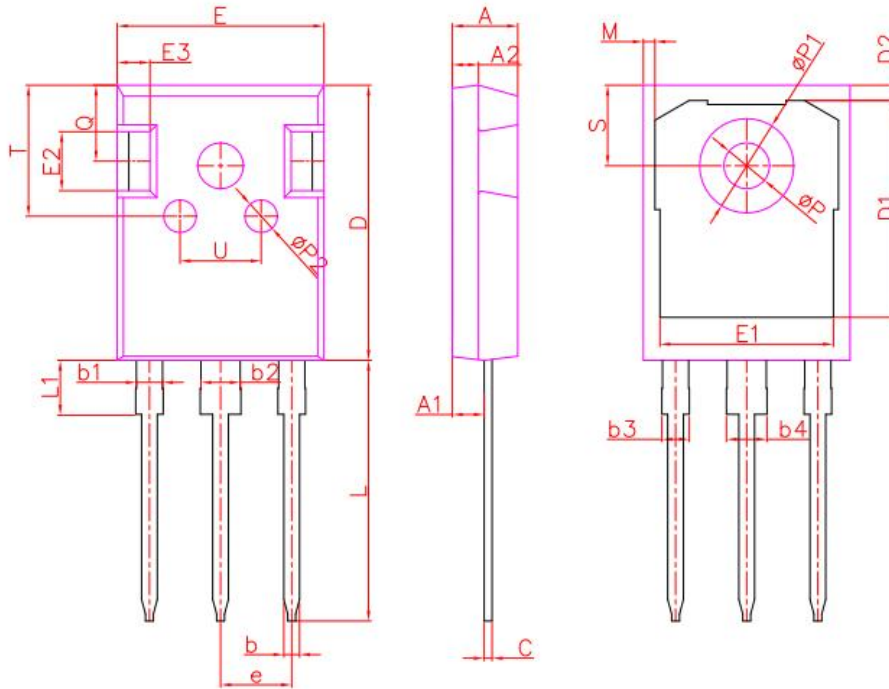


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-247-3L Package Information



TO247			
DIM.	MIN.	NOM.	MAX.
A	4.90	5.00	5.10
A1	2.31	2.432	2.51
A2	1.90	2.00	2.10
b	1.16	1.20	1.26
b1	1.96	2.00	2.06
b2	2.96	3.00	3.06
b3	-	-	2.25
b4	-	-	3.25
c	0.59	0.60	0.66
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.26	13.50
E2	4.40	4.50	4.60
E3	2.40	2.50	2.60
e	5.436BSC		
L	19.80	19.90	20.10
L1	-	-	4.30
M	0.35	0.89	0.95
P	3.40	3.50	3.60
P1	7.00	7.20	7.40
P2	2.40	2.50	2.60
Q	5.60	5.80	6.00
S	6.05	6.15	6.25
T	9.80	10.00	10.20
U	6.00	6.20	6.40
All dimensions in millimeters			

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