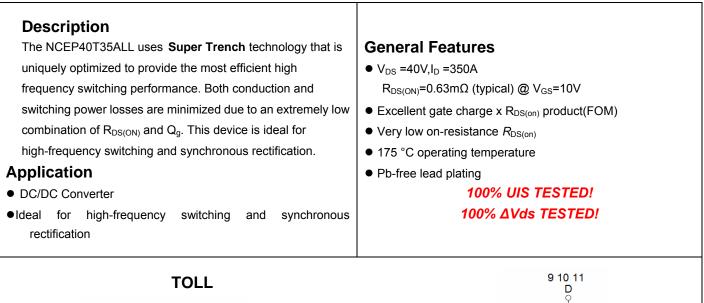
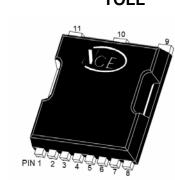
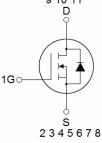


NCE N-Channel Super Trench Power MOSFET







Schematic Diagram

Package Marking and Ordering Information

ſ	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
	NCEP40T35ALL	NCEP40T35ALL	TOLL	-	-	-

Absolute Maximum Ratings (T_c=25[°]C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	40	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous (Silicon Limited)	Ι _D	350	А
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	247	A
Pulsed Drain Current (Note 1)	I _{DM}	1400	А
Maximum Power Dissipation	PD	500	W
Derating factor		3.33	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	1800	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{θJC}	0.3	°C /W	
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Electrical Characteristics (T_c=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	40		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	· ·					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0		4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =175A	-	0.63	0.8	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =175A	100	-	-	S
Dynamic Characteristics (Note4)	· ·		-			
Input Capacitance	C _{lss}		-	11669	-	PF
Output Capacitance	C _{oss}	V_{DS} =20V, V_{GS} =0V,	-	4641	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	140	-	PF
Switching Characteristics (Note 4)	· ·					
Turn-on Delay Time	t _{d(on)}		-	26	-	nS
Turn-on Rise Time	tr	V _{DD} =20V,I _D =175A	-	14	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_G =1.6 Ω	-	110	-	nS
Turn-Off Fall Time	t _f		-	17	-	nS
Total Gate Charge	Qg	\/	-	182	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =20V,I _D =175A, V _{GS} =10V	-	59		nC
Gate-Drain Charge	Q _{gd}		-	38		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =175A	-		1.2	V
Diode Forward Current (Note 2)	I _S		-	-	350	А
Reverse Recovery Time	t _{rr}	T_J = 25°C, I_F = I_S	-		66	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-		240	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

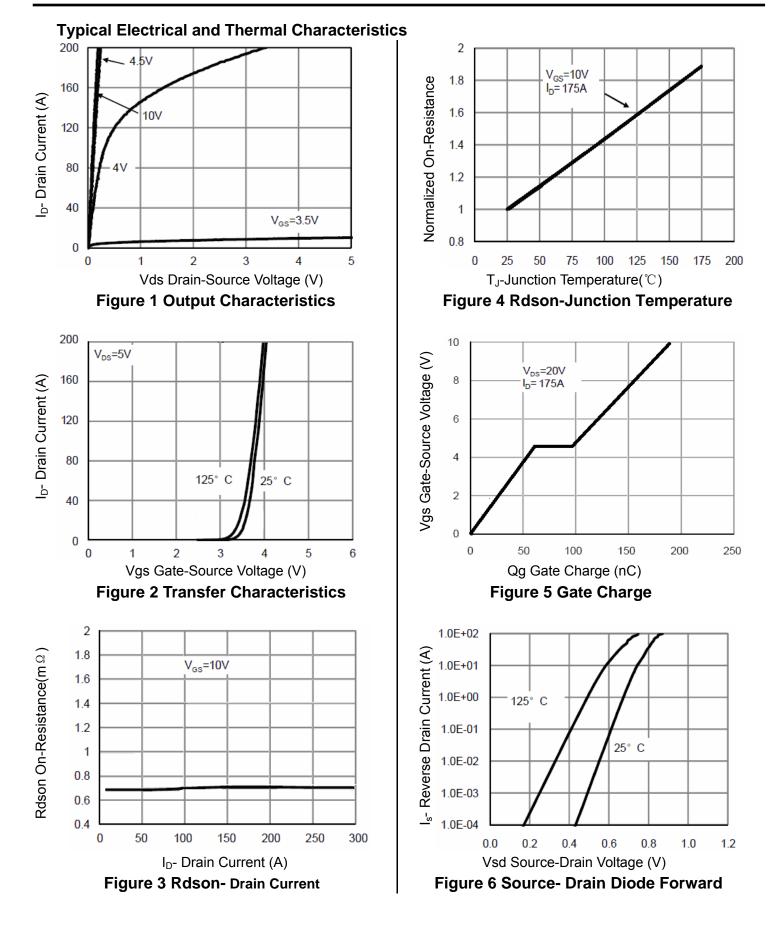
3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

5. EAS condition : Tj=25 $^\circ \!\! \mathbb{C}$,V_{DD}=20V,V_G=10V,L=0.5mH,Rg=25 Ω



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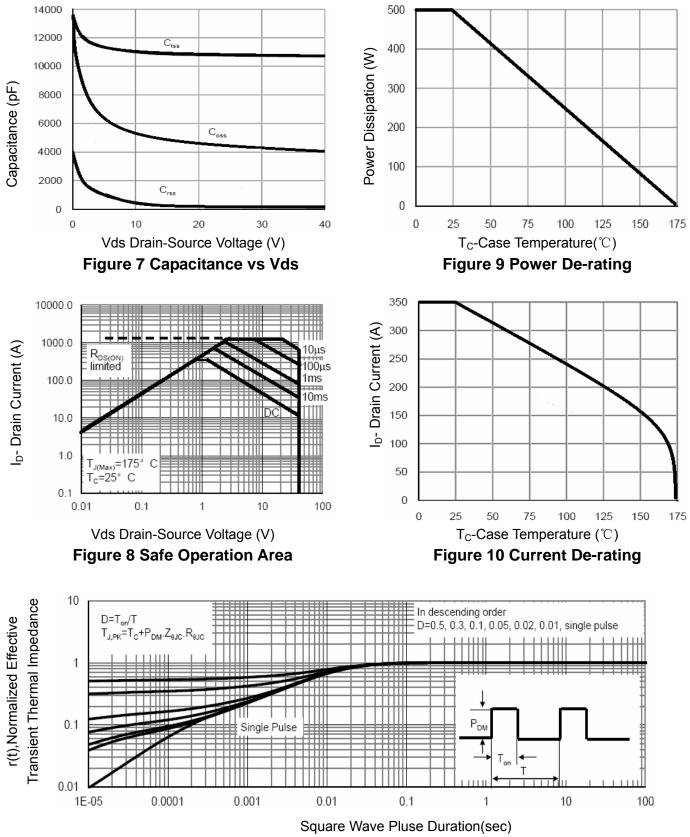
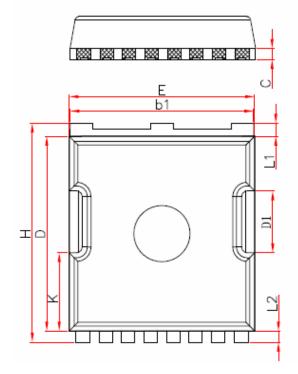


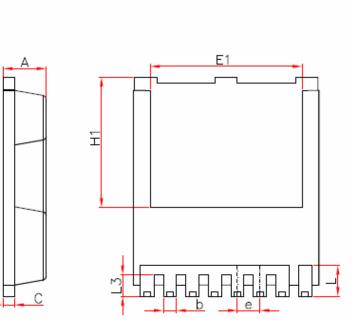
Figure 11 Normalized Maximum Transient Thermal Impedance



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TOLL Package Information





Symbol	Millimeters			
	Min.	Nom.	Max.	
А	2.20	2.30	2.40	
b	0.65	0.75	0.85	
b1	9.70	9.80	9.90	
С	0.50	0.60	0.70	
D	10.30	10.40	10.50	
D1	3.15	3.3	3.45	
Е	9.70	9.90	10.10	
E1	8.00	8.10	8.20	
е	1.10	1.20	1.30	
Н	11.6	11.7	11.8	
H1	6.85	6.95	7.05	
K	4.08	4.18	4.28	
L	1.60	1.65	2.10	
L1	0.60	0.70	0.80	
L2	0.50	0.60	0.70	
L3	1.05	1.20	1.30	



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