

NCE Automotive P-Channel Super Trench Power MOSFET

Description

The NCEAP40P60G uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification

Application

- Automotive application
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

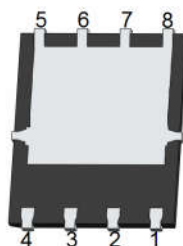
General Features

- $V_{DS} = -40V, I_D = -60A$
- $R_{DS(ON)} = 8.8m\Omega$ (typical) @ $V_{GS} = -10V$
- $R_{DS(ON)} = 12.5m\Omega$ (typical) @ $V_{GS} = -4.5V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested
- 100% ΔV_{ds} tested
- **AEC-Q101 qualified**

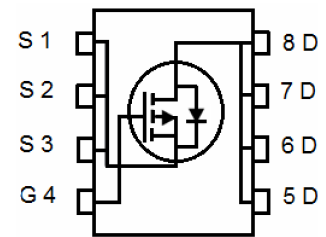
DFN 5X6



Top View



Bottom View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AP40P60G	NCEAP40P60G	DFN5X6-8L	-	-	-

Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	$I_D(T_c = 25^\circ C)$	-68	A
Drain Current-Continuous($T_c = 100^\circ C$)	$I_D(T_c = 100^\circ C)$	-48.6	A
Pulsed Drain Current	I_{DM}	-272	A
Maximum Power Dissipation($T_c = 25^\circ C$)	$P_D(T_c = 25^\circ C)$	96	W
Derating factor		0.64	W/ $^\circ C$
Single pulse avalanche energy (Note 1)	E_{AS}	352	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.56	$^\circ C/W$
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Electrical Characteristics (T_c=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1.1	-1.7	-2.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-20A	-	8.8	11.0	mΩ
		V _{GS} =-4.5V, I _D =-20A	-	12.5	17.0	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-20A	-	30	-	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =-20V, V _{GS} =0V, F=1.0MHz	-	2450	-	pF
Output Capacitance	C _{oss}		-	660	-	pF
Reverse Transfer Capacitance	C _{rss}		-	18	-	pF
Switching Characteristics (Note 2)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-20V, I _D =-20A V _{GS} =-10V, R _G =1.6Ω	-	9	-	nS
Turn-on Rise Time	t _r		-	4	-	nS
Turn-Off Delay Time	t _{d(off)}		-	30	-	nS
Turn-Off Fall Time	t _f		-	5	-	nS
Total Gate Charge	Q _g	V _{DS} =-20V, I _D =-20A, V _{GS} =-10V	-	39	-	nC
Gate-Source Charge	Q _{gs}		-	7.8	-	nC
Gate-Drain Charge	Q _{gd}		-	5.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-20A	-	-	-1.2	V
Diode Forward Current	I _S		-	-	-68	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = -20A	-	22	-	nS
Reverse Recovery Charge	Q _{rr}	di/dt = 100A/μs	-	58	-	nC

Notes:

1. EAS condition : T_J=25°C, V_{DD}=-20V, V_G=-10V, L=0.5mH, R_G=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=175°C. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

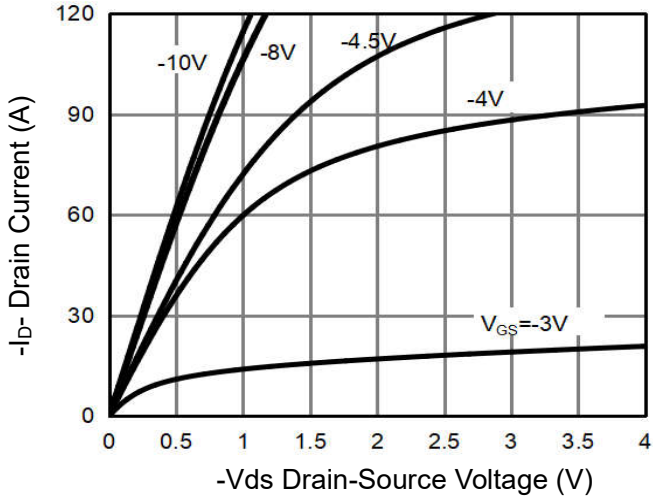


Figure 1 Output Characteristics

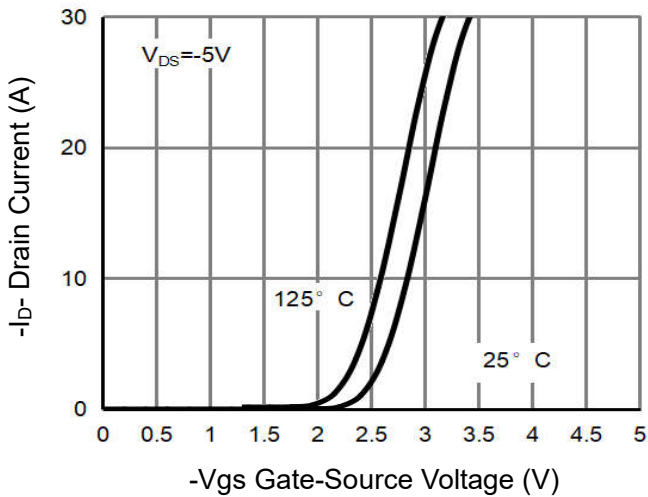


Figure 2 Transfer Characteristics

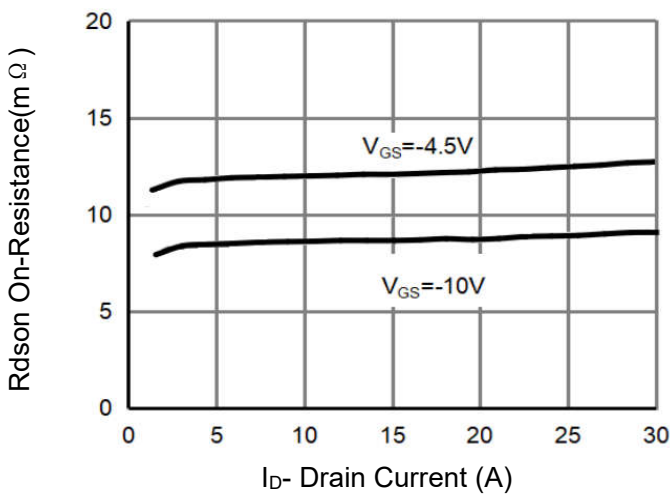


Figure 3 Rdson- Drain Current

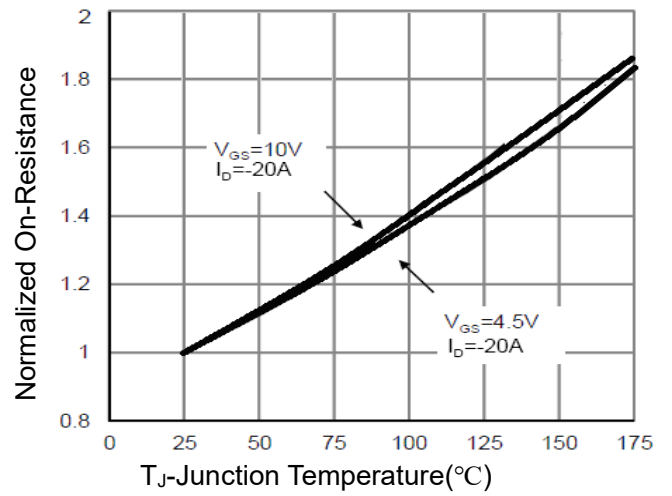


Figure 4 Rdson-Junction Temperature

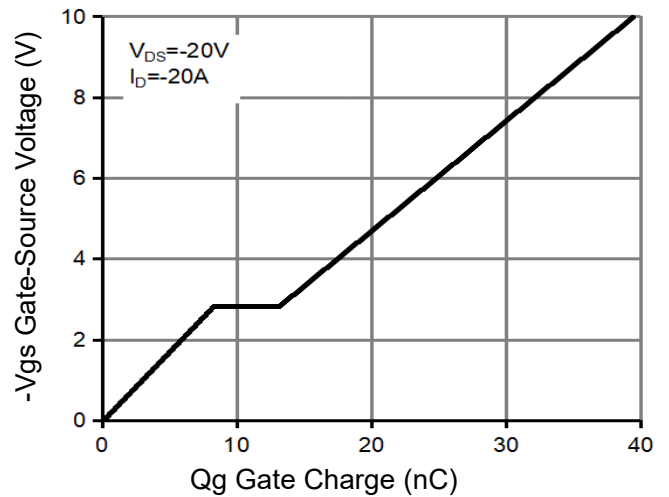


Figure 5 Gate Charge

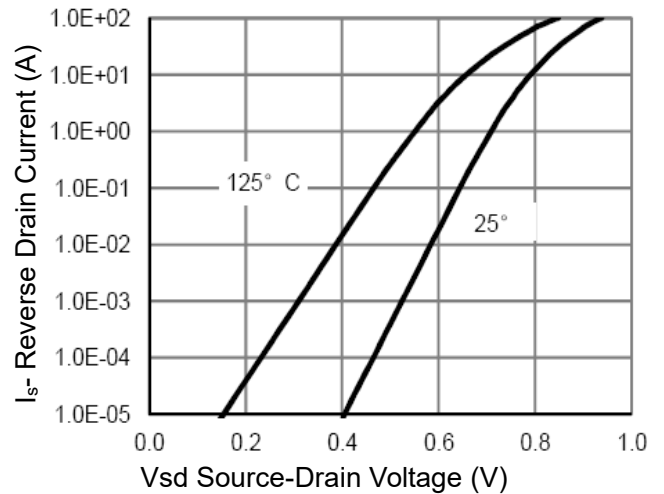


Figure 6 Source- Drain Diode Forward

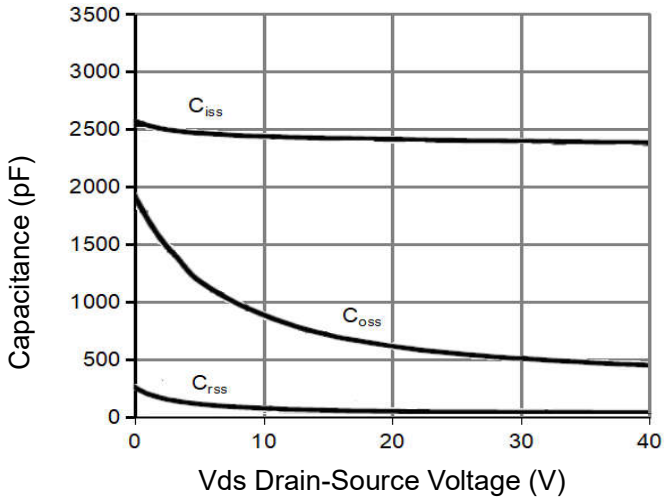


Figure 7 Capacitance vs Vds

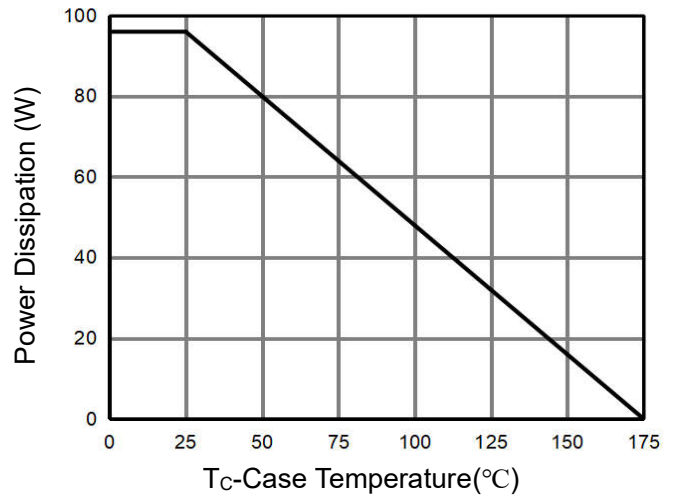


Figure 9 Power De-rating

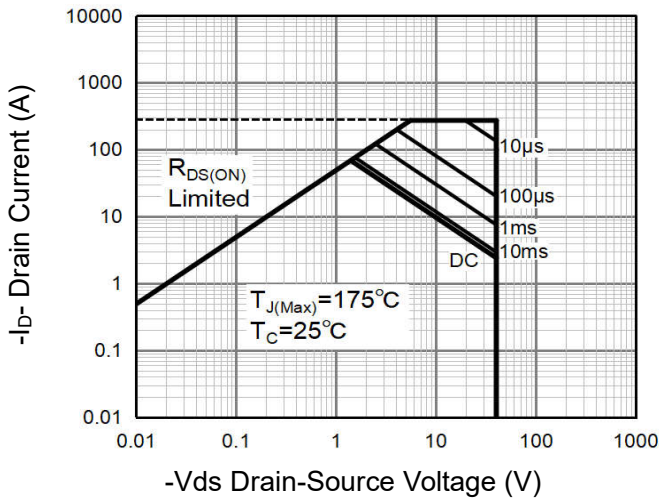


Figure 8 Safe Operation Area (Note3)

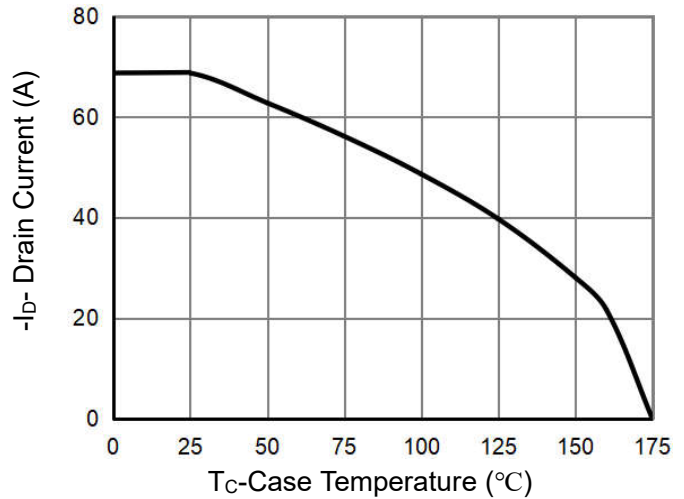


Figure 10 Current De-rating

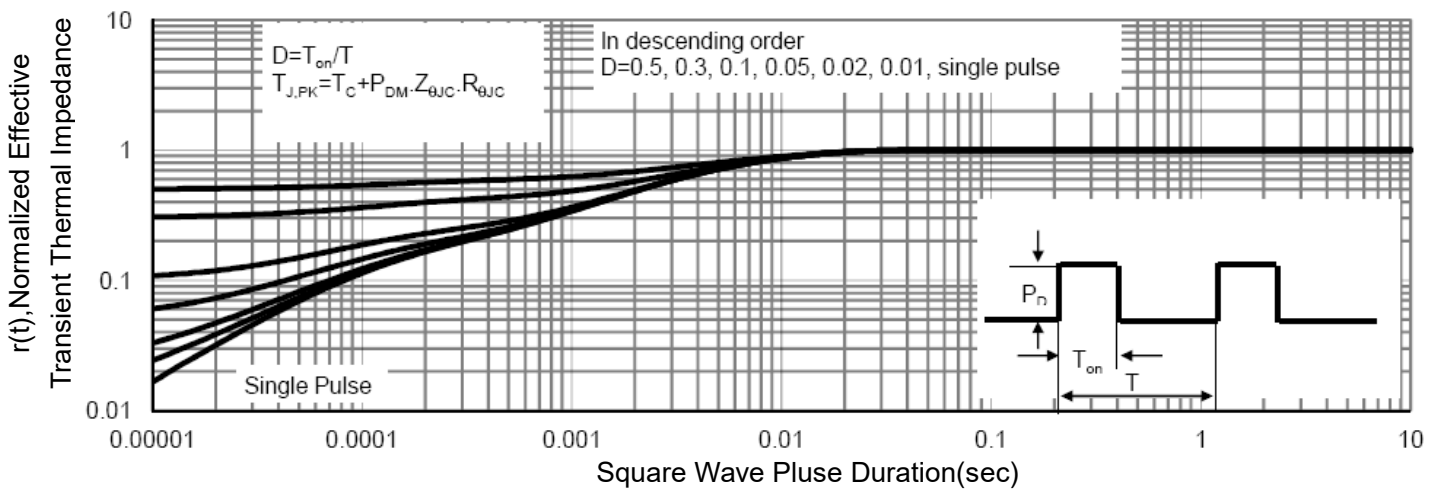
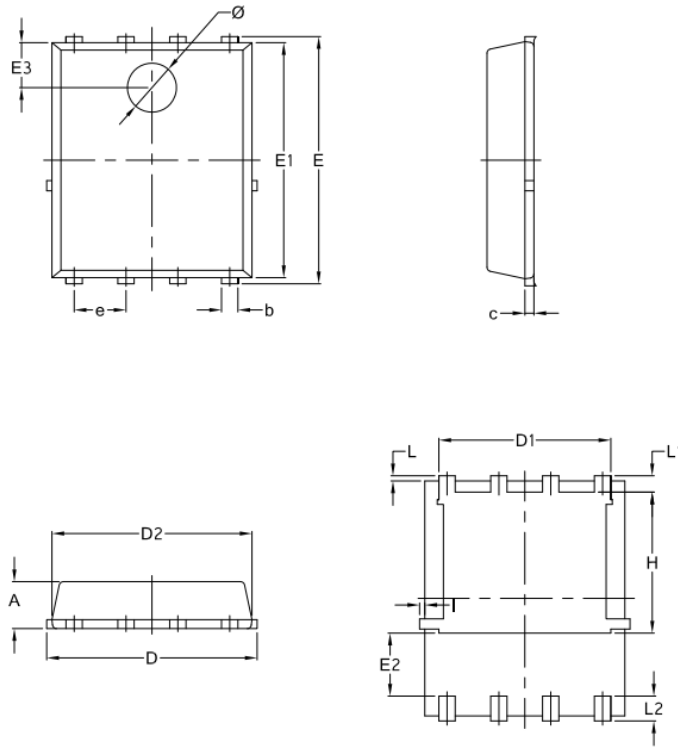


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.15	0.30	0.0059	0.0118
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.40	—	0.0551	—
E3	1.00	1.20	0.0394	0.0472
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.71	0.0150	0.0280
H	3.30	3.50	0.1299	0.1378
I	—	0.18	—	0.0070
Ø	1.10	1.30	0.0433	0.0512

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