

NCE N-Channel Super Trench Power MOSFET

Description

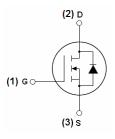
The NCEP02T10T uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

- V_{DS} =200V, I_{D} =100A $R_{DS(ON)}$ <11m Ω @ V_{GS} =10V
- Excellent gate charge x R_{DS(on)} product
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic diagram



TO-247 top view

100% UIS TESTED!

100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP02T10T	NCEP02T10T	TO-247-3L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	200	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	100	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	70.7	Α
Pulsed Drain Current	I _{DM}	400	Α
Maximum Power Dissipation	P _D	300	W
Derating factor		2	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	1216	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2)	R _{eJC}	0.5	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			-			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	200		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =200V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.5		4.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =50A	-	9.5	11	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =50A	70	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -400\/\/ -0\/	-	4200	-	PF
Output Capacitance	C _{oss}	V_{DS} =100V, V_{GS} =0V, F=1.0MHz	-	333.1	-	PF
Reverse Transfer Capacitance	C _{rss}	r=1.0lvln2	-	8.8	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V_{DD} =100V, I_{D} =50A V_{GS} =10V, R_{G} =4.7 Ω	-	18	-	nS
Turn-on Rise Time	t _r		-	26	-	nS
Turn-Off Delay Time	t _{d(off)}		-	41	-	nS
Turn-Off Fall Time	t _f		-	11	-	nS
Total Gate Charge	Qg	\/ -400\/ L -50A	-	63.2		nC
Gate-Source Charge	Q _{gs}	V_{DS} =100V, I_{D} =50A, V_{GS} =10V	-	24		nC
Gate-Drain Charge	Q_{gd}	V _{GS} -10V	-	16.4		nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =100A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	100	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C$, $I_F = 50A$	-	140		nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	600		nC

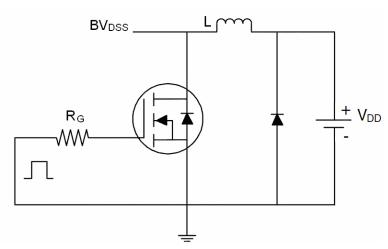
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\!\!\mathrm{C}$,V_DD=50V,V_G=10V,L=0.5mH,Rg=25 Ω

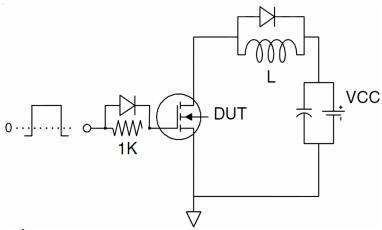


Test Circuit

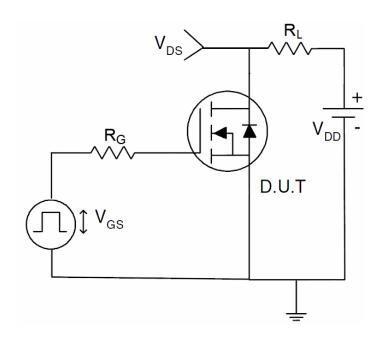
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit







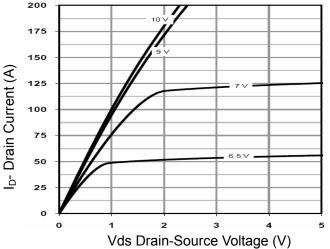


Figure 1 Output Characteristics

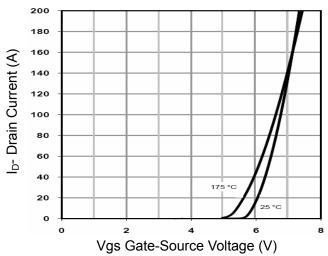


Figure 2 Transfer Characteristics

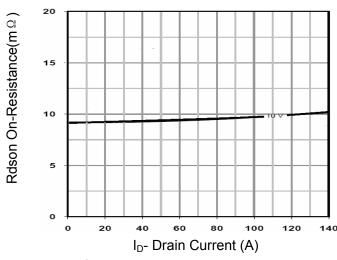


Figure 3 Rdson- Drain Current

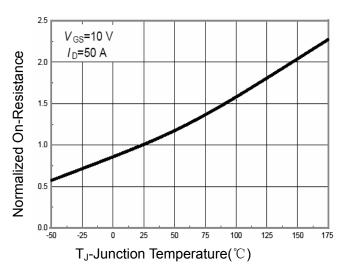


Figure 4 Rdson-JunctionTemperature

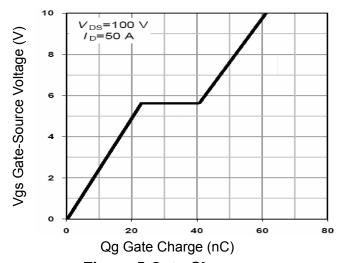


Figure 5 Gate Charge

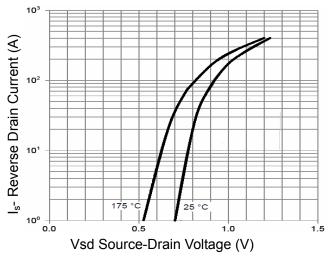
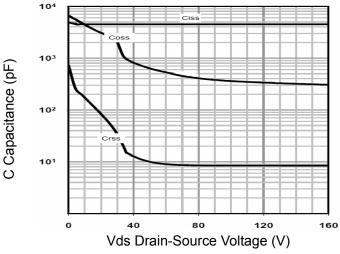
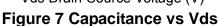
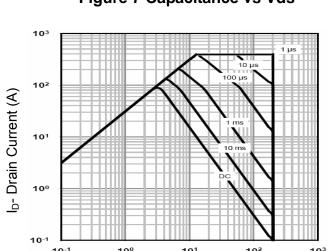


Figure 6 Source- Drain Diode Forward









Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area

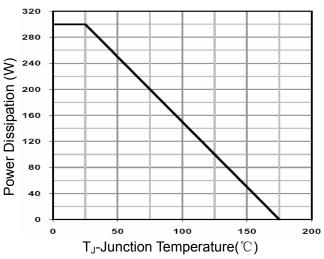


Figure 9 Power De-rating

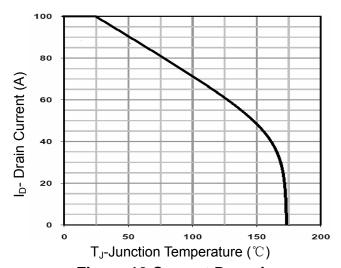


Figure 10 Current De-rating

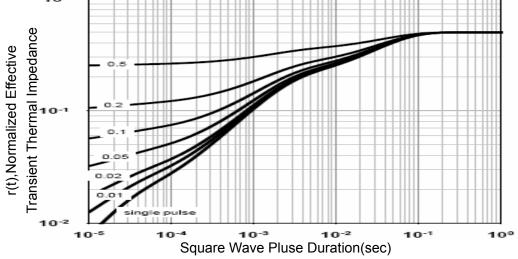
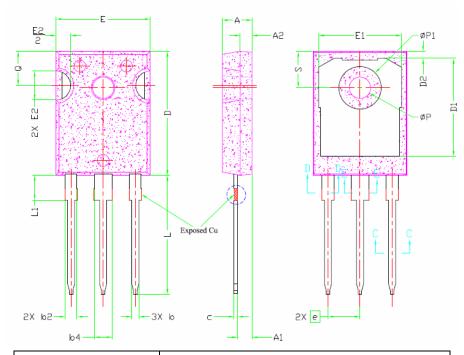


Figure 11 Normalized Maximum Transient Thermal Impedance



TO-247 Package Information



Symbol	Dimensions In Millimeters				
Symbol	Min.	Nom.	Max.		
Α	4.83	5.02	5.21		
A1	2.29	2.41	2.55		
A2	1.50	2.00	2.49		
b	1.12	1.20	1.33		
b1	1.12	1.20	1.28		
b2	1.91	2.00	2.39		
b3	1.91	2.00	2.34		
b4	2.87	3.00	3.22		
b5	2.87	3.00	3.18		
С	0.55	0.60	0.69		
c1	0.55	0.60	0.65		
D	20.80	20.95	21.1		
D1	16.25	16.55	17.65		
D2	0.51	1.19	1.35		
E	15.75	15.94	16.13		
E1	13.46	14.02	14.16		
E2	4.32	4.91	5.49		
е	5.44BSC				
L	19.81	20.07	20.32		
L1	4.10	4.19	4.40		
Q	5.39	5.79	6.20		
ФР	3.56	3.61	3.65		
S	6.04	6.17	6.30		



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