

N-Channel Super Junction Power MOSFET IV

General Description

The series of devices use advanced trench gate super junction technology and design to provide ultra-low RDS(ON) and low gate charge and With a rapid recovery body diode. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, industrial power applications, Fast charger, new energy vehicle charging pile, on-board OBC etc.

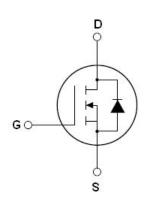
Features

- New technology for high voltage device
- Ultra low on-resistance and ultra low conduction losses
- Ultra Low Gate Charge cause lower driving requirements
- Diode reverse recovery speed is super fast
- High reliability
- ROHS compliant&Halogen Free

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- On-board charger(OBC)

710 V V_{DS min@Tjmax} 110 mΩ RDS(ON)TYP ID 26 А Qg 41 nC



Schematic diagram

Intrinsic fast-recovery body diode ∻

TO-220

GDS

Package Marking And Ordering Information

Device	Device Package	Marking
NCE65NF130	TO-220-3L	NCE65NF130

Table 1. Absolute Maximum Ratings (T_c=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGs=0V)	VDS	650	V
Gate-Source Voltage (VDs=0V) AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (VDs=0V) DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	26	A
Continuous Drain Current at Tc=100°C	I _{D (DC)}	18.2	A
Pulsed drain current ^(Note 1)	I _{DM (pluse)}	78	A
Maximum Power Dissipation(Tc=25°C)	PD	237	W
Derate above 25°C		1.58	W/°C
Avalanche current ^(Note 1)	I _{AS}	7	A
Drain Source voltage slope, $V_{DS} \leqslant 480$ V,	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS} \leqslant 480 V,I_{SD} < I_D$	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55+175	°C

* limited by maximum junction temperature



Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	0.63	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Table 5. Electrical Characteristics	(TA-23 Current wise noted)						
Parameter	Symbol	Condition	Min	Тур	Мах	Unit	
On/off states						-	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250uA	650			V	
Zero Gate Voltage Drain Current(Tc=25°C)	I _{DSS}	V _{DS} =650V,V _{GS} =0V			10	μA	
Zero Gate Voltage Drain Current(Tc=125°C)	I _{DSS}	V _{DS} =650V,V _{GS} =0V			400	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			±100	nA	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =500uA	3.5	4.2	5.0	V	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =13A		110	130	mΩ	
Dynamic Characteristics	- -	·	•				
Input Capacitance	Clss	N/ 50/11/ 01/		2161		pF	
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V,		95		pF	
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz		50		pF	
Total Gate Charge	Qg			41.2		nC	
Gate-Source Charge	Q _{gs}	V _{DS} =480V,I _D =13A,		16.3		nC	
Gate-Drain Charge	Q _{gd}	V _{GS} =10V		12.8		nC	
Gate plateau voltage	Vgp			7.0		V	
Intrinsic gate resistance	R _G	f = 1 MHz open drain		1.5		Ω	
Switching times			•				
Turn-on Delay Time	t _{d(on)}			43		nS	
Turn-on Rise Time	tr	V _{DD} =380V,I _D =13A,		16		nS	
Turn-Off Delay Time	t _{d(off)}	R _G =1.7Ω,V _{GS} =10V		93		nS	
Turn-Off Fall Time	t _f			20		nS	
Source- Drain Diode Characteristics			•				
Source-drain current(Body Diode)	I _{SD}	T _05%0			26	А	
Pulsed Source-drain current(Body Diode)	Isdm	- T _c =25°C			78	Α	
Forward On Voltage	Vsd	Tj=25°C,I _{SD} =26A,V _{GS} =0V		1.0	1.2	V	
Reverse Recovery Time	trr			145		nS	
Reverse Recovery Charge	Qrr	Tj=25°C,I⊧=13A,di/dt=100A/µs		0.725		uC	
Peak Reverse Recovery Current	Irrm			10		Α	

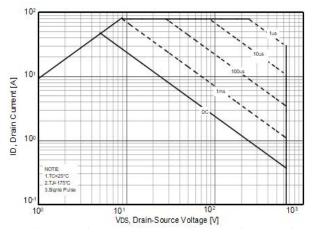
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

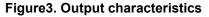
2. Tj=25 $^\circ\!\mathrm{C}$,VDD=50V,VG=10V, R_G=25 Ω

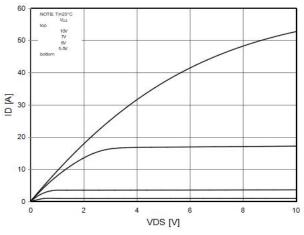


TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure1. Safe operating area









30 40 I_D, Drain Current [A] 50

60

70

Figure5. Static drain-source on resistance

Figure2. Capacitance

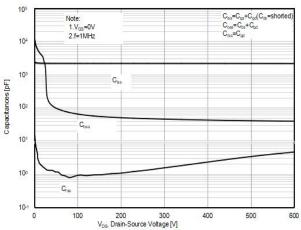
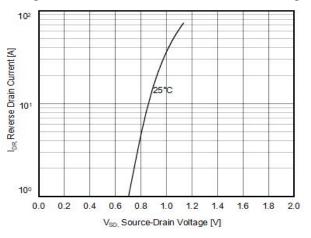
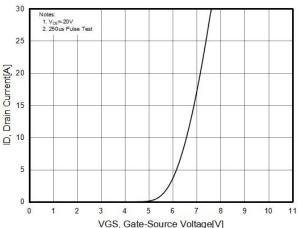


Figure4. Source-Drain Diode Forward Voltage







10

20



Figure7. R_{DS(ON)} vs Junction Temperature

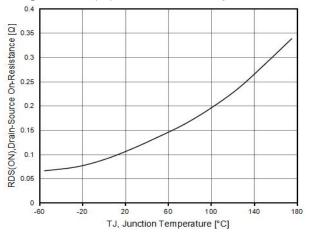


Figure9. Gate charge waveforms

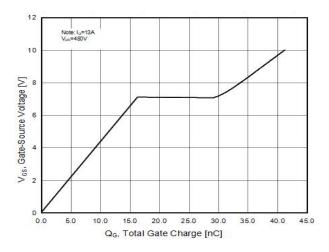


Figure8. BV_{DSS} vs Junction Temperature

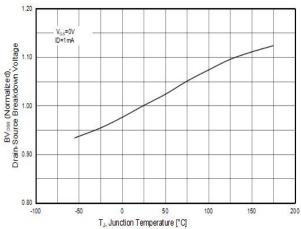
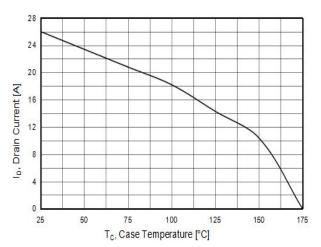


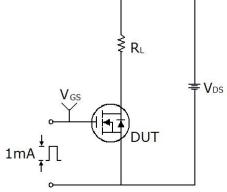
Figure10. Maximum I_D vs Junction Temperature

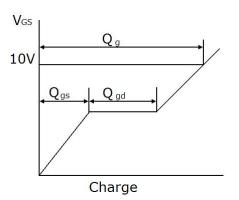




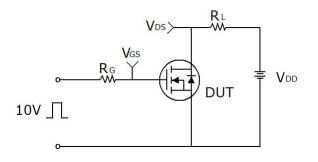
Test circuit

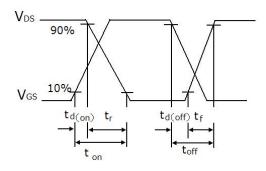
1) Gate charge test circuit & Waveform



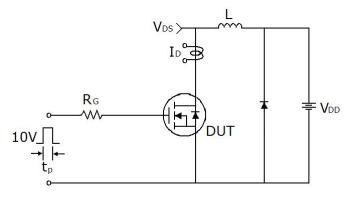


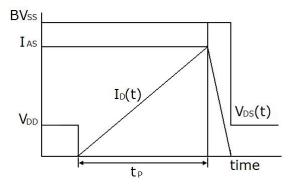
2) Switch Time Test Circuit:





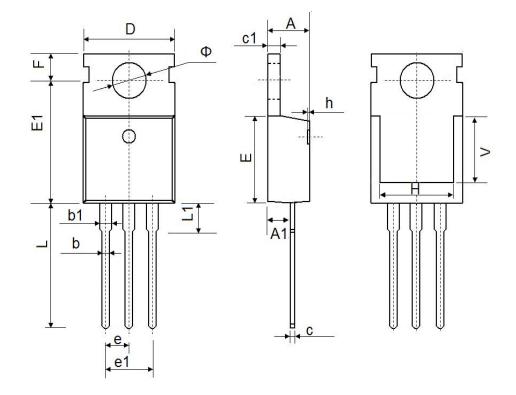
3) Unclamped Inductive Switching Test Circuit & Waveforms







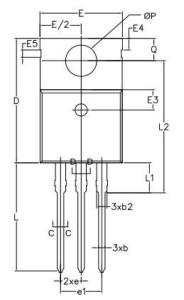
TO-220-3L-E Package Information

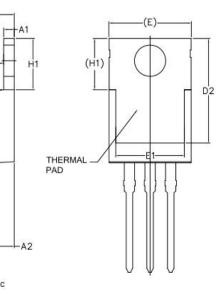


Symbol	Dimensions	Dimensions In Millimeters		s In Inches
Cymbol	Min.	Max.	Min.	Max.
A	4.20	4.60	0.165	0.181
A1	2.25	2.55	0.089	0.100
b	0.70	0.90	0.028	0.035
b1	1.17	1.37	0.046	0.054
С	0.33	0.65	0.013	0.026
c1	1.20	1.40	0.047	0.055
D	8.95	9.75	0.352	0.384
E	9.74	10.04	0.352	0.384
E1	9.91	10.25	0.390	0.404
е	2.54	BSC	0.100)BSC
e1	5.08	BSC	0.200)BSC
Н	15.45	15.85	0.608	0.624
L	12.90	13.40	0.508	0.528
L1	2.85	3.25	0.112	0.128
Φ	3.40	3.80	0.134	0.150



TO-220-3L-J Package Information





O. much al	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.47	4.67	0.176	0.184	
A1	1.20	1.40	0.047	0.055	
A2	2.35	2.90	0.093	0.114	
b	0.71	0.91	0.028	0.036	
b1	0.71	0.86	0.028	0.034	
b2	1.22	1.36	0.048	0.054	
с	0.47	0.60	0.019	0.024	
c1	0.47	0.55	0.019	0.022	
D	14.70	15.80	0.579	0.622	
D1	8.90	9.47	0.350	0.373	
D2	11.75	13.60	0.463	0.535	
E	9.70	10.37	0.382	0.408	
E1	7.00	8.89	0.276	0.350	
E2	9.80	10.20	0.386	0.402	
E3	2.40	2.60	0.094	0.102	
E4	1.27	1.57	0.050	0.062	
е	2.5	4BSC	0.100	BSC	
e1	5.08BSC		0.200	BSC	
Н	3.00	3.40	0.118	0.134	
L	12.90	14.80	0.508	0.583	
L1	2.54	3.84	0.100	0.151	
L2	12.13	16.50	0.478	0.650	
ФР	3.60	3.90	0.142	0.154	
Q	4.60	4.90	0.181	0.193	

D1



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