

## **NCE N-Channel Super Trench Power MOSFET**

#### **Description**

The NCEP02525F uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{\text{DS(ON)}}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

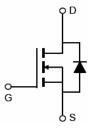
#### **General Features**

- $V_{DS}$  =250V, $I_D$  =25A  $R_{DS(ON)}$ =60m $\Omega$  (typical) @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

#### **Application**

- LED backlighting
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED!



#### Schematic diagram



Marking and pin assignment



TO-220F top view

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP02525F	NCEP02525F	TO-220F	-	-	-

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

About the Maximum Ratings (14-20 Camboo Canol Wice Hotea)					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	250	V		
Gate-Source Voltage	V <sub>GS</sub>	±20	V		
Drain Current-Continuous	I <sub>D</sub>	25	А		
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	17.5	А		
Pulsed Drain Current	I <sub>DM</sub>	100	А		
Maximum Power Dissipation	P <sub>D</sub>	45	W		
Derating factor		0.3	W/℃		
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	320	mJ		
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 175	℃		

#### **Thermal Characteristic**

Thermal Résistance, Junction-to-Case <sup>(Note 2)</sup>	R <sub>eJC</sub>	3.3	°C/W
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Electrical Characteristics (T<sub>A</sub>=25 ℃ unless otherwise noted)

Parameter Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	250	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =250V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2.5	3.5	4.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	60	70	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	15	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	\/ -405\/\/ -0\/	-	1600		PF
Output Capacitance	Coss	$V_{DS}$ =125V, $V_{GS}$ =0V, F=1.0MHz	-	92		PF
Reverse Transfer Capacitance	C <sub>rss</sub>	r=1.0WIn2	-	4.3		PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	7	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =125 $V$ , $R_L$ =7.5 $\Omega$	-	9	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =3 $\Omega$	-	25	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	5	-	nS
Total Gate Charge	Qg	\/ -425\/ L -20A	-	24	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =125V, $I_{D}$ =20A, $V_{GS}$ =10V	-	9.5	-	nC
Gate-Drain Charge	$Q_{gd}$	VGS-10V	-	5.6	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	25	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25^{\circ}C, I_F = I_S$	-	45	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	160	-	nC

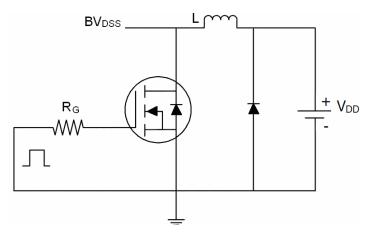
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25  $^{\circ}\text{C}$  ,VDD=50V,VG=10V,L=0.5mH,Rg=25 $\Omega$

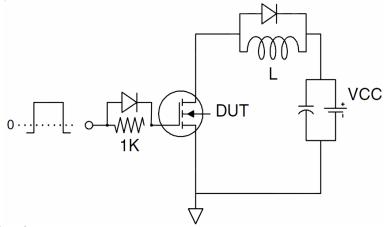


### **Test Circuit**

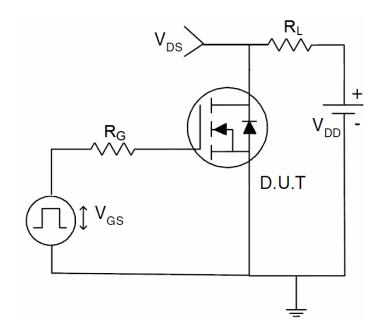
## 1) E<sub>AS</sub> test Circuit



### 2) Gate charge test Circuit

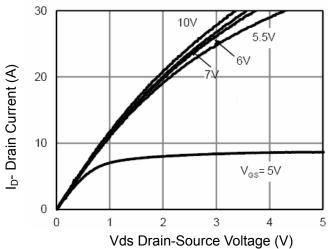


## 3) Switch Time Test Circuit

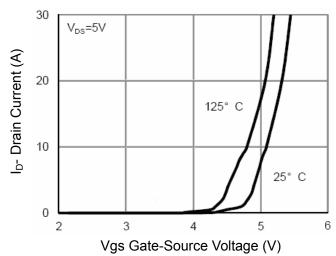




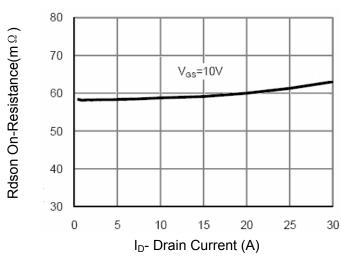
# **Typical Electrical and Thermal Characteristics**



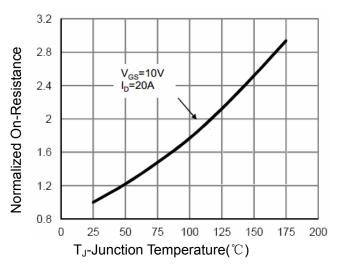
**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 



**Figure 3 Rdson- Drain Current** 



**Figure 4 Rdson-Junction Temperature** 

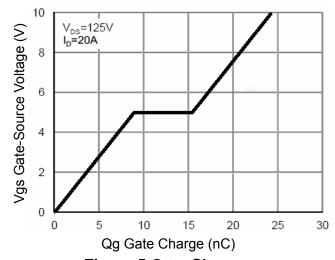


Figure 5 Gate Charge

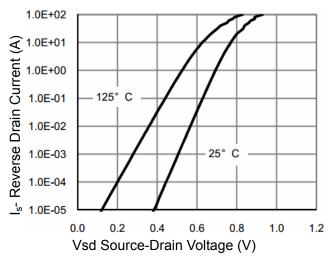
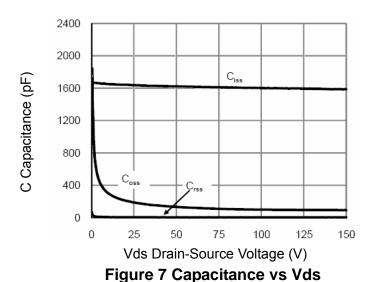
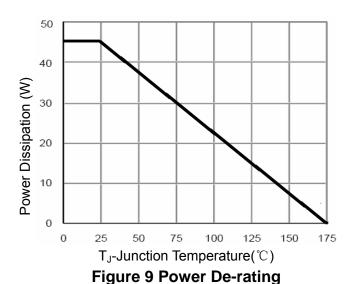


Figure 6 Source- Drain Diode Forward







100.0 10.0 00μs lp- Drain Current (A) R<sub>DS(ON)</sub> limited 1.0 0ms DC 0.1 Γ<sub>J(Max)</sub>=175° T<sub>C</sub>=25° C 0.0 0.01 100 1000 Vds Drain-Source Voltage (V)

20 Lain Current (A) 10 Lain Current (A) 10 Lain Current (B) 10 Lai

**Figure 8 Safe Operation Area** 

 $T_J$ -Junction Temperature (°C) **Figure 10 Current De-rating** 

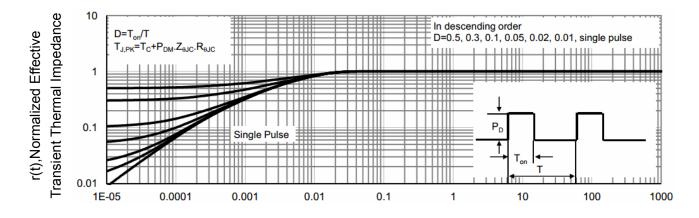
100

125

150

175

75



40

0

0

25

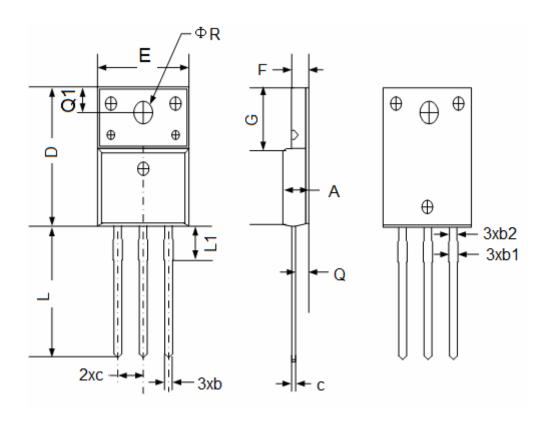
50

Square Wave Pluse Duration(sec)

**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **TO-220F Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches			
Cymbol	Min.	Max.	Min.	Max.		
А	4.50	4.83	0.18	0. 19		
b	0.70	0.91	0.03	0.04		
b1	1.20	1.47	0.05	0.06		
b2	1.10	1.38	0.04	0.05		
С	0.45	0.63	0.02	0.02		
D	15.67	16.07	0.62	0. 63		
е	2.54	2.54 BSC		0.10 BSC		
E	9.96	10.36	0.39	0.41		
F	2.34	2.74	0.09	0. 11		
G	6.48	6.90	0. 26	0. 27		
L	12.68	13.30	0.50	0. 52		
L1	3.13	3.50	0.12	0. 14		
Q	2.56	2.93	0.10	0. 12		
Q1	3.20	3.40	0.13	0. 13		
ФК	3.08	3.28	0. 12	0. 13		



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