

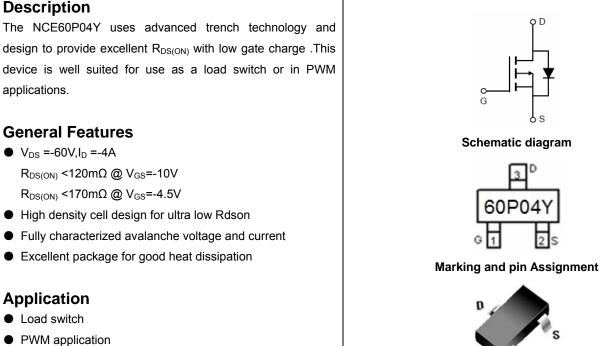


SOT-23-3L top view



NCE P-Channel Enhancement Mode Power MOSFET

Description



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
60P04Y	NCE60P04Y	SOT-23-3L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_c=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	-4	А
Pulsed Drain Current	I _{DM}	-12	А
Maximum Power Dissipation	PD	1.5	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	R _{0JA}	83.3	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	!		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA -60		-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	····					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.5	-2.2	-3.0	V
Drain-Source On-State Resistance	5	V _{GS} =-10V, I _D =-4A	-	106	120	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =-4.5V, I _D =-3A	-	135	170	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-4A	-	10	-	S
Dynamic Characteristics (Note4)	····					
Input Capacitance	C _{lss}		-	930	-	PF
Output Capacitance	C _{oss}	V _{DS} =-30V,V _{GS} =0V, F=1.0MHz	-	85	-	PF
Reverse Transfer Capacitance	C _{rss}		-	35	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	8	-	nS
Turn-on Rise Time	tr	V_{DD} =-30V, RL=7.5 Ω ,	-	4	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V,R _G =3 Ω	-	32	-	nS
Turn-Off Fall Time	t _f		-	7	-	nS
Total Gate Charge	Qg	N/ 001 /A	-	25	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-30,I _D =-4A,	-	3	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-10V	-	7	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-4A	-		-1.2	V
Diode Forward Current (Note 2)	I _S		-	-	-4	А
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =- 4A	-	25		nS
Reverse Recovery Charge	Qrr	di/dt = -100A/µs ^(Note3)	-	31		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

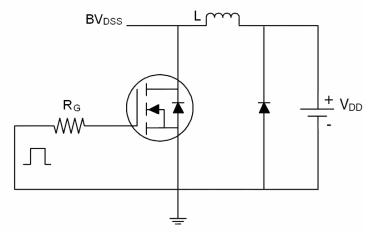


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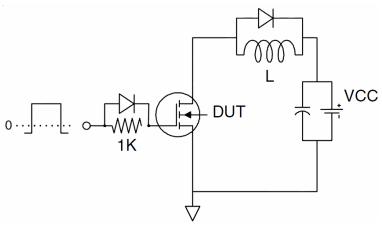
Pb Free Product



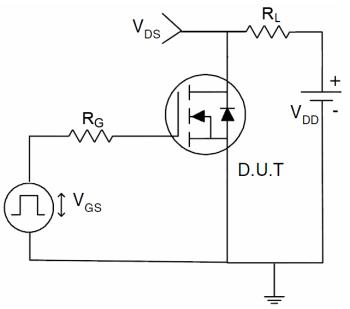
Test Circuit 1) E_{AS} test Circuit



2) Gate charge test Circuit

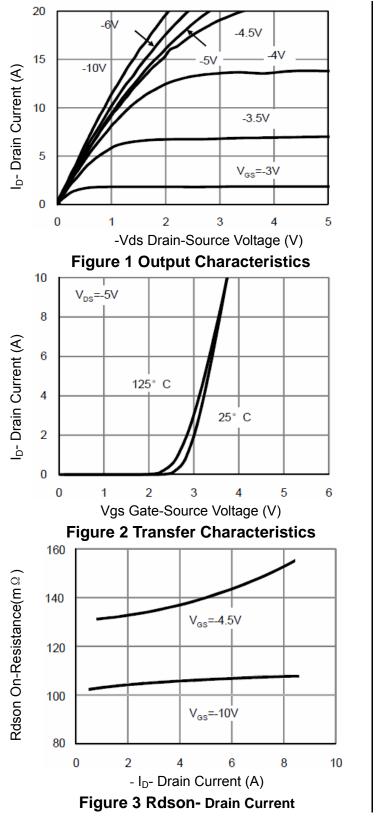


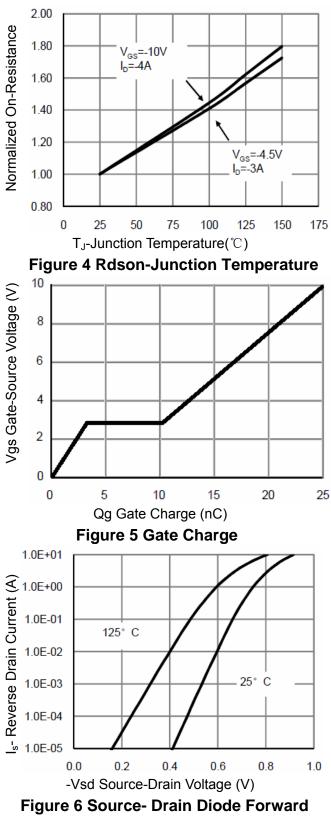
3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)



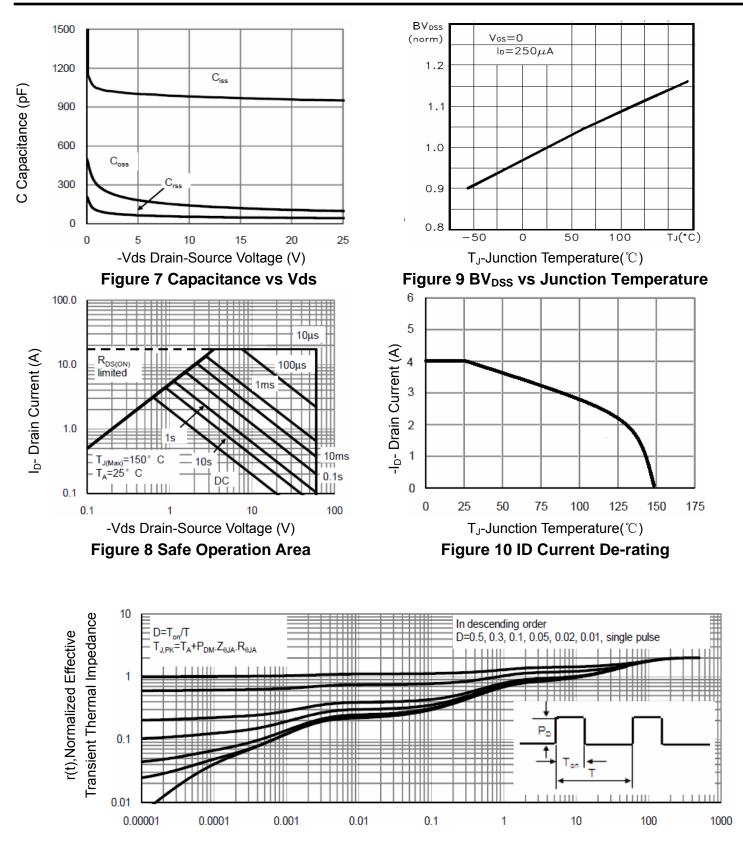




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NCE60P04Y

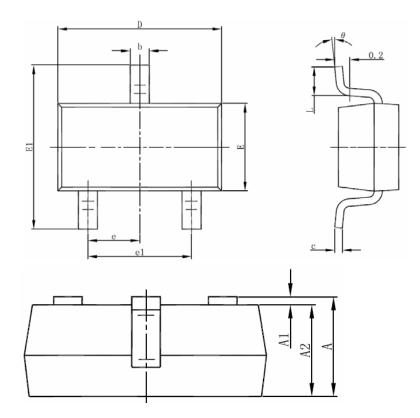


Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance





SOT-23-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
с	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Notes

1. All dimensions are in millimeters.

2. Tolerance ±0.10mm (4 mil) unless otherwise specified

3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.

4. Dimension L is measured in gauge plane.

5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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