

PbFreeProduct

NCE75TD120WT

# 1200V, 75A, Trench FS II Fast IGBT

#### **General Description:**

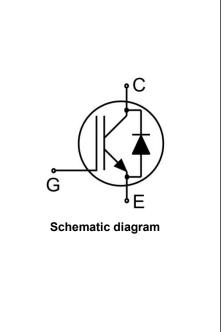
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 1200V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

#### Features

- Trench FSII Technology Offering
- Very low V<sub>CE(sat)</sub>
- High speed switching
- Positive temperature coefficient in V<sub>CE(sat)</sub>
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

#### Application

• Welding



#### Package Marking and Ordering Information

Device	Device Package	Device Marking		
NCE75TD120WT	TO-247	NCE75TD120WT		



TO-247

## Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter Value		Units	
V <sub>CES</sub>	Collector-Emitter Voltage	1200	V	
$V_{\text{GES}}$	Gate- Emitter Voltage	±30	V	
1	Collector Current	150	A	
lc	Collector Current @Tc = 100 °C	75	A	
I <sub>Cpuls</sub>	Pulsed Collector Current, $t_p$ limited by $T_{jmax}$	225	A	
-	turn off safe operating area, $V_{CE}$ =1200V, Tj=150°C	225	A	
IF	Diode Continuous Forward Current @T <sub>c</sub> = 100 °C	75	A	
I <sub>FM</sub>	Diode Maximum Forward Current	225	A	
Р	Power Dissipation @ T <sub>C</sub> = 25°C	833	W	
PD	Power Dissipation @T <sub>c</sub> = 100 °C	417	W	
$T_{J},T_{stg}$	Operating Junction and Storage Temperature Range	-55 to +175	°C	
ΤL	Maximum Temperature for Soldering	260	°C	



NCE75TD120WT

# **Thermal Characteristic**

Symbol	Parameter	Value	Units
R <sub>eJC</sub>	Thermal Resistance, Junction to case for IGBT	0.18	°C/W
R <sub>eJC</sub>	Thermal Resistance, Junction to case for Diode	0.5	°C/W
R <sub>0JA</sub>	Thermal Resistance, Junction to Ambient	40	°C/W

# **Electrical Characteristics (Tc=25°C unless otherwise noted)**

Overale of	Deremeter	Test Canditions		Value			11
Symbol	ymbol Parameter Test Condition		naitions	Min.	Тур.	Max.	Units
Static Chara	cteristics					· · ·	
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> =0V	,I <sub>CE</sub> =1mA	1200			V
I <sub>CES</sub>	Collector-Emitter Leakage Current	V <sub>GE</sub> =0V,	√ <sub>CE</sub> =1200V			5	uA
I <sub>GES(F)</sub>	Gate to Emitter Forward Leakage	V <sub>GE</sub> =+30	V,V <sub>CE</sub> =0V			200	nA
I <sub>GES(R)</sub>	Gate to Source Reverse Leakage	V <sub>GE</sub> =-30V,V <sub>CE</sub> =0V				200	nA
		Ic=75A	Tj=25°C		1.9	2.2	V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	V <sub>GE</sub> =15V	Tj=150°C		2.2		V
$V_{\text{GE(th)}}$	Gate Threshold Voltage	Ic=1mA,Vce=Vge		4.5		6.5	V
Dynamic Cha	aracteristics	·					
Cies	Input Capacitance	— V <sub>CE</sub> =30V,V <sub>GE</sub> =0V, — f=1MHz			13830		pF
Coes	Output Capacitance				320		
Cres	Reverse Transfer Capacitance				280		
Qg	Total Gate Charge	V <sub>CC</sub> =960V, I <sub>C</sub> =75A, V <sub>GE</sub> =15V			450		
Q <sub>ge</sub>	Gate to Emitter Charge				87		nC
Q <sub>gc</sub>	Gate to Collector Charge				204		
Switching C	haracteristics	·					
t <sub>d(ON)</sub>	Turn-on Delay Time				19		
tr	Rise Time				17		
$t_{\text{d}(OFF)}$	Turn-Off Delay Time	V <sub>CE</sub> =600V,I <sub>C</sub> =75A, V <sub>GE</sub> =0/15V, R <sub>g</sub> =8Ω			170		ns
t <sub>f</sub>	Fall Time				18		
Eon	Turn-On Switching Loss	Inducti	ve Load		5.5		
E <sub>off</sub>	Turn-Off Switching Loss	-			2.5		mJ
E <sub>ts</sub>	Total Switching Loss				8.0		

# Electrical Characteristics of the Diode( $T_c$ = 25°C unless otherwise specified):

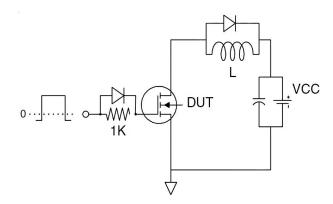
Symbol	Parameter	Test Conditions	Rating			l lusita
			Min.	Тур.	Max.	Units
Vfm	Diode Forward Voltage	I⊧=37.5A		2.2	3.0	V
Trr	Reverse Recovery Time	1 - 27 5 4		150		ns
I <sub>RRM</sub>	Diode Peak Reverse Recovery Current	I <sub>F</sub> =37.5A,		10		А
Qrr	Reverse Recovery Charge	di/dt=700A/us		2.2		uC
Pulse width tt	₅≤380µs,δ≤2%					





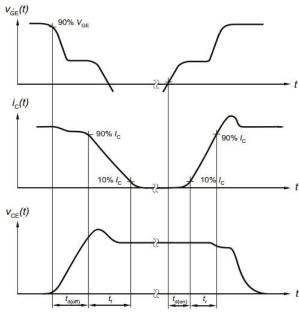
## **Test Circuit**

## 1) Gate Charge Test Circuit

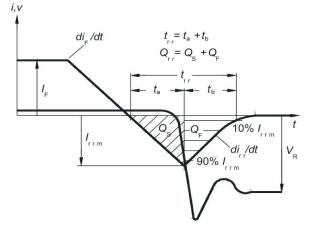


## Switching characteristics

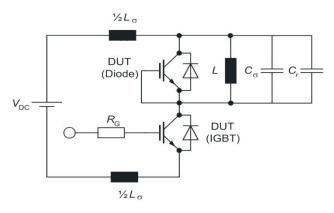
1) Definition of switching times



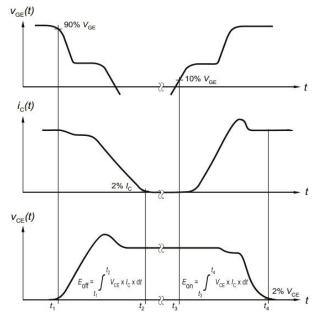
## 3) Definition of diode switching characteristics



## 2) Switch Time Test Circuit

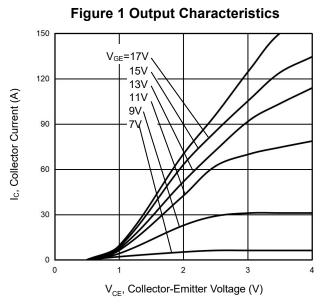


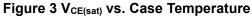
## 2) Definition of switching losses

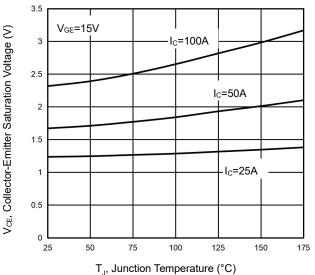




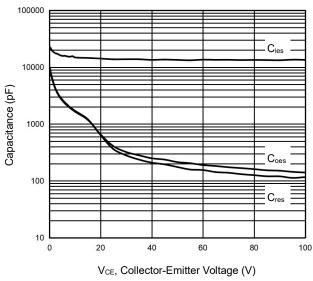
# **Typical Electrical and Thermal Characteristics**

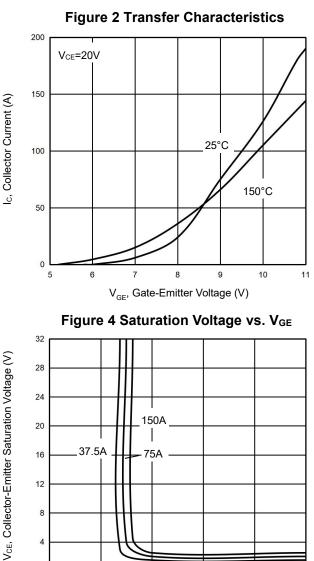


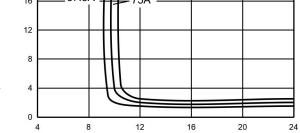




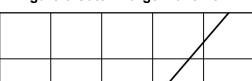


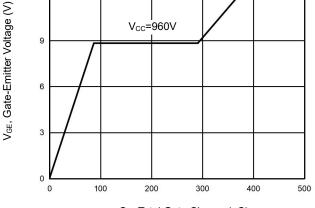






V<sub>GE</sub>, Gate-Emitter Voltage (V) Figure 6 Gate Charge Wave Form





Q<sub>G</sub>, Total Gate Charge (nC)

15

12

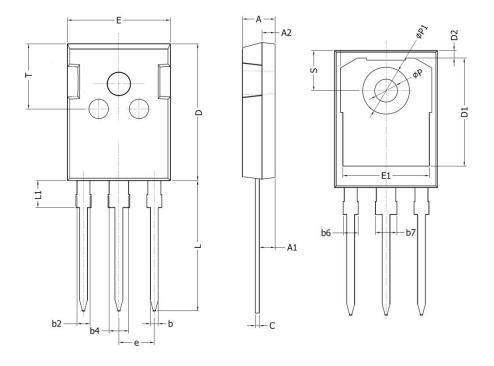
4

Page



PbFreeProduct NCE75TD120WT

# **TO-247 Package Information**



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.90	5.10	0.193	0.201	
A1	2.31	2.51	0.091	0.099	
A2	1.9	2.1	0.075	0.083	
b	1.16	1.26	0.046	0.050	
b2	1.96	2.06	0.077	0.081	
b4	2.96	3.06	0.117	0.120	
b6	-	2.25	-	0.089	
b7	-	3.25	-	0.128	
С	0.59	0.66	0.023	0.026	
D	20.90	21.10	0.823	0.831	
D1	16.25	16.85	0.640	0.663	
D2	1.05	1.35	0.041	0.053	
E	15.70	15.90	0.618	0.626	
E1	13.10	13.50	0.516	0.531	
е	5.436	5.436 BSC		С	
L	19.80	20.10	0.780	0.791	
L1	-	4.30	-	0.169	
Р	3.40	3.60	0.134	0.142	
P1	7.00	7.40	0.276	0.291	
S	6.05	6.25	0.238	0.246	
Т	9.80	10.20	0.386	0.402	



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