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### NCE P-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE40P13S uses advanced trench technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub> =-40V,I<sub>D</sub> =-13A

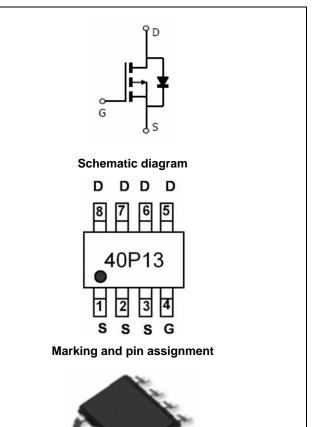
 $R_{DS(ON)}$  <15m $\Omega$  @  $V_{GS}$ =-10V

 $R_{DS(ON)}$  <18m $\Omega$  @  $V_{GS}$ =-4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- DC-DC converter



SOP-8 top view

#### Package Marking and Ordering Information

	<u> </u>				
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
40P13	NCE40P13S	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

5 \ A	,		
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-40	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	-13	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	-9	Α
Pulsed Drain Current	I <sub>DM</sub>	50	Α
Maximum Power Dissipation	P <sub>D</sub>	2.5	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance ,Junction-to-Ambient(Note 2)	R <sub>eJA</sub>	50	°C/W
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# **NCE40P13S**

# Electrical Characteristics ( $T_A$ =25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-1.3	-2	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-12A	-	12	15	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-15V,I <sub>D</sub> =-10A	35	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	V - 20VVV -0V	-	2800	-	PF
Output Capacitance	Coss	$V_{DS}$ =-20V, $V_{GS}$ =0V, F=1.0MHz	-	320	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	r=1.0MHZ	-	220	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	11	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-20 $V$ , $R_L$ =2 $\Omega$	-	75	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =6 $\Omega$	-	89	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	35	-	nS
Total Gate Charge	$Q_g$	\/ - 20\/   - 424	-	40	-	nC
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =-20V, $I_{D}$ =-12A, $V_{GS}$ =-10V	-	6	-	nC
Gate-Drain Charge	$Q_{gd}$	v <sub>GS</sub> 10 v	-	12	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-12A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	-13	Α

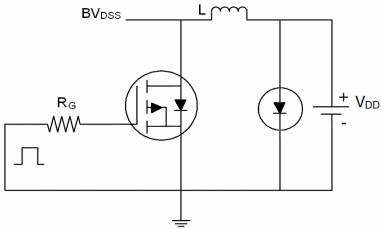
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production

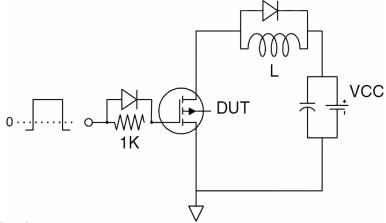


# **Test Circuit**

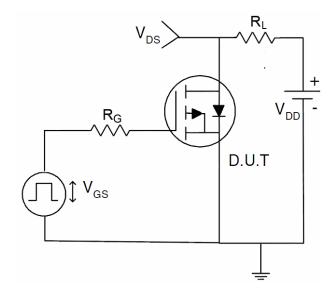
# 1) E<sub>AS</sub> Test Circuit



# 2) Gate Charge Test Circuit



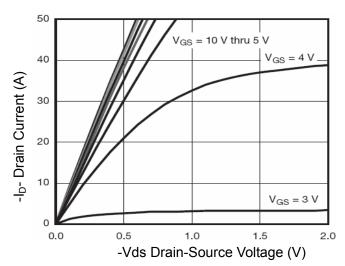
# 3) Switch Time Test Circuit



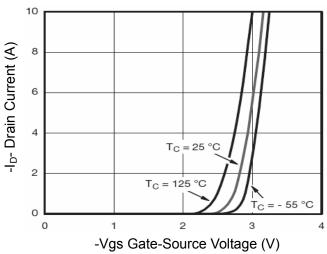
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# NCE40P13S

# Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

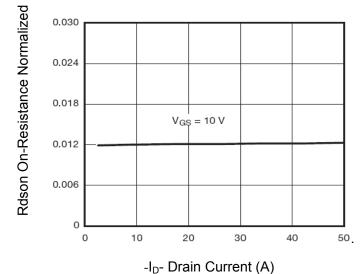
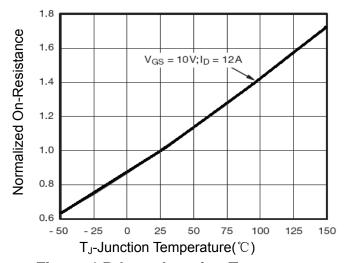
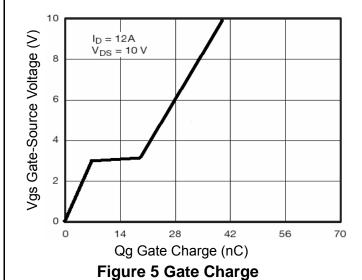


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 



100 Is- Reverse Drain Current (A) T<sub>J</sub> = 150 °C T<sub>J</sub> = 25 °C 0.1 0.5 Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward



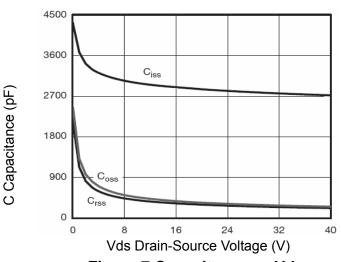


Figure 7 Capacitance vs Vds

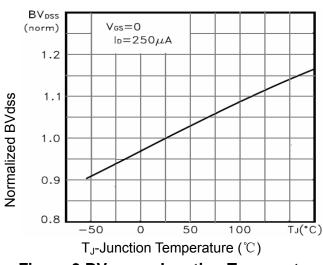
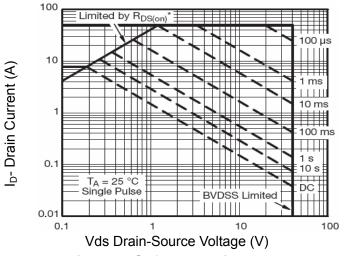


Figure 9 BV<sub>DSS</sub> vs Junction Temperature



**Figure 8 Safe Operation Area** 

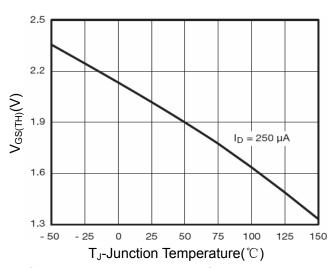


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

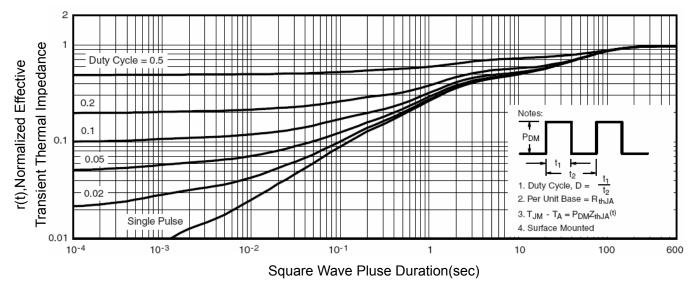
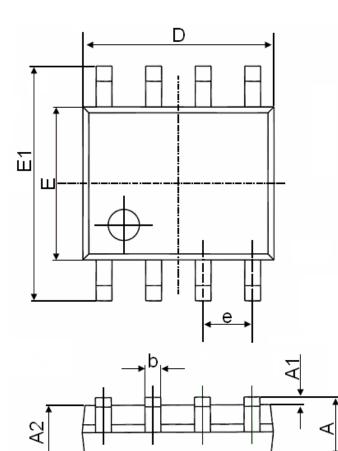


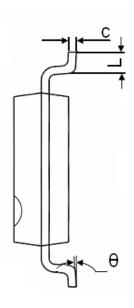
Figure 11 Normalized Maximum Transient Thermal Impedance

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# NCE40P13S

# **SOP-8 Package Information**





Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

# NCE40P13S

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