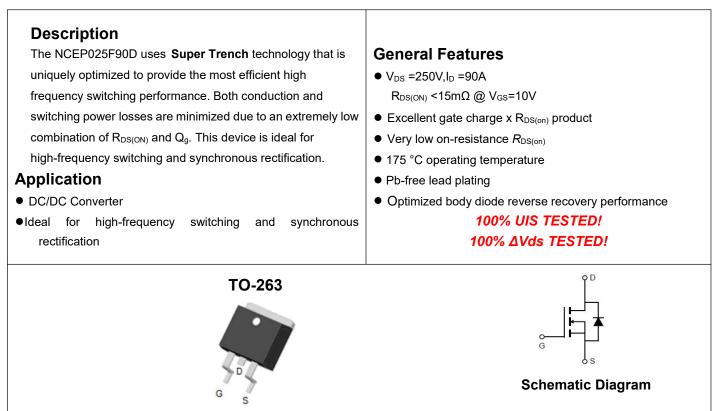


NCE N-Channel Super Trench Power MOSFET



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP025F90D	NCEP025F90D	TO-263-2L	Ø330mm	24mm	800units

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	250	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	ID	90	А
Drain Current-Continuous(T _C =100 ℃)	I _D (100℃)	63.6	А
Pulsed Drain Current	Ідм	360	А
Maximum Power Dissipation	PD	330	W
Derating factor		2.2	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	1700	mJ
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 175	°C

Thermal Characteristic

	Thermal Resistance, Junction-to-Case ^(Note 2)	R _{eJC}	0.45	°C/W
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Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	I		1			•
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	250		-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =250V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			ľ			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.5		4.5	V
Drain-Source On-State Resistance	Rds(on)	V _{GS} =10V, I _D =45A	-	14	15	mΩ
Gate resistance	Rg		-	3.3	-	Ω
Forward Transconductance	G FS	V _{DS} =10V,I _D =45A	70	-	-	S
Dynamic Characteristics (Note4)	·		·			
Input Capacitance	Clss	V _{DS} =125V,V _{GS} =0V,	-	6595	-	PF
Output Capacitance	Coss	v _{DS} ≕125V,v _{GS} =0V, F=1.0MHz	-	409.5	-	PF
Reverse Transfer Capacitance	Crss		-	11	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	19.5	-	nS
Turn-on Rise Time	tr	V _{DD} =125V,I _D =45A	-	28	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =4.7 Ω	-	48	-	nS
Turn-Off Fall Time	t _f		-	15	-	nS
Total Gate Charge	Qg	V _{DS} =125V,I _D =45A,	-	90.9		nC
Gate-Source Charge	Q _{gs}		-	40.4		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	18		nC
Drain-Source Diode Characteristics	I					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =90A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	90	A
Reverse Recovery Time	trr	$T_J = 25^{\circ}C, I_F = 45A$	-	186		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	1.35		uC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t \leq 10 sec.

3. Pulse Test: Pulse Width ≤ 300 μ s, Duty Cycle ≤ 2%.

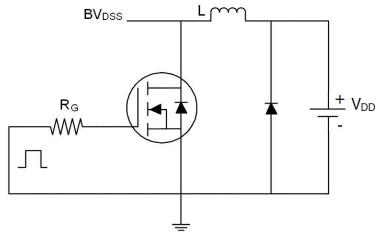
4. Guaranteed by design, not subject to production

5. EAS condition : Tj=25 $^\circ \!\! \mathbb{C}$,V_{DD}=50V,V_G=10V,L=0.5mH,Rg=25\Omega

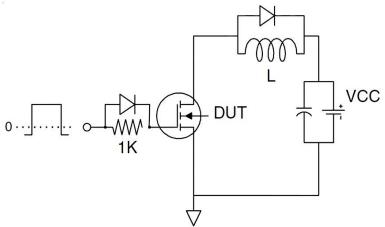


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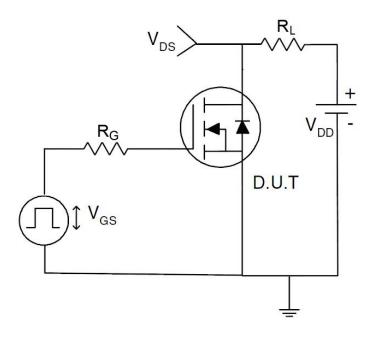
Test Circuit 1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit

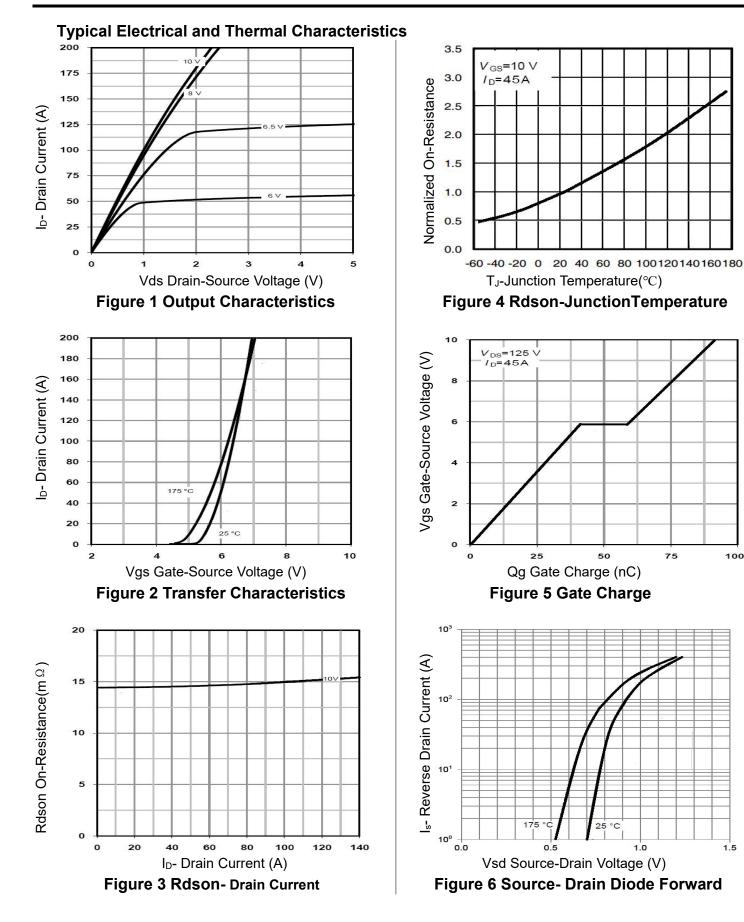




75

1.0

100

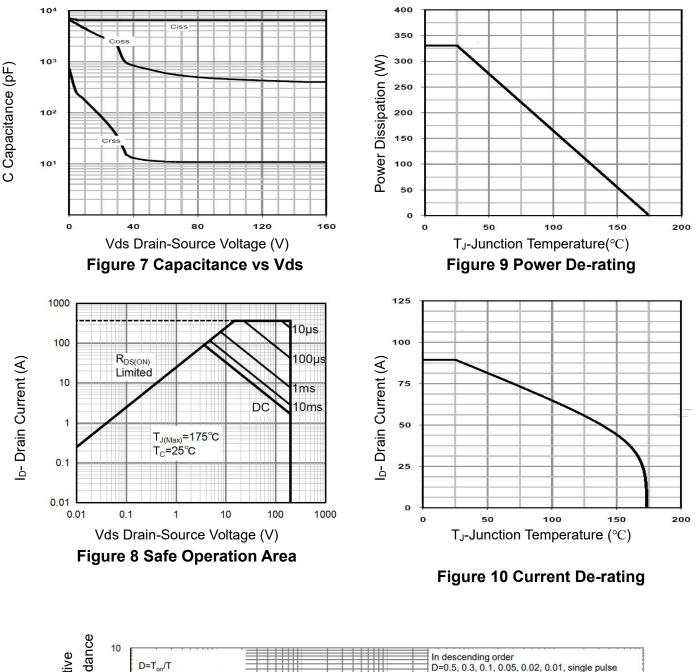


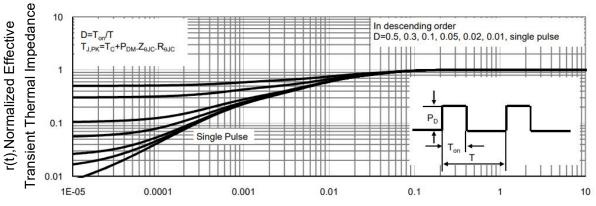
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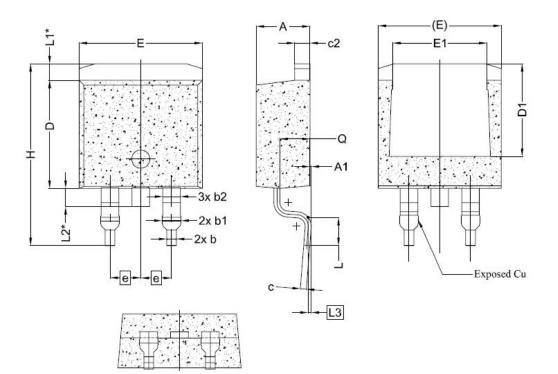


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



TO-263-2L Package Information



SYMBOL	ſ	DIMENSION	S	
SYMBOL	MIN,	NOM.	MAX.	
А	4.24	4.44	4.64	
A1	0.00	0.10	0.15	
b	0.70	0.80	0.90	
b1	1.20	1.55	1,75	
b2	1,20	1,45	1,70	
с	0.40	0.50	0.60	
c2	1,15	1,27	1,40	
D	8.82	8.92	9.02	
D1	6,86	7.65	2) <u>1929</u>	
E	9,96	10,16	10,36	
E1	6.89	7.77	7,89	
e	2,54 BSC			
н	14,61	15,00	15,88	
L	1.78	2.32	2.79	
L1	1.36 REF.			
L2	1.50 REF.			
L3	0.25 BSC			
Q	2,30	2,48	2,70	



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