

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE0103Y uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

• $V_{DS} = 100V, I_{D} = 3A$

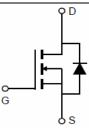
 $R_{DS(ON)}\,{<}160m\Omega\,\,\textcircled{@}\,\,V_{GS}{=}10V\quad (Typ:136m\Omega)$

 $R_{DS(ON)}$ <170m Ω @ V_{GS} =4.5V (Typ:140m Ω)

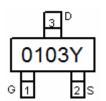
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



Marking and pin assignment



SOT-23-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
0103Y	NCE0103Y	SOT-23-3L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	3	Α
Drain Current-Pulsed (Note 1)	I _{DM}	20	Α
Maximum Power Dissipation	P _D	1.5	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}\mathbb{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	100	°C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0VI_{D}=250\mu A$		-	-	٧
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA

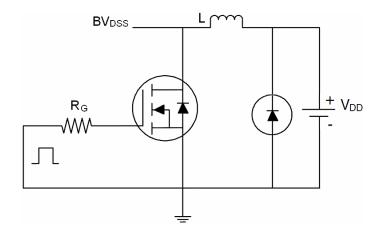
Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)			•				
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1.0	1.5	2.0	V	
Desir Course On Ctata Resistance	-	V _{GS} =10V, I _D =3A	-	136	160	0	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =3A	-	140	170	mΩ	
Forward Transconductance	g FS	V_{DS} =5 V , I_{D} =3 A	-	5	-	S	
Dynamic Characteristics (Note4)			•				
Input Capacitance	C _{lss}	\/ -50\/\/ -0\/	-	650	-	PF	
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V, F=1.0MHz	-	24	-	PF	
Reverse Transfer Capacitance	C _{rss}	F=1.0IVIH2	-	20	-	PF	
Switching Characteristics (Note 4)			•				
Turn-on Delay Time	t _{d(on)}		-	6	-	nS	
Turn-on Rise Time	t _r	V_{DD} =50V, R_L =19 Ω	-	4	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	20	-	nS	
Turn-Off Fall Time	t _f		-	4	-	nS	
Total Gate Charge	Qg	\/ F0\/ L 0A	-	20		nC	
Gate-Source Charge	Q _{gs}	$V_{DS}=50V,I_{D}=3A,$	-	2.1	-	nC	
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	3.3	-	nC	
Drain-Source Diode Characteristics			•	•		•	
Diode Forward Voltage (Note 3)	V _{SD}	V_{GS} =0 V , I_{S} =3 A	-	-	1.2	V	
Diode Forward Current (Note 2)	I _S		-	-	3	Α	

Notes:

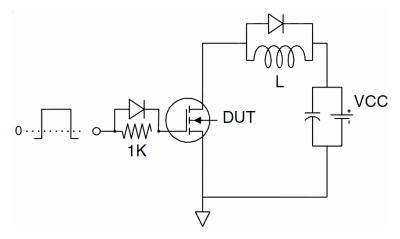
- **1.** Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to productio

Test Circuit

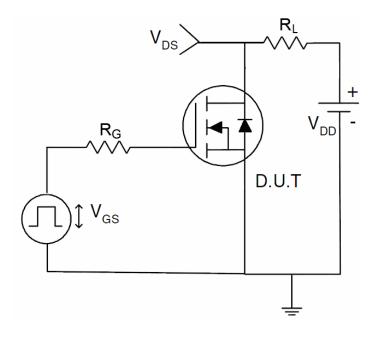
1) E_{AS} test circuit



2) Gate charge test circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

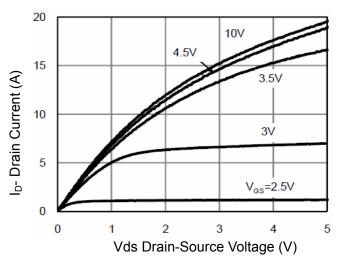


Figure 1 Output Characteristics

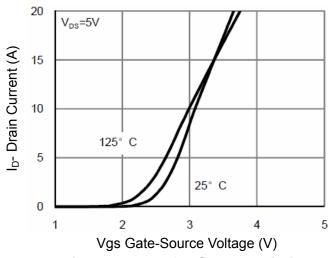


Figure 2 Transfer Characteristics

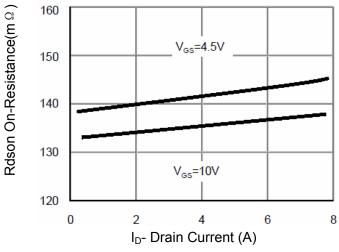


Figure 3 Rdson- Drain Current

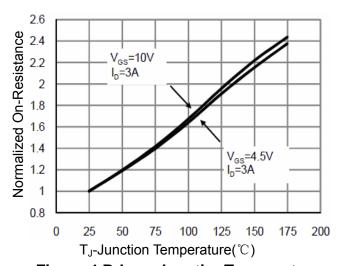


Figure 4 Rdson-JunctionTemperature

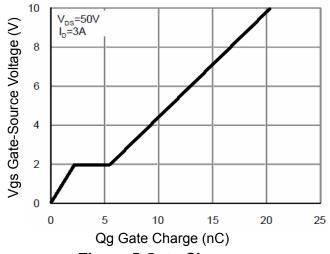


Figure 5 Gate Charge

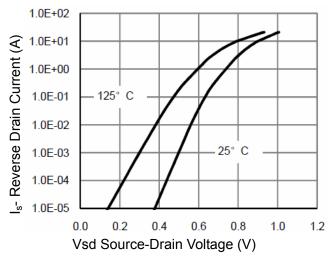


Figure 6 Source- Drain Diode Forward

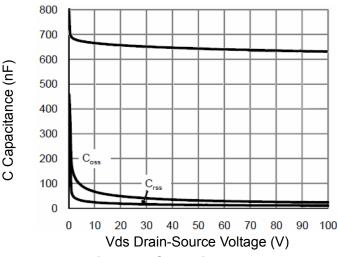


Figure 7 Capacitance vs Vds

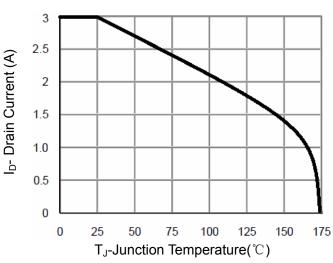


Figure 9 BV_{DSS} vs Junction Temperature

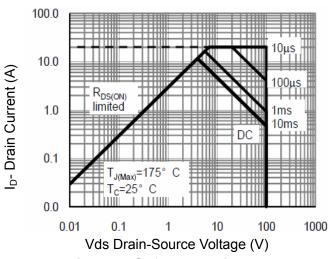


Figure 8 Safe Operation Area

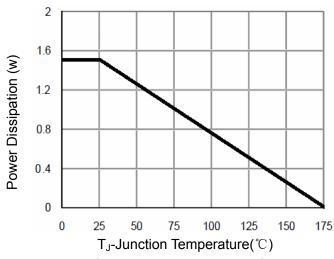


Figure 10 Power De-rating

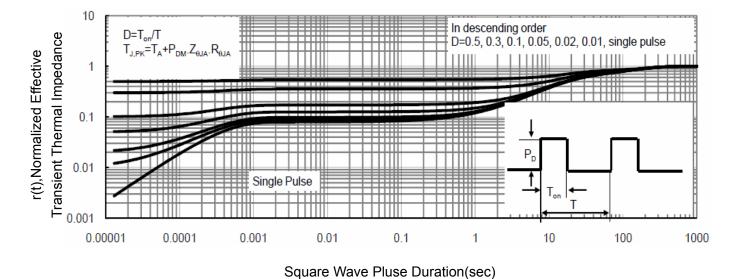
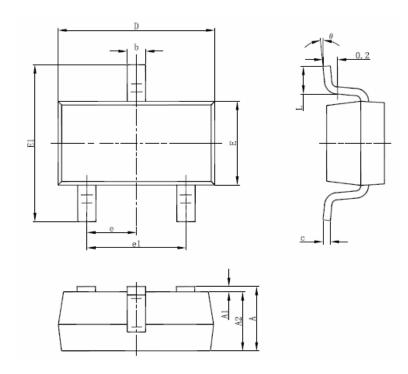


Figure 11 Normalized Maximum Transient Thermal Impedance

SOT-23-3L Package Information



Symbol	Dimensions Ir	Millimeters	Dimensions	In Inches	
Symbol	Min Max		Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	(BSC)	0.037(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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