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Schematic diagram

Pin Assignment

NCE20P70G

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DFN5x6 -8L top view

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NCE P-Channel Enhancement Mode Power MOSFET



The NCE20P70G uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

• $V_{DS} = -20V, I_D = -70A$ $R_{DS(ON)} < 3m\Omega @ V_{GS} = -4.5V$ $R_{DS(ON)} < 4m\Omega @ V_{GS} = -2.5V$ $R_{DS(ON)} < 8m\Omega @ V_{GS} = -1.8V$

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Load switch
- Battery protection

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE20P70G	NCE20P70G	DFN 5x6 -8L	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-20	V
Gate-Source Voltage	V _{GS}	±10	V
Drain Current-Continuous	I _D	-70	А
Drain Current-Continuous(Tc=100℃)	I _D (100℃)	-49.5	A
Pulsed Drain Current ^(Note 1)	I _{DM}	-280	A
Maximum Power Dissipation	PD	130	W
Derating factor		1.04	W/°C
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{eJC}	0.96	°C /W



Electrical Characteristics (T_c=25 $^\circ\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·	·				
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-0.4	-0.6	-1.0	V
		V _{GS} =-4.5V, I _D =-20A	-	2.3	3	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-2.5V, I _D =-20A	-	2.8	4	
		V _{GS} =-1.8V, I _D =-20A		3.8	8	
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-20A	100	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}	V _{DS} =-10V,V _{GS} =0V,	-	4950	-	PF
Output Capacitance	Coss		-	380	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	290	-	PF
Switching Characteristics (Note 4)	·	·				
Turn-on Delay Time	t _{d(on)}		-	20	-	nS
Turn-on Rise Time	tr	V _{DD} =-10V, R _{GEN} =3Ω V _{GS} =-4.5V,R _L =0.5Ω	-	50	-	nS
Turn-Off Delay Time	t _{d(off)}		-	100	-	nS
Turn-Off Fall Time	t _f		-	40	-	nS
Total Gate Charge	Qg	V 40V/L 00A	-	100	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-10V,I _D =-20A, V _{GS} =-4.5V	-	21	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-4.5V	-	32	-	nC
Drain-Source Diode Characteristics		·				
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-20A	-	-	-1.2	V
Diode Forward Current (Note 2)	I _S		-	-	-70	А
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = -10A	-	48	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	55	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				y LS+LD)

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

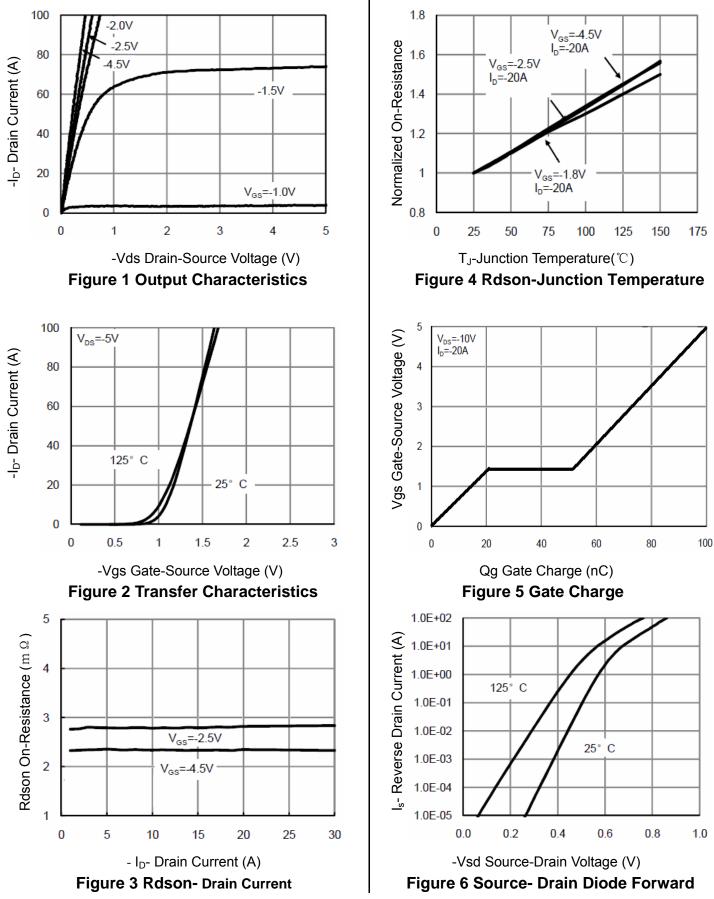
2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production



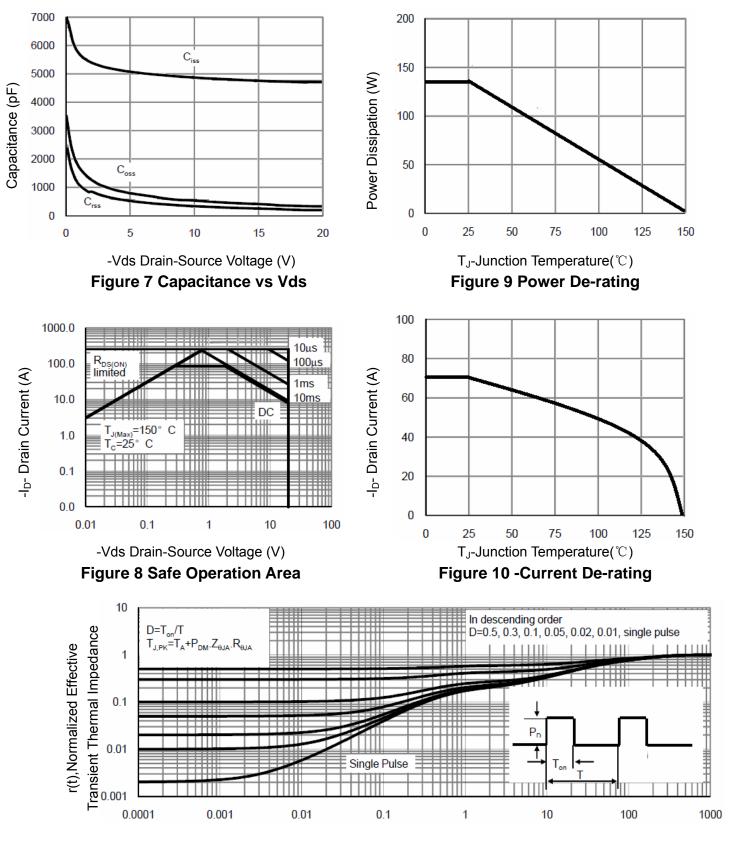
Typical Electrical and Thermal Characteristics (Curves)





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NCE20P70G

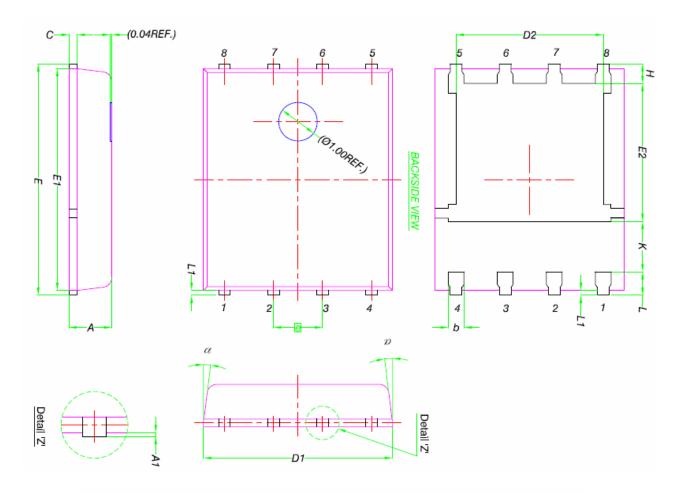


Square Wave Pluse Duration(sec)

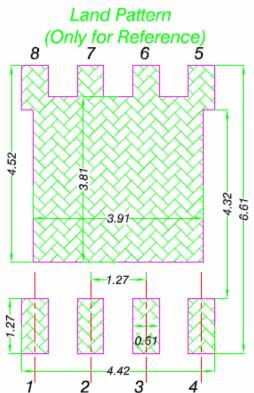
Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



	MILLIMETERS			
DIM.	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
A1	0	-	0.05	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Е	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	1.27 BSC			
Н	0.41	0.51	0.61	
к	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
α	0°	-	12°	





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