

NCE N-Channel Enhancement Mode Power MOSFET

Description

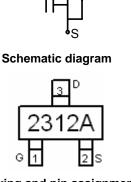
The NCE2312A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

- V_{DS} = 20V,I_D = 5A
 - $R_{DS(ON)} < 35m\Omega @ V_{GS}=2.5V$
 - $R_{DS(ON)} < 28m\Omega @ V_{GS}=4.5V$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management



Marking and pin assignment



Package Marking and Ordering Information

	J	J			
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2312A	NCE2312A	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	20	V
Gate-Source Voltage	Vgs	±10	V
Drain Current-Continuous	I _D	5	A
Drain Current-Pulsed (Note 1)	I _{DM}	15	A
Maximum Power Dissipation	PD	1.25	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

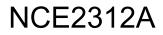
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ extsf{ heta}JA}$	100	°C/W

Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V Ι _D =250μΑ	20	22	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	-	1	μA



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Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	ł					•
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.5	0.65	1.0	V
Drain Courses On State Desistence	R _{DS(ON)}	V _{GS} =2.5V, I _D =4.5 A	-	20	35	mΩ
Drain-Source On-State Resistance		V _{GS} =4.5V, I _D =5A	-	17	28	mΩ
Forward Transconductance	g fs	V _{DS} =15V,I _D =5A	25	-	-	S
Dynamic Characteristics (Note4)	·			•		•
Input Capacitance	Clss		-	780	-	PF
Output Capacitance	Coss	- V _{DS} =10V,V _{GS} =0V, F=1.0MHz	-	140	-	PF
Reverse Transfer Capacitance	C _{rss}		-	80	-	PF
Switching Characteristics (Note 4)	·			•		•
Turn-on Delay Time	t _{d(on)}		-	9	-	nS
Turn-on Rise Time	tr	V _{DD} =10V,I _D =1A	-	30	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5V, R_{GEN} =6 Ω	-	35	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg		-	11	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V,I _D =5A,V _{GS} =4.5V	-	2.3	-	nC
Gate-Drain Charge	Q _{gd}	1	-	2.9	-	nC
Drain-Source Diode Characteristics		•				
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	5	А

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t \leq 10 sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production



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Typical Electrical and Thermal Characteristics

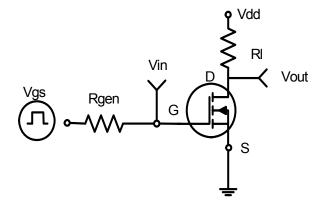
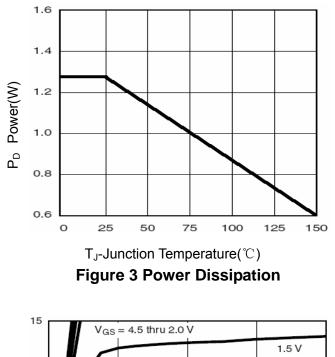
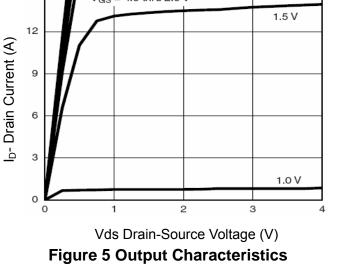
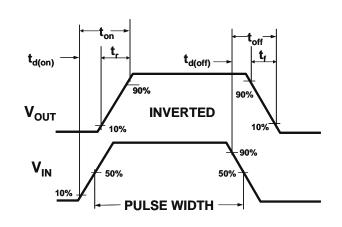


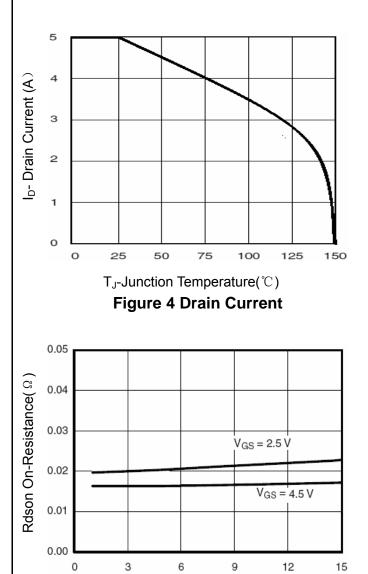
Figure 1:Switching Test Circuit











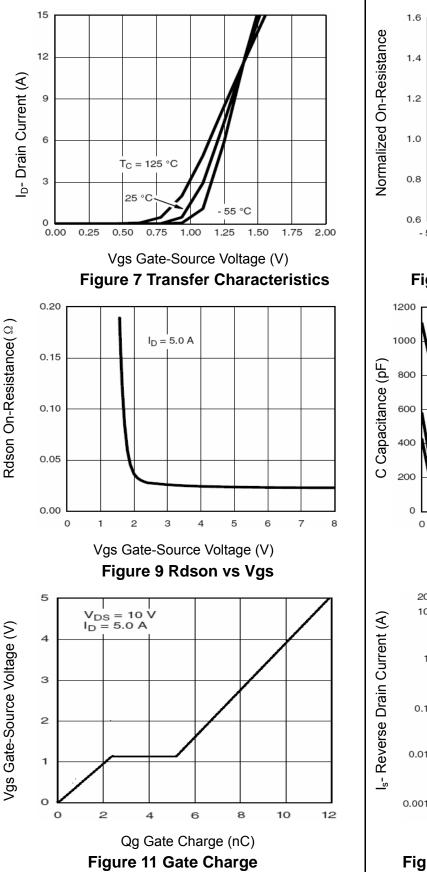
I_D- Drain Current (A) Figure 6 Drain-Source On-Resistance

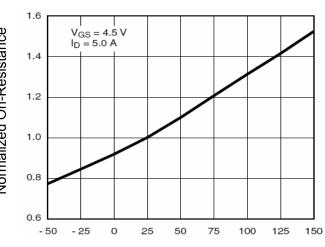


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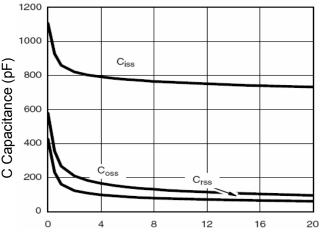
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T_J-Junction Temperature(℃)





Vds Drain-Source Voltage (V) Figure 10 Capacitance vs Vds

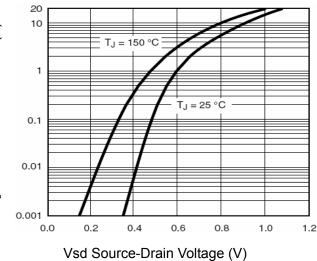
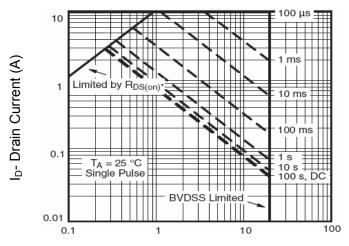


Figure 12 Source- Drain Diode Forward







Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

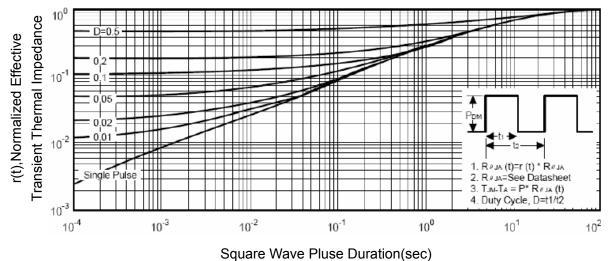
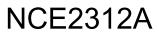


Figure 14 Normalized Maximum Transient Thermal Impedance

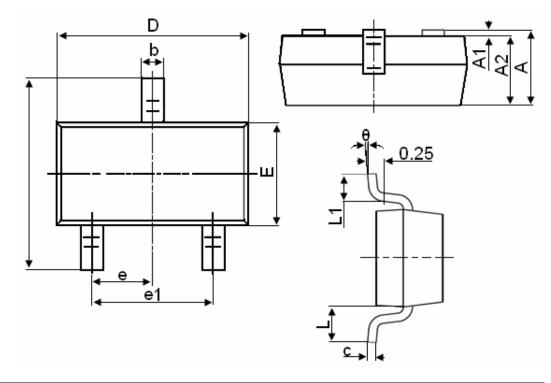


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SOT-23 Package Information



Symbol	Dimensions in Millimeters				
Symbol	MIN.	MAX.			
А	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
с	0.080	0.150			
D	2.800	3.000			
E	1.200	1.400			
E1	2.250	2.550			
е		0.950TYP			
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	0°	8°			

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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