

NCE30TD60BP

600V, 30A, Trench FS II Fast IGBT

General Description:

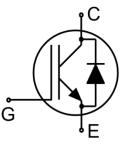
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 600V Trench FS II IGBT offers superior conduction and switching performances, and easy parallel operation;

Features

- Trench FSII Technology offering
- Very low V_{CE(sat)}
- High speed switching
- Positive temperature coefficient in V_{CE(sat)}
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

Application

- Air Condition
- Inverters
- Motor drives



Schematic diagram

Package Marking and Ordering Information

Device	Device Package	Device Marking
NCE30TD60BP	TO-3P	NCE30TD60BP



Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

TO-3P

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	600	V
V _{GES}	Gate- Emitter Voltage	±30	V
	Collector Current	60	Α
Ic	Collector Current @T _C = 100°C	30	Α
I _{Cplus}	Pulsed Collector Current, t _p limited by T _{jmax}	90	Α
-	turn off safe operating area, V _{CE} =600V, Tj=150°C	90	Α
I _F	Diode Continuous Forward Current @T _C = 100°C	30	Α
I _{FM}	Diode Maximum Forward Current	90	Α
	Power Dissipation @ T _C = 25°C	190	W
P _D	Power Dissipation @T _C = 100 °C	95	W
T _J ,T _{stg}	Operating Junction and Storage Temperature Range	-55 to +175	°C
TL	Maximum Temperature for Soldering	260	°C
t _{sc}	Short circuit withstand time V _{GE} =15V, V _{CC} ≤400V, Allowed number of short circuits<1000Time between short circuits:≥1.0s,Tj≤150°C	5	us





Thermal Characteristic

Symbol	Parameter	Value	Units
Rejc	Thermal Resistance, Junction to case for IGBT	0.78	°C/W
Rejc	Thermal Resistance, Junction to case for Diode	1.08	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics (Tc=25°C unless otherwise noted)

0		Test Conditions		Value			
Symbol	Parameter			Min.	Тур.	Max.	Units
Static Chara	cteristics				•	· ·	
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	V _{GE} =0V	,I _{CE} =1mA	600			V
Ices	Collector-Emitter Leakage Current	V _{GE} =0V	V _{CE} =600V			4	uA
I _{GES(F)}	Gate to Emitter Forward Leakage	V _{GE} =+30	V,V _{CE} =0V			200	nA
I _{GES(R)}	Gate to Source Reverse Leakage	V _{GE} =-30	V,Vce =0V			200	nA
V	O-ll-stan Freittan O-tombian Valtana	Ic=30A	Tj=25°C		1.7	1.9	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{\text{GE}}=15V$	Tj=150°C		1.9		V
$V_{GE(th)}$	Gate Threshold Voltage	Ic=1mA	,V _{CE} =V _{GE}	4.0	5.0	6.0	V
Dynamic Cha	aracteristics						
Cies	Input Capacitance	V _{CE} =25V,V _{GE} =0V, f=1MHz			3552		pF
Coes	Output Capacitance				106		
Cres	Reverse Transfer Capacitance				67		
Qg	Total Gate Charge	Vcc=480V, Ic=30A VgE=15V			132		nC
Qge	Gate to Emitter Charge				28		
Qgc	Gate to Collector Charge				54		
I _{C(SC)}	Short circuit collector current Max.1000 short circuits Time between short circuits: ≥1.0s	V _{GE} =15V,V _{CC} ≤400V, t _{SC} ≤5us,Tj≤150°C			190		А
Switching Cl	naracteristics						
$t_{d(ON)}$	Turn-on Delay Time				19		
t _r	Rise Time				17		20
$t_{\text{d}(\text{OFF})}$	Turn-Off Delay Time	V_{CC} =400V, I_{C} =30A V_{GE} =0/15V, R_{g} =5 Ω			166		ns
t f	Fall Time				16		
Eon	Turn-On Switching Loss	Inductive Load			0.36		
E _{off}	Turn-Off Switching Loss				0.32		mJ
Ets	Total Switching Loss				0.68		

Electrical Characteristics of the Diode (Tc= 25°C unless otherwise specified):

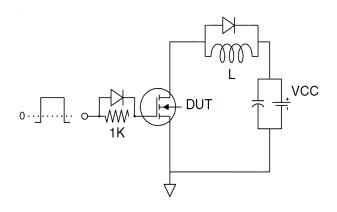
Symbol	Parameter	Toot Conditions	Rating			Unito
Symbol		Test Conditions	Min.	Тур.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F =30A	-	1.7	1.9	V
Trr	Reverse Recovery Time			178		ns
I _{RRM}	Diode Peak Reverse Recovery Current	I _F =30A, di/dt=200A/us		4		А
Qrr	Reverse Recovery Charge			0.4		uC
Pulse width t _{tp} ≤380μs,δ≤2%						



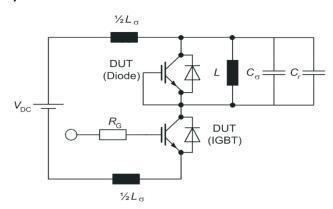
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Test Circuit

1) Gate Charge Test Circuit

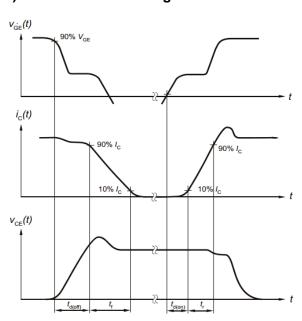


2) Switch Time Test Circuit

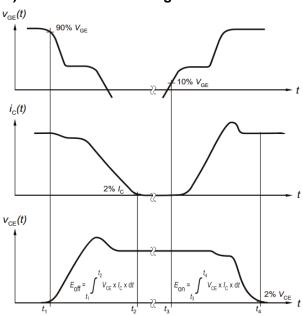


Switching characteristics

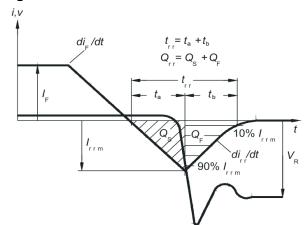
1) Definition of switching times



2) Definition of switching losses



3) Definition of diode switching characteristics





Typical Electrical and Thermal Characteristics

Figure 1 Output Characteristics

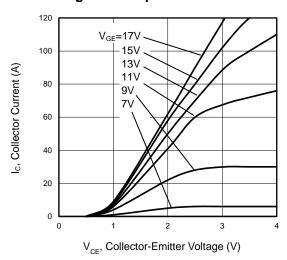


Figure 3 V_{CEsat} vs. Case Temperature

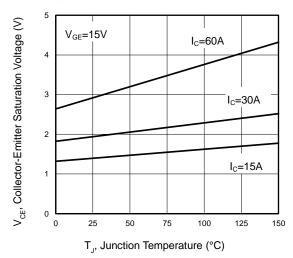


Figure 5 Capacitance Characteristics

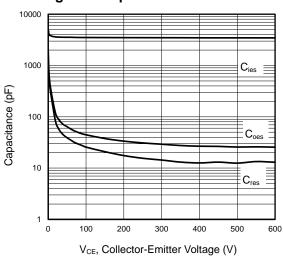


Figure 2 Transfer Characteristics

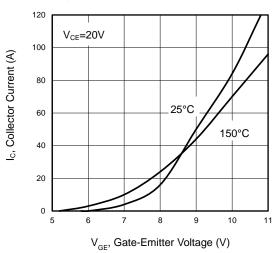


Figure 4 Saturation Voltage vs. V_{GE}

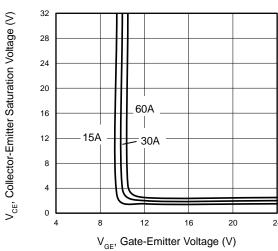
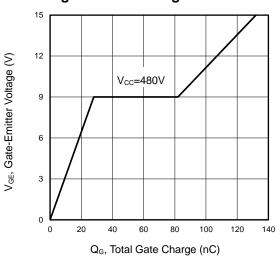


Figure 6 Gate charge waveform





Typical Electrical and Thermal Characteristics

Figure 7 Gate-emitter Threshold Voltage as a Function of Junction Temperature

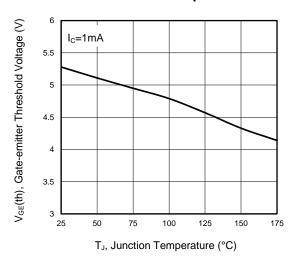


Figure 9 Typical Switching Times as a Function of Gate Resistor

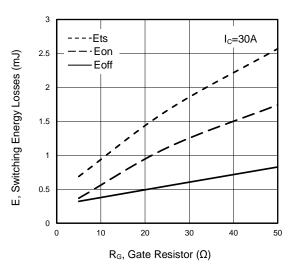


Figure 11 Typical Collector-emitter Saturation Voltage as a function of Collector Current

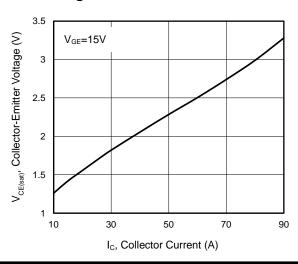


Figure 8 Power Dissipation as a Function of Case Temperature

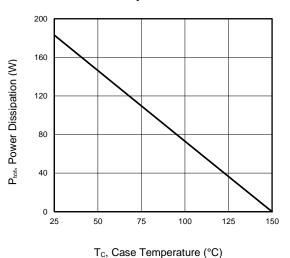


Figure 10 Typical Switching Times as a

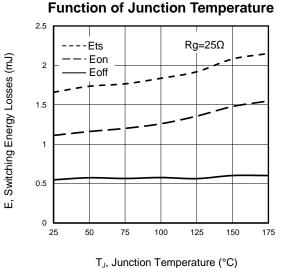
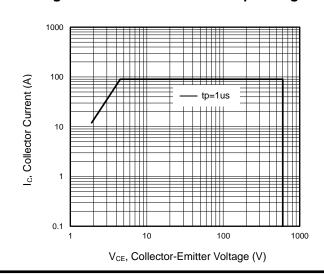


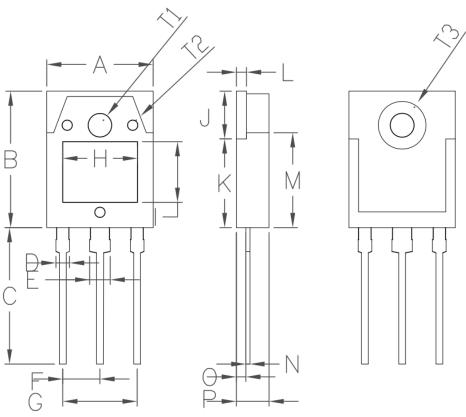
Figure 13 Forward Bias Safe Operating Area



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TO-3P-3L Package Information



Complead	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	15.50	15.70	0.61	0.62	
В	19.70	20.10	0.78	0.79	
С	20.10	20.50	0.79	0.81	
D	2.	00	0.	08	
Е	3.	00	0.	12	
F	5.	45	0.	21	
G	10	.90	0.43		
Н	10.80	11.00	0.43	0.43	
I	8.80	9.00	0.35	0.35	
J	6.85	7.15	0.27	0.28	
K	12.75	13.05	0.50	0.51	
L	1.49	1.51	0.06	0.06	
М	13.70	14.00	0.54	0.55	
N	0.59	0.61	0.02	0.02	
0	1.32	1.48	0.05	0.06	
Р	4.70	4.90	0.19	0.19	
T1	3.	3.50 0.14		14	
T2	1.	1.50		0.06	
T3	7.00		0.28		





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