NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE40H12K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

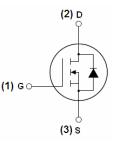
- V_{DS} =40V,I_D =120A
 - $R_{DS(ON)}$ <4.0m Ω @ V_{GS} =10V
 - $R_{DS(ON)}$ <7m Ω @ V_{GS} =4.5V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-252-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE40H12K	NCE40H12K	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	120	Α
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	85	Α
Pulsed Drain Current	I _{DM}	330	Α
Maximum Power Dissipation	P _D	120	W
Derating factor		0.8	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	1080	mJ
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 175	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta JC}$	1.25	°C/W	
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Electrical Characteristics (T_C=25 °C unless otherwise noted)

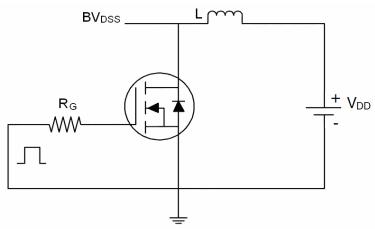
Parameter Parameter	Symbol Condition		Min	Тур	Max	Unit	
Off Characteristics	<u> </u>		•				
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	DμA 40		-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μΑ	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	1.2	1.8	2.5	V	
Drain-Source On-State Resistance	Б	V _{GS} =10V, I _D =20A	-	3.6	4.0	mΩ	
Diain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =10A	-	5.8	7.0	11122	
Forward Transconductance	g FS	V _{DS} =10V,I _D =20A	26	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}	\/ -20\/\/ -0\/	-	5400	-	PF	
Output Capacitance	C _{oss}	V_{DS} =20V, V_{GS} =0V, F=1.0MHz	-	970	-	PF	
Reverse Transfer Capacitance	C _{rss}	F-1.0WHZ	-	380	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	15	-	nS	
Turn-on Rise Time	t _r	V_{DD} =20V, I_D =2A, R_L =1 Ω	-	18	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	52	-	nS	
Turn-Off Fall Time	t _f		-	23	-	nS	
Total Gate Charge	Qg	V -20V/1 -20A	-	75		nC	
Gate-Source Charge	Q _{gs}	V_{DS} =20V, I_D =20A, V_{GS} =10V	-	10.5		nC	
Gate-Drain Charge	Q_{gd}	V _{GS} -10V	-	17		nC	
Drain-Source Diode Characteristics	<u>.</u>						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =40A	-		1.2	V	
Diode Forward Current (Note 2)	Is		-	-	120	Α	
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 40A	-	42	-	nS	
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	45	-	nC	
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)					

Notes:

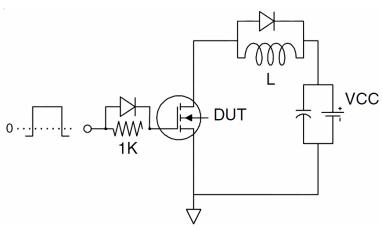
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- $\textbf{5.}~E_{AS}~condition: Tj=25^{\circ}\text{C}, V_{DD}=20\text{V}, V_{G}=10\text{V}, L=1\text{mH}, Rg=25\Omega, ~~I_{AS}=46.5\text{A}$

Test circuit

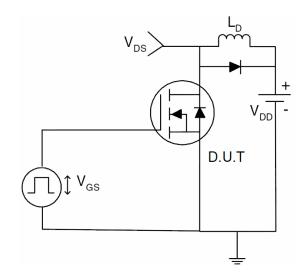
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

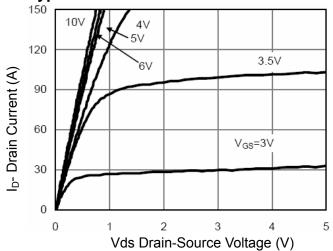


Figure 1 Output Characteristics

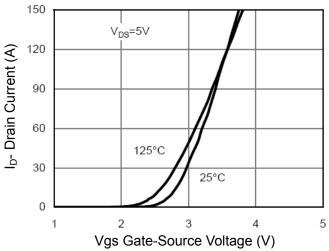


Figure 2 Transfer Characteristics

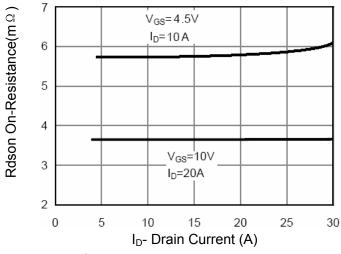


Figure 3 Rdson- Drain Current

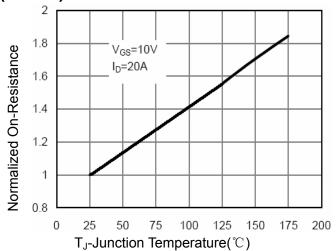


Figure 4 Rdson-JunctionTemperature

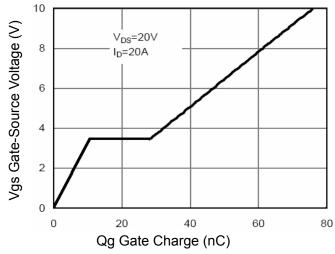


Figure 5 Gate Charge

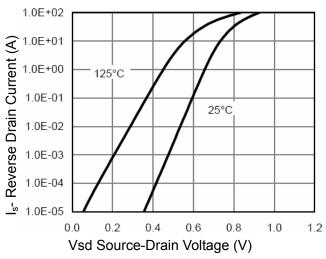
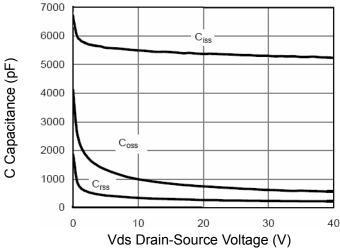


Figure 6 Source- Drain Diode Forward

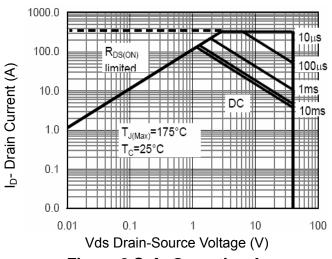


150 Power Dissipation (W) 120 90 60 30 0 0 25 50 75 100 125 150 175 T_J-Junction Temperature (°C)

180

Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



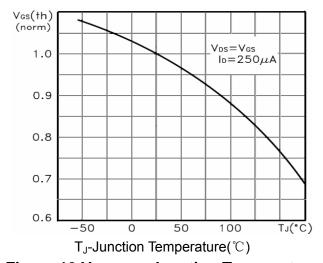
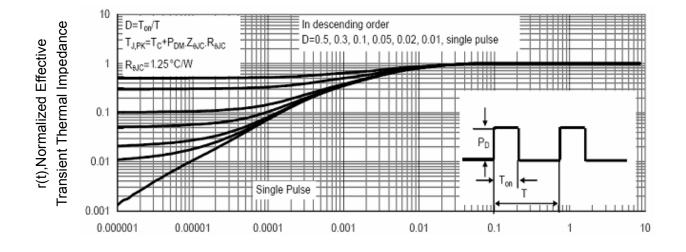


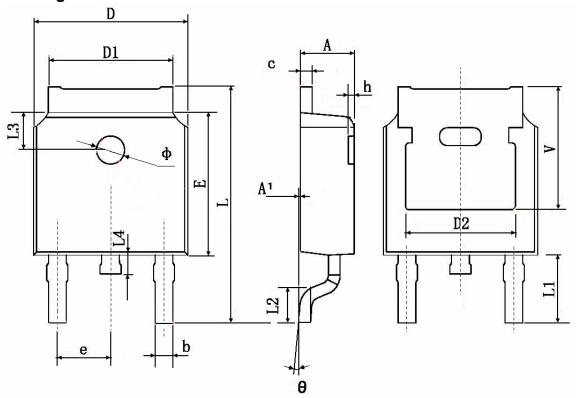
Figure 8 Safe Operation Area

Figure 10 V_{GS(th)} vs Junction Temperature



Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.8	4.830 TYP.		TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.90	0 TYP.	0.114	TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.60	0 TYP.	0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.35	0 TYP.	0.211 TYP.		



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