



## NCE N-Channel Enhancement Mode Power MOSFET

## Description

The NCE5080K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

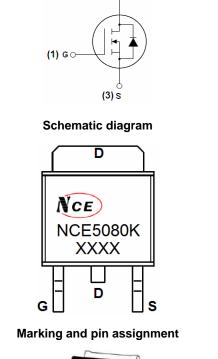
- V<sub>DS</sub> =50V,I<sub>D</sub> =80A
  - $R_{DS(ON)} < 7.5 m\Omega @ V_{GS} = 10V$
  - $R_{DS(ON)} < 9m\Omega @ V_{GS}=4.5V$
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

### Application

- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply

#### 100% UIS TESTED!

### 100% ΔVds TESTED!



(2) D



TO-252-2L top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE5080K	NCE5080K	TO-252-2L	TO-252-2L -		-

## Absolute Maximum Ratings (T<sub>c</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	50	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	80	А
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	56.5	А
Pulsed Drain Current	I <sub>DM</sub>	320	А
Maximum Power Dissipation	PD	100	W
Derating factor		0.67	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	400	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C
Thermal Characteristic			
Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	R <sub>eJC</sub>	1.5	°C/W





## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	50	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.0	1.5	2.5	V	
Durain Course On Chata Desistence		$V_{GS}$ =10V, $I_{D}$ =20A	-	5.6	7.5		
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	6.7 9		9	mΩ	
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	-	20	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C <sub>lss</sub>		-	3600	-	PF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}=25V, V_{GS}=0V,$	-	340	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	230	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	12	-	nS	
Turn-on Rise Time	tr	$V_{DD}$ =25V,, $R_L$ =1 $\Omega$	-	30	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =3 $\Omega$	-	45	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	31	-	nS	
Total Gate Charge	Qg	)/ OF)// OOA	-	65		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =25V,I <sub>D</sub> =20A,	-	13		nC	
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	20		nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-		1.2	V	
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	80	Α	
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 20A	-	36	-	nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	48	-	nC	

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

**2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.

**3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

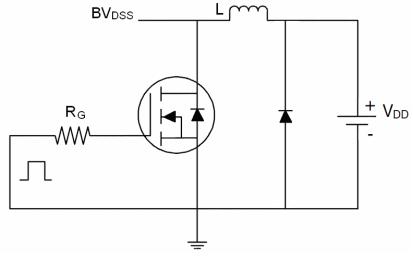
- 4. Guaranteed by design, not subject to production
- 5.  $E_{AS}$  condition : Tj=25  $^\circ\!\mathrm{C}$  ,V\_{DD}=25V,V\_G=10V,L=0.5mH,Rg=25\Omega ,



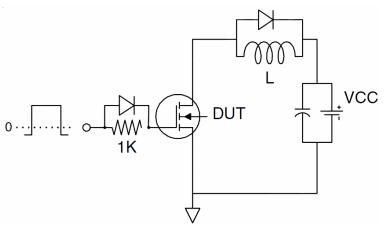
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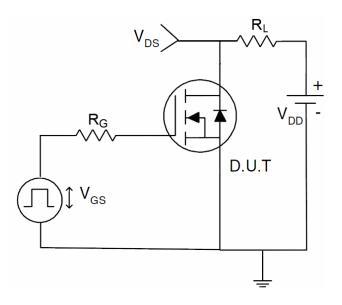
## Test circuit 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit



## 3) Switch Time Test Circuit







150

40

25°C

0.8

1.0

50

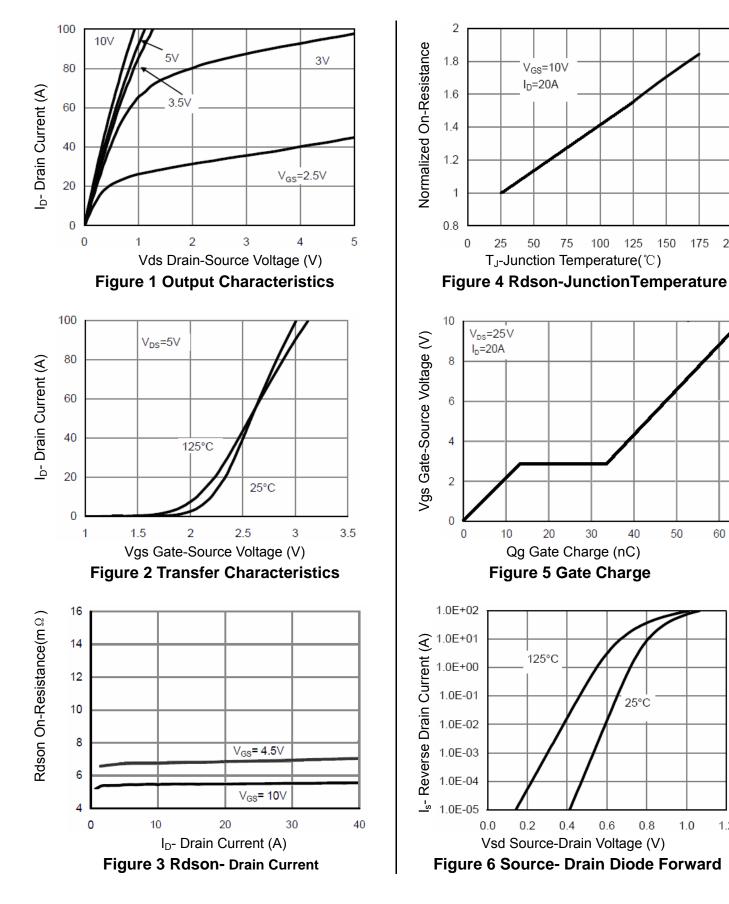
60

70

200

175

## **Typical Electrical and Thermal Characteristics (Curves)**



1.2



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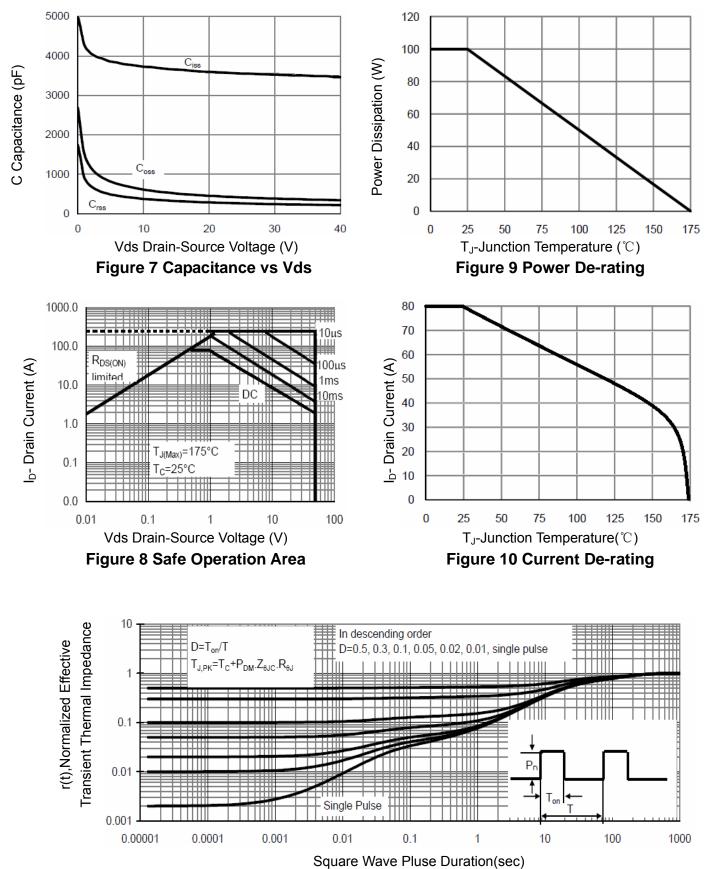


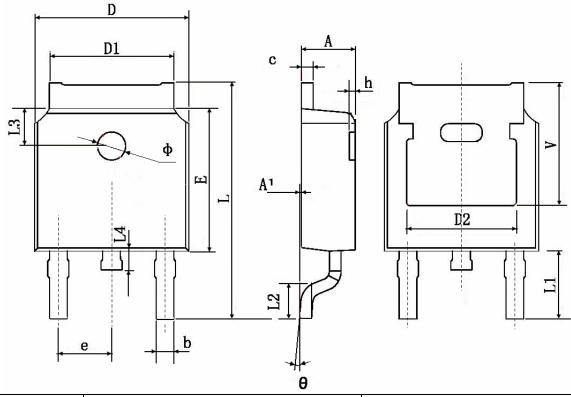
Figure 11 Normalized Maximum Transient Thermal Impedance



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# TO-252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches			
	Min.	Max.	Min.	Max.		
A	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	4.830	TYP.	0.190 TYP.			
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600	00 TYP. 0.063 TYP.		TYP.		
L4	0.600	1.000	0.024	0.039		
Φ	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350	TYP.	0.211 TYP.			





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