

**Pb Free Product** 



# NCE P-Channel Enhancement Mode Power MOSFET

### **Description**

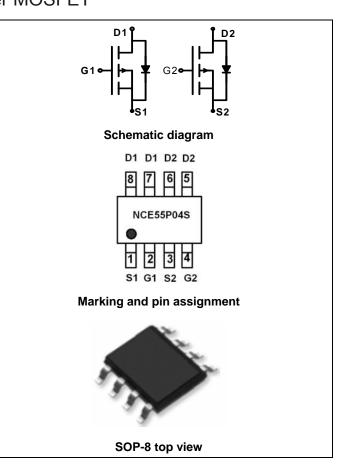
The NCE55P04S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **General Features**

- $V_{DS}$  =-55V, $I_{D}$  =-4A  $R_{DS(ON)}$  <82m $\Omega$  @  $V_{GS}$ =-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

### **Application**

- Power switching application
- Hard switched and high frequency circuits
- DC-DC Converter



### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE55P04S	NCE55P04S	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-55	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	-4	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	-2.8	Α
Pulsed Drain Current	I <sub>DM</sub>	-25	Α
Maximum Power Dissipation	P <sub>D</sub>	3	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$

## **Thermal Characteristic**

Thermal Resistance ,Junction-to-Ambient <sup>(Note 2)</sup>	$R_{ heta JA}$	42	°C/W	
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### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

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Parameter	Symbol	Condition	Min	Тур	Max	Unit



## http://www.ncepower.com

## NCE55P04S

Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-55	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-55V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-1.5	-2.6	-3.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A	-	66	82	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-15V,I <sub>D</sub> =-4A	16	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\\ O5\\\\ O1\	-	1450	-	PF
Output Capacitance	Coss	$V_{DS}$ =-25V, $V_{GS}$ =0V,	-	145	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz		110	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-30 $V$ , $R_L$ =30 $\Omega$	-	9	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =6 $\Omega$	-	65	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	30	-	nS
Total Gate Charge	Qg		-	26	-	nC
Gate-Source Charge	$Q_gs$	$V_{DS}$ =-30V, $I_{D}$ =-4A,	-	4.5	-	nC
Gate-Drain Charge	$Q_gd$	V <sub>GS</sub> =-10V	-	7	-	nC
Drain-Source Diode Characteristics	- 1		•	1		
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =-4A	-	-	-1.2	V
Diode Forward Current (Note 2)	Is		-	-	-4	Α

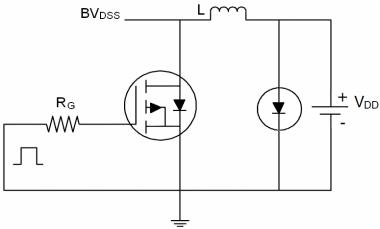
#### Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

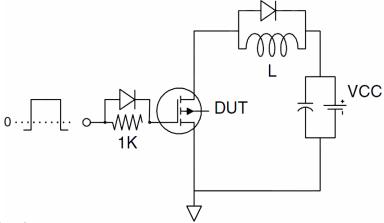


## **Test Circuit**

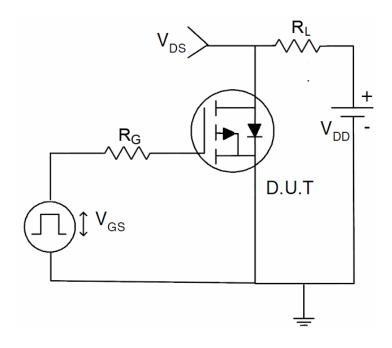
## 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit

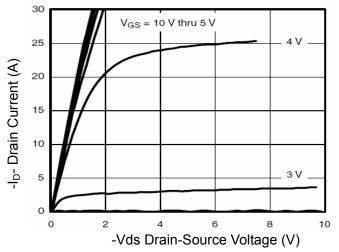


## 3) Switch Time Test Circuit

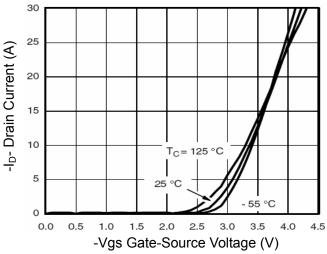




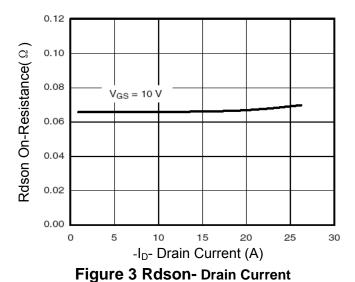
## **Typical Electrical and Thermal Characteristics**



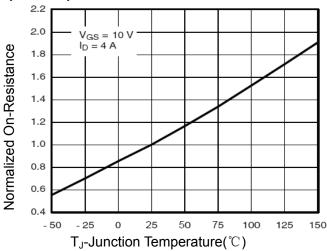
**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 



(Curves)



**Figure 4 Rdson-Junction Temperature** 

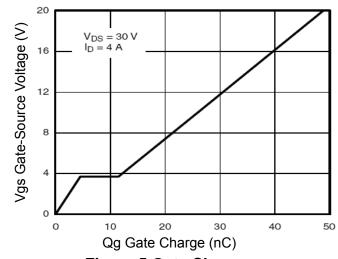


Figure 5 Gate Charge

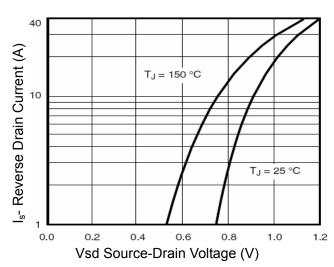


Figure 6 Source- Drain Diode Forward



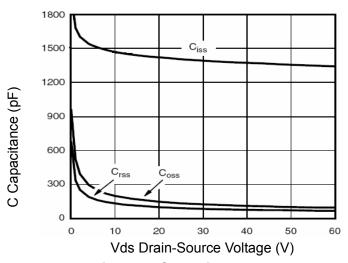


Figure 7 Capacitance vs Vds

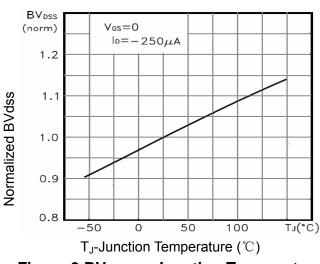
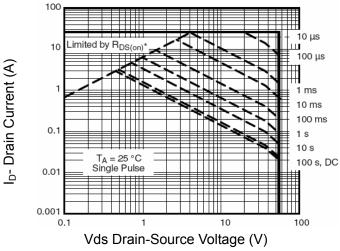


Figure 9 BV<sub>DSS</sub> vs Junction Temperature



**Figure 8 Safe Operation Area** 

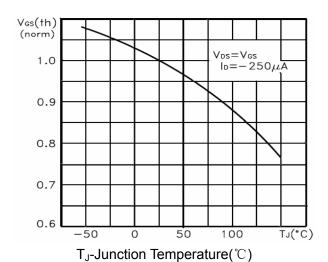


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

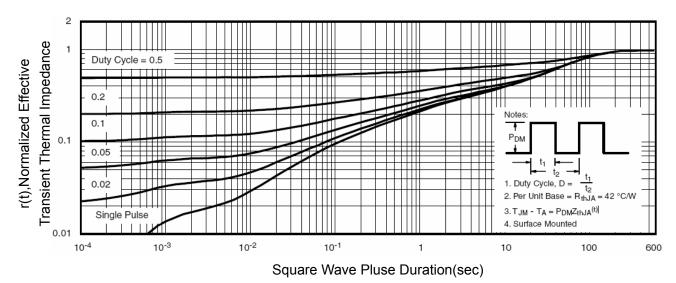
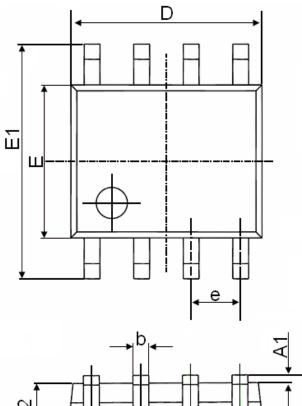


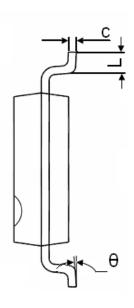
Figure 11 Normalized Maximum Transient Thermal Impedance

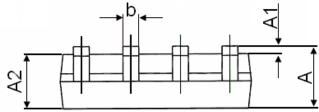
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## **SOP-8 Package Information**







Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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