

## NCE N-Channel Enhancement Mode Power MOSFET

## **Description**

The NCE8205A uses advanced trench technology to provide excellent  $R_{\rm DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

•  $V_{DS} = 20V, I_D = 6A$ 

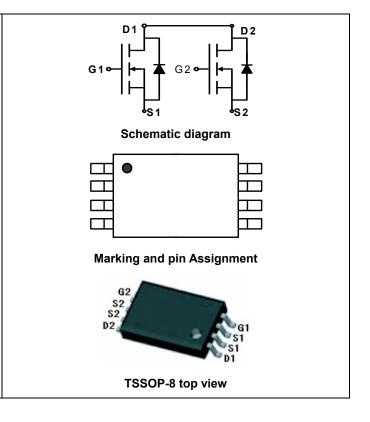
 $R_{DS(ON)}$  < 37m $\Omega$  @  $V_{GS}$ =2.5V

 $R_{DS(ON)}$  < 27m $\Omega$  @  $V_{GS}$ =4.5V

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

### **Application**

- Battery protection
- Load switch
- Power management



## **Package Marking And Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
8205A	NCE8205A	TSSOP-8	Ø330mm	12mm	3000 units

## Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>G</sub> s	±10	V
Drain Current-Continuous	I <sub>D</sub>	6	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	25	А
Maximum Power Dissipation	P <sub>D</sub>	1.5	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C

## **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	83	°C/W

#### **Electrical Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)**

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	20	21	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =19.5V,V <sub>GS</sub> =0V	-	-	1	μA



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# **NCE8205A**

Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.5	0.7	1.2	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4.5A	-	21	27	mΩ
Dialii-Source Oil-State Resistance		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3.5A	-	27	37	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =4.5A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -9\/\/ -0\/	-	600	-	PF
Output Capacitance	Coss	$V_{DS}$ =8 $V$ , $V_{GS}$ =0 $V$ , F=1.0MHz	-	330	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F = 1.01VII 12	-	140	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	10	20	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =10 $V$ , $I_{D}$ =1 $A$	-	11	25	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =4.5 $V$ , $R_{GEN}$ =6 $\Omega$	-	35	70	nS
Turn-Off Fall Time	t <sub>f</sub>		-	30	60	nS
Total Gate Charge	$Q_g$	V <sub>DS</sub> =10V,I <sub>D</sub> =6A,	-	10	15	nC
Gate-Source Charge	$Q_{gs}$	$V_{DS}=10V,ID=0A,$ $V_{GS}=4.5V$	-	2.3	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> -4.5V	-	1.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =1.7A	-	0.75	1.2	V
Diode Forward Current (Note 2)	Is		-	-	1.7	Α

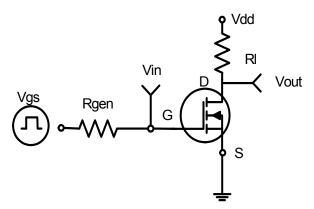
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

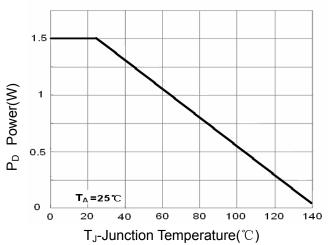




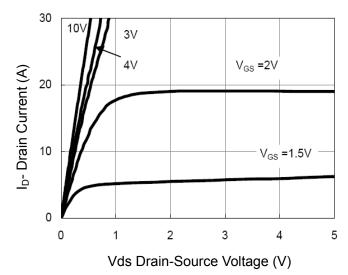
## **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 



**Figure 3 Power Dissipation** 



**Figure 5 Output Characteristics** 

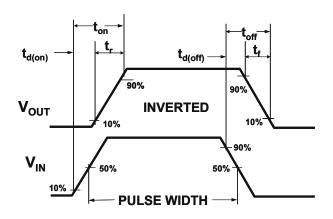
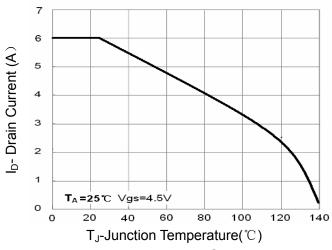


Figure 2:Switching Waveforms



**Figure 4 Drain Current** 

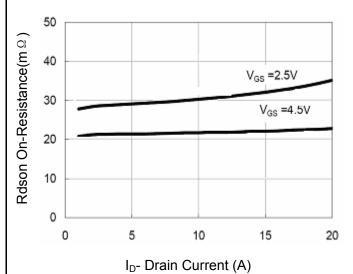
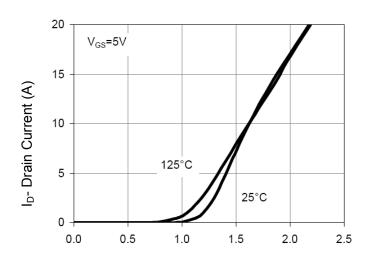


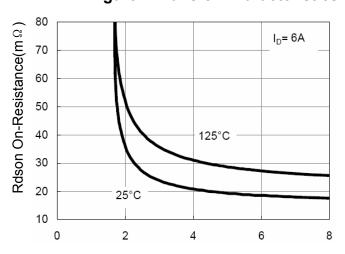
Figure 6 Drain-Source On-Resistance







Vgs Gate-Source Voltage (V) **Figure 7 Transfer Characteristics** 



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

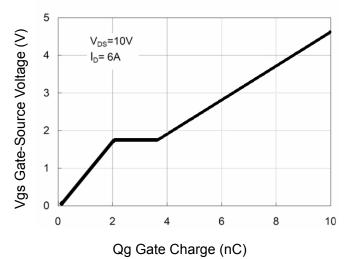
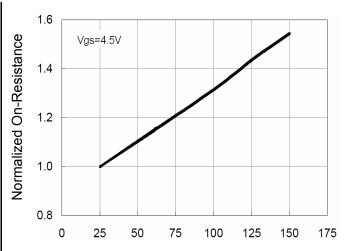


Figure 11 Gate Charge



 $T_J$ -Junction Temperature( $^{\circ}$ C)

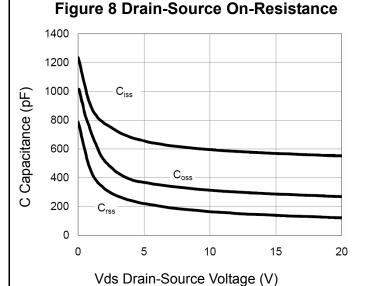
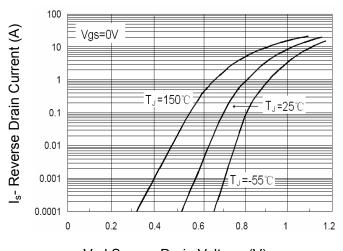


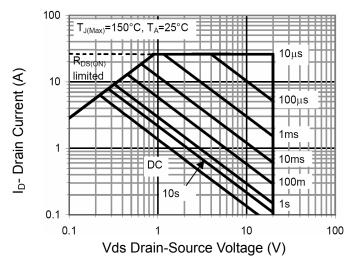
Figure 10 Capacitance vs Vds



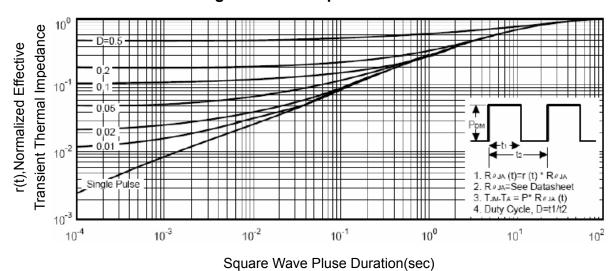
Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward





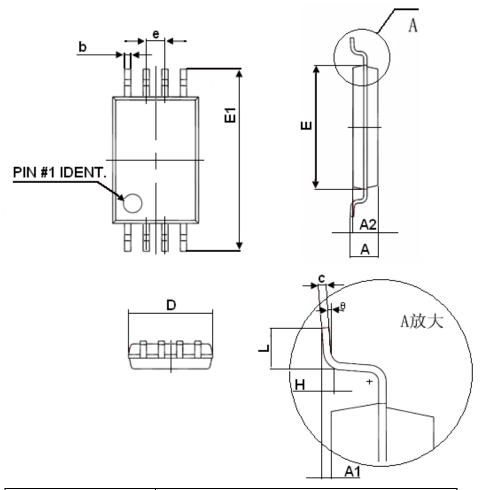
**Figure 13 Safe Operation Area** 



**Figure 14 Normalized Maximum Transient Thermal Impedance** 



# **Tssop-8 Package Information**



Symbol	Dimensions In Millimeters			
Symbol	Min	Max		
D	2.900	3.100		
E	4.300	4.500		
b	0.190	0.300		
С	0.090	0.200		
E1	6.250	6.550		
Α		1.100		
A2	0.800	1.000		
A1	0.020 0.150			
е	0.65(BSC)			
L	0.500 0.700			
Н	0.25(TYP)			
Θ	1° 7°			



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