

NCE N-Channel Super Trench II Power MOSFET

Description

The series of devices uses Super Trench II technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS}(\text{ON})}$ and $Q_g.$ This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching synchronous rectification

General Features

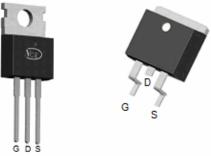
- V_{DS} =85V,I_D =160A $R_{DS(ON)}$ =3.10m Ω , typical (TO-220)@ V_{GS} =10V $R_{DS(ON)}$ =2.95m Ω , typical (TO-263)@ V_{GS} =10V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating

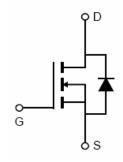
100% UIS TESTED! 100% AVds TESTED!

TO-220



TO-263





Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP033N85	NCEP033N85	TO-220	-	-	-
NCEP033N85D	NCEP033N85D	TO-263	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	85	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	160	А
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	112	Α
Pulsed Drain Current	I _{DM}	640	Α
Maximum Power Dissipation	P _D	220	W
Derating factor		1.47	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	1295	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$



NCEP033N85, NCEP033N85D

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta JC}$	0.68	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition		Min	Тур	Max	Unit
Off Characteristics				•			
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V I_D =250 μ A		85		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =85V,V _{GS} =0V		-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V		-	-	±100	nA
On Characteristics (Note 3)				•			
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =250μA		2.0	3.0	4.0	V
Drain Course On Ctata Desistance	Б)/ /0\/ l 00A	TO-220	-	3.1	3.3	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =10V, I_D =80A	TO-263		2.95	3.3	mΩ
Gate resistance	R_G			-	1.9	-	Ω
Forward Transconductance	g FS	V_{DS} =5 V , I_{D} =	80A		90	-	S
Dynamic Characteristics (Note4)				•			
Input Capacitance	C _{lss}	V _{DS} =40V,V _{GS} =0V, F=1.0MHz		-	7200	-	PF
Output Capacitance	Coss			-	1100	-	PF
Reverse Transfer Capacitance	C _{rss}			-	24	-	PF
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}	V_{DD} =40V, I_{D} =80A V_{GS} =10V, R_{G} =1.6 Ω		-	21	-	nS
Turn-on Rise Time	t _r			-	12.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$			-	48	-	nS
Turn-Off Fall Time	t _f			-	12	-	nS
Total Gate Charge	Q_g	\/ -40\/.	.004	-	115	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} =40V,I _D =80A, V _{GS} =10V		-	39		nC
Gate-Drain Charge	Q_{gd}			-	32		nC
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =80A		-		1.2	V
Diode Forward Current (Note 2)	Is			-	-	160	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 80A		-	80	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$		-	147	-	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V_DD=40V,V_G=10V,L=0.5mH,Rg=25 Ω

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Typical Electrical and Thermal Characteristics

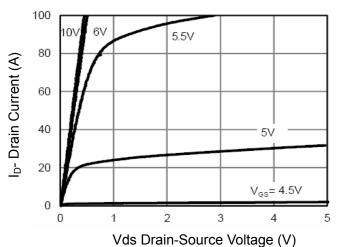


Figure 1 Output Characteristics

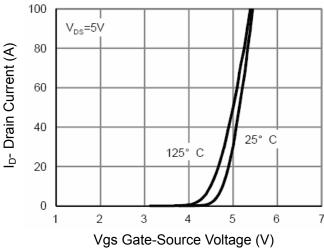


Figure 2 Transfer Characteristics

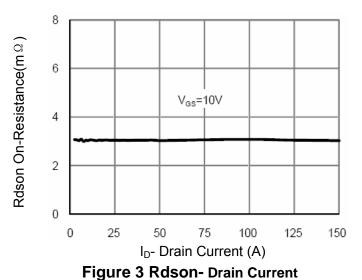


Figure 4 Rdson-Junction Temperature

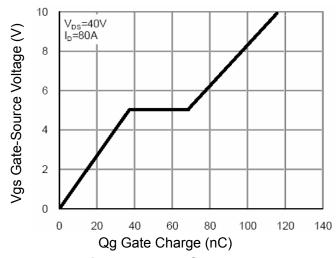


Figure 5 Gate Charge

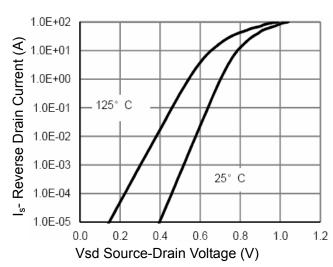


Figure 6 Source- Drain Diode Forward

V1.0



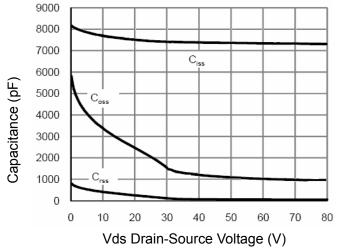


Figure 7 Capacitance vs Vds

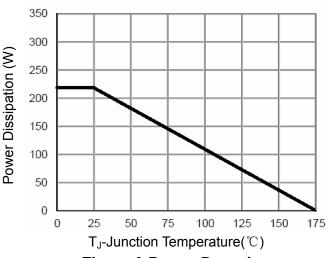


Figure 9 Power De-rating

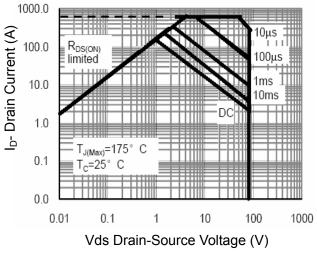


Figure 8 Safe Operation Area

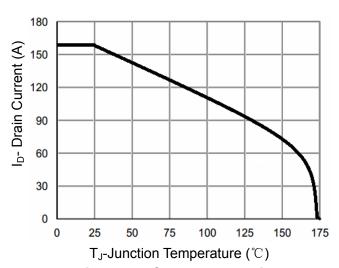


Figure 10 Current De-rating

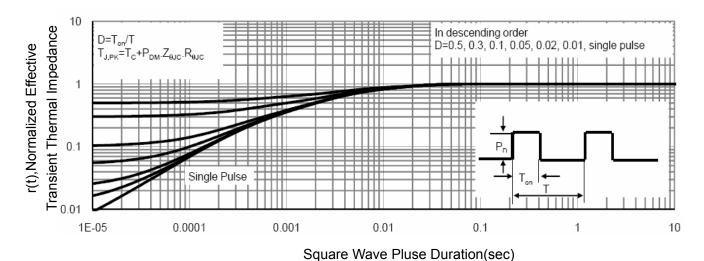
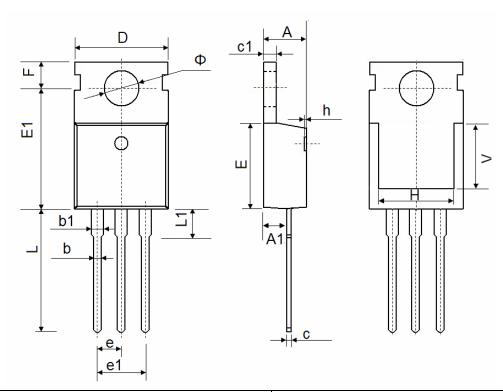


Figure 11 Normalized Maximum Transient Thermal Impedance

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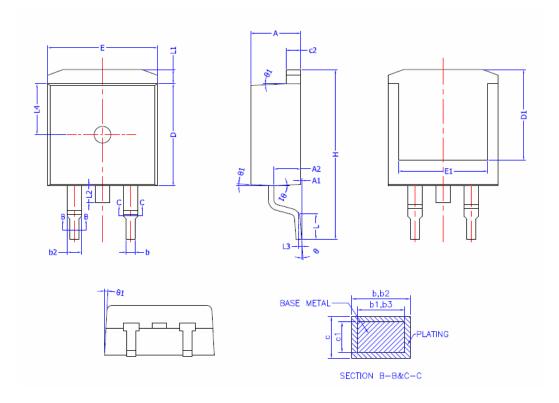
TO-220-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
Е	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540	TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	6.900 REF.		0.276 REF.		
Ф	3.400	3.800	0.134	0.150	

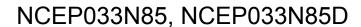


TO-263-2L Package Information



COMMON DIMENSIONS (UNITS OF MEASURE =MILLIMETER)

SYMBOL	MIN	NOM	MAX		
Α	4.40	4.50	4.60		
A1	0	0.10	0.25		
A2	2,20	2,40	2,60		
b	0,76	_	0,89		
b1	0,75	0,80	0,85		
b2	1,23		1,37		
b3	1,22	1,27	1,32		
С	0,47	_	0,60		
c1	0.46	0.51	0.56		
c2	1,25	1.30	1.35		
D	9,10	9.20	9.30		
D1	8,00	_	—		
E	9,80	9.90	10.00		
E1	7,80 —		_		
e	2.54 BSC				
Н	14.90	15.30	15.70		
L	2.00	2,30	2.60		
L1	1.17	1.27	1.40		
L2	— — 1,75				
L3	0.25BSC				
L4	4.60 REF				
θ	0°	— 8°			
θ1	1°	3° 5°			





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