

(2) D

(3) s

Schematic diagram

NCEP048N72 XXXXX

Marking and pin assignment

TO-220-3L top view

NCE

(1) G C

# NCE N-Channel Super Trench Power MOSFET

#### Description

The NCEP048N72 uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### **General Features**

- V<sub>DS</sub> =72V,I<sub>D</sub> =110A
  - $R_{DS(ON)}$  <4.3m $\Omega$  @ V<sub>GS</sub>=10V
- Excellent gate charge x R<sub>DS(on)</sub> product
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

## Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED!

100% ΔVds TESTED!

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP048N72	NCEP048N72	TO-220-3L	-	-	-

### Absolute Maximum Ratings (T<sub>c</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	VDS	72	V	
Gate-Source Voltage	Vgs	±20	V	
Drain Current-Continuous	ID	110	A	
Drain Current-Continuous(Tc=100 ℃)	l₀ (100°C)	77.8	A	
Pulsed Drain Current	I <sub>DM</sub>	440	A	
Maximum Power Dissipation	PD	155	W	
Derating factor		1.03	W/°C	
Single pulse avalanche energy (Note 5)	Eas	460	mJ	
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C	



#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup> R <sub>0JC</sub> 0.97
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### Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			-			1
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	72		-	V
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =72V,V <sub>GS</sub> =0V -		-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)	· · ·		•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =55A	-	3.9	4.3	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =10V,I <sub>D</sub> =55A	40	-	-	S
Dynamic Characteristics (Note4)				·		
Input Capacitance	Clss		-	3200	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =35V,V <sub>GS</sub> =0V,	-	519	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	18	-	PF
Switching Characteristics (Note 4)	· · ·		•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	11	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =35V,I <sub>D</sub> =55A	-	5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =4.7 $\Omega$	-	56	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Qg		-	48.8		nC
Gate-Source Charge	Qgs	$V_{DS}$ =35V,I <sub>D</sub> =55A,	-	16.9		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	8.7		nC
Drain-Source Diode Characteristics	· · ·		·	· · ·		
Diode Forward Voltage (Note 3)	Vsd	V <sub>GS</sub> =0V,I <sub>S</sub> =110A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	110	A
Reverse Recovery Time	trr	$T_J$ = 25°C, $I_F$ = $I_S$	-	47		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	59		nC

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t  $\leq$  10 sec.

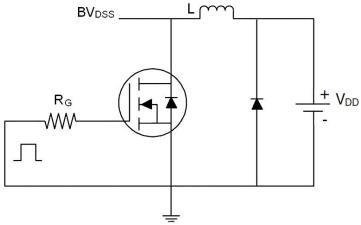
3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

4. Guaranteed by design, not subject to production

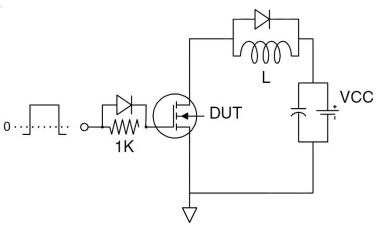
5. EAS condition : Tj=25  $^\circ C, V_{DD}$ =30V,V\_G=10V,L=0.5mH,Rg=25 $\Omega$ 



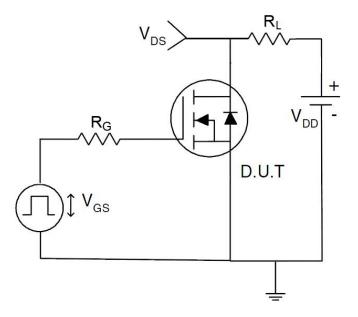
## Test Circuit 1) E<sub>AS</sub> test Circuit



#### 2) Gate charge test Circuit



3) Switch Time Test Circuit





75

100

40

25° С

0.8

1.0

1.2

0.6

0.4

125

150

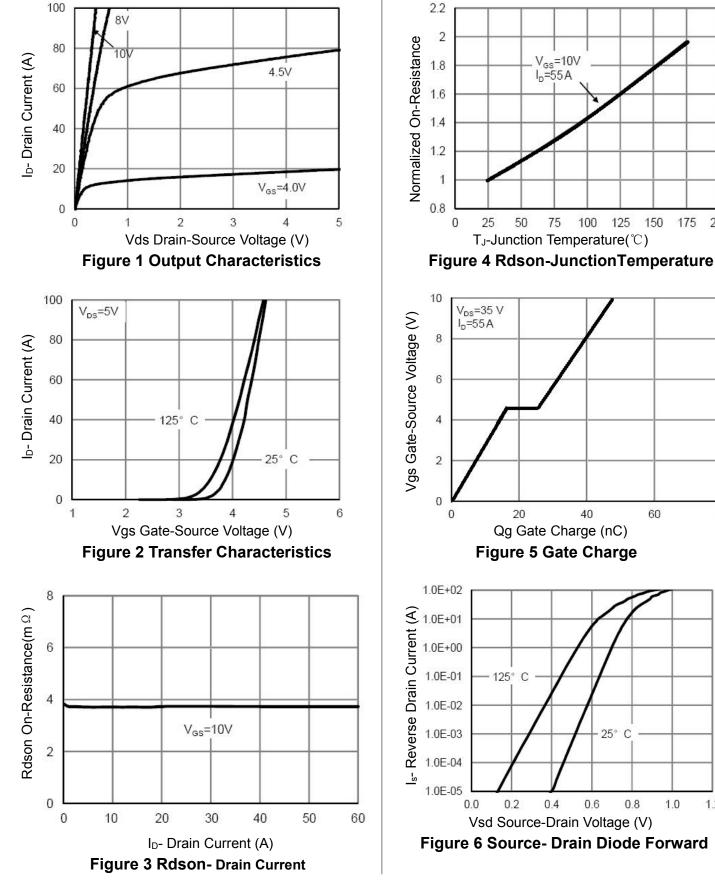
60

175

200

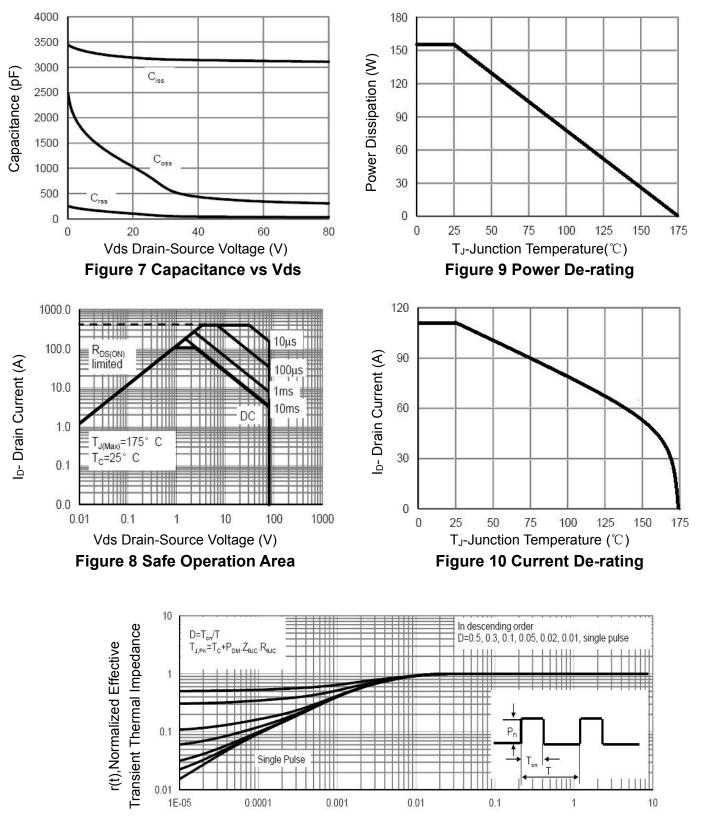
80







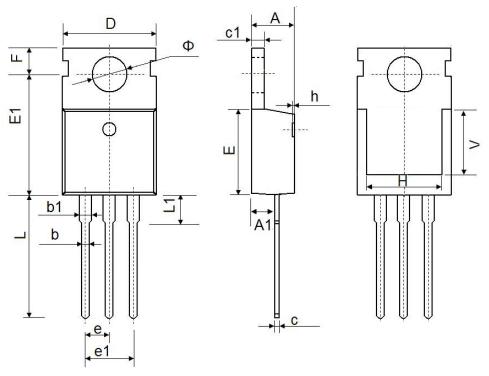
#### http://www.ncepower.com



Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance



# TO-220-3L Package Information



Cumple al	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540 TYP.		0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500 REF.		0.295 REF.		
Ф	3.400	3.800	0.134	0.150	

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