

60 V, 3 A low leakage current Trench MEGA Schottky barrier rectifier 4 April 2022

Product data sheet

1. General description

Trench Maximum Efficiency General Application (MEGA) Schottky barrier rectifier encapsulated in a CFP5 (SOD128) small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \le 3 A$
- Reverse voltage: $V_R \le 60 V$ •
- Low forward voltage •
- Low leakage current due to Trench MEGA Schottky technology •
- High power capability due to clip-bonding technology
- Small and flat lead SMD power plastic package
- Suitable for both reflow and wave soldering
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 155 °C		-	-	3	A
V _R	reverse voltage	T _j = 25 °C		-	-	60	V
V _F	forward voltage	I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	550	620	mV
I _R	reverse current	V _R = 10 V; pulsed; T _j = 25 °C	[1]	-	0.14	0.9	μA
		V _R = 60 V; pulsed; T _j = 25 °C	[1]	-	0.3	1.8	μA

[1] Very short pulse, in order to maintain a stable junction temperature.

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5. Pinning information

Table 2.	Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode		
2	А	anode		K 🔁 A
			CFP5 (SOD128)	sym001

6. Ordering information

Table 3. Ordering information						
Type number	Package	Package				
	Name	Description	Version			
PMEG60T30ELP-Q	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128			

7. Marking

Table 4. Marking codes					
Type number	Marking code				
PMEG60T30ELP-Q	E2				

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	60	V
l _F	forward current	δ = 1; T _{sp} ≤ 150 °C		-	4.2	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 155 °C		-	3	A
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave; T _{j(init)} = 25 °C		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.75	W
			[2]	-	1.2	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

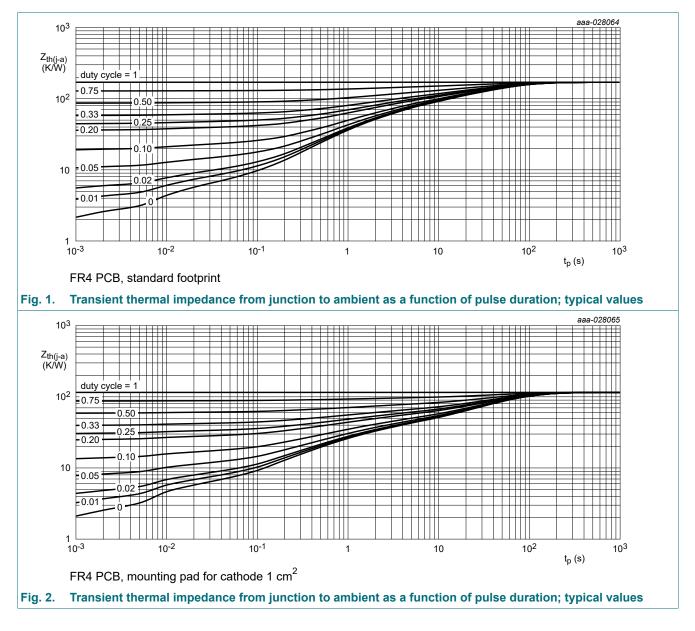
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	th(j-a) thermal resistance from junction to ambient	in free air	[1] [2]	-	-	200	K/W
			[1] [3]	-	-	120	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	12	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.



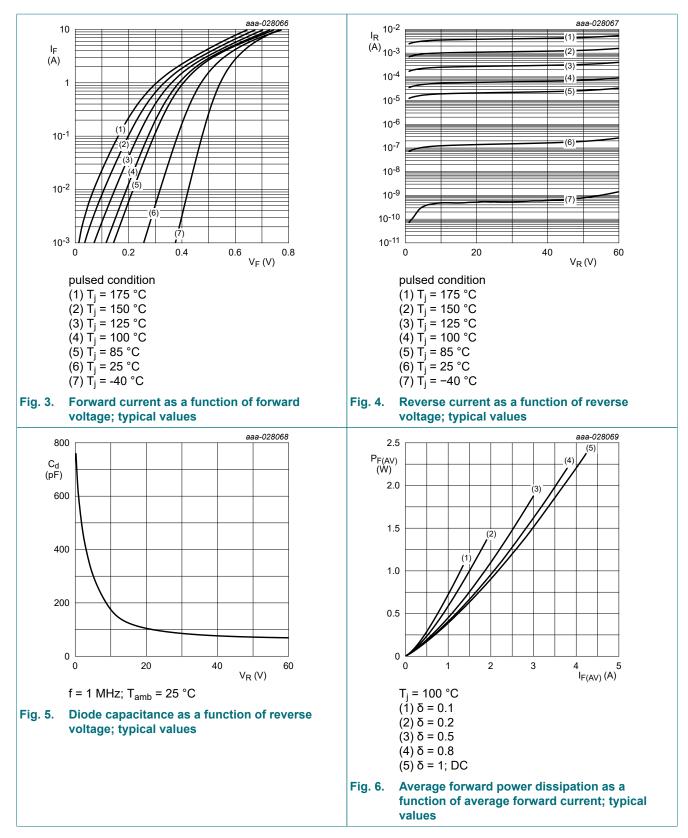
10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_R = 1 mA; pulsed; T_j = 25 °C	[1]	60	-	-	V
V _F	forward voltage	I _F = 0.1 A; pulsed; T _j = 25 °C	[1]	-	380	450	mV
		I _F = 0.5 A; pulsed; T _j = 25 °C	[1]	-	440	510	mV
		I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	470	540	mV
		I _F = 2 A; pulsed; T _j = 25 °C	[1]	-	515	590	mV
		I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	550	620	mV
		I _F = 3 A; pulsed; T _j = -40 °C	[1]	-	610	-	mV
		I _F = 3 A; pulsed; T _j = 125 °C	[1]	-	480	-	mV
I _R	reverse current	V _R = 10 V; pulsed; T _j = 25 °C	[1]	-	0.14	0.9	μA
		V _R = 40 V; pulsed; T _j = 25 °C	[1]	-	0.18	-	μA
		V _R = 60 V; pulsed; T _j = 25 °C	[1]	-	0.3	1.8	μA
		V _R = 60 V; pulsed; T _j = 125 °C	[1]	-	0.5	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	560	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	170	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 ^{\circ}\text{C}$		-	16	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 200 A/µs; I _F = 6 A; V _R = 26 V; T _j = 25 °C		-	16	-	ns
V _{FRM}	peak forward recovery voltage	I _F = 0.5 A; dI _F /dt = 20 A/μs; T _j = 25 °C		-	460	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.

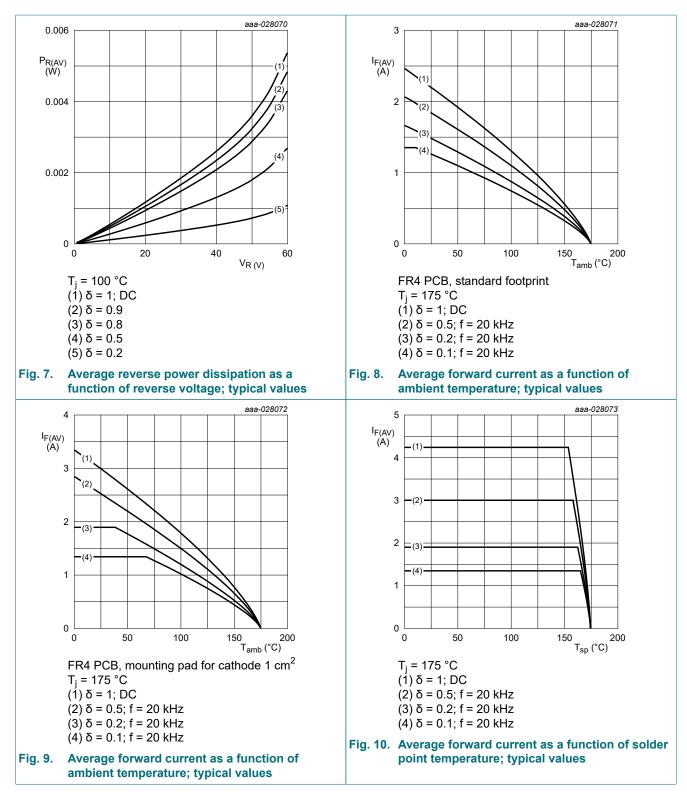
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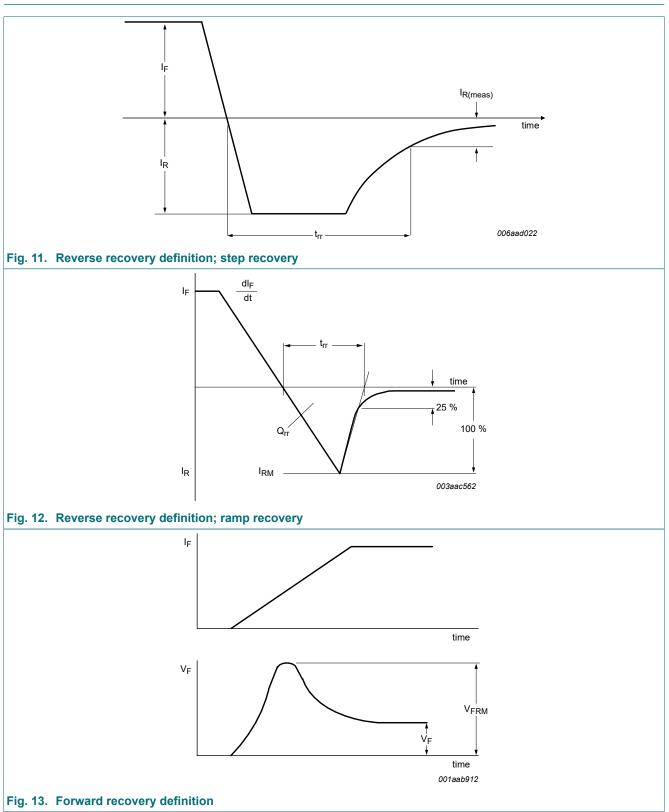
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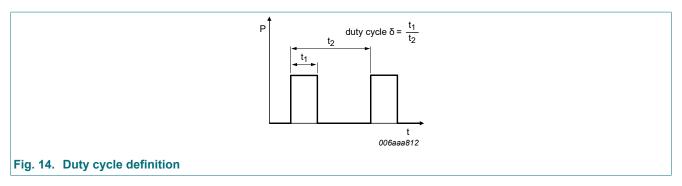
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11. Test information



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The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}=I_M \times \delta$ with I_M defined as peak current

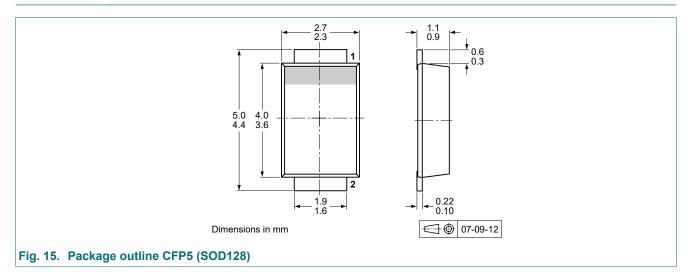
 $I_{RMS}=I_{F(AV)}$ at DC, and $I_{RMS}=I_M \times \sqrt{\delta}$

with I_{RMS} defined as RMS current.

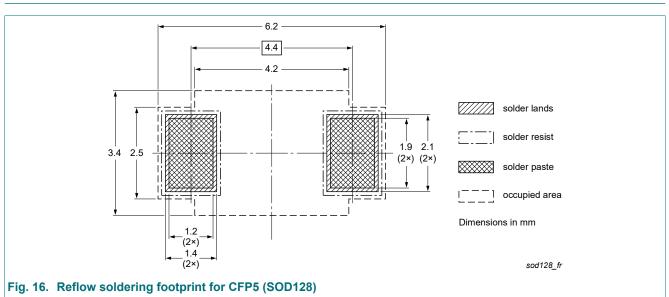
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

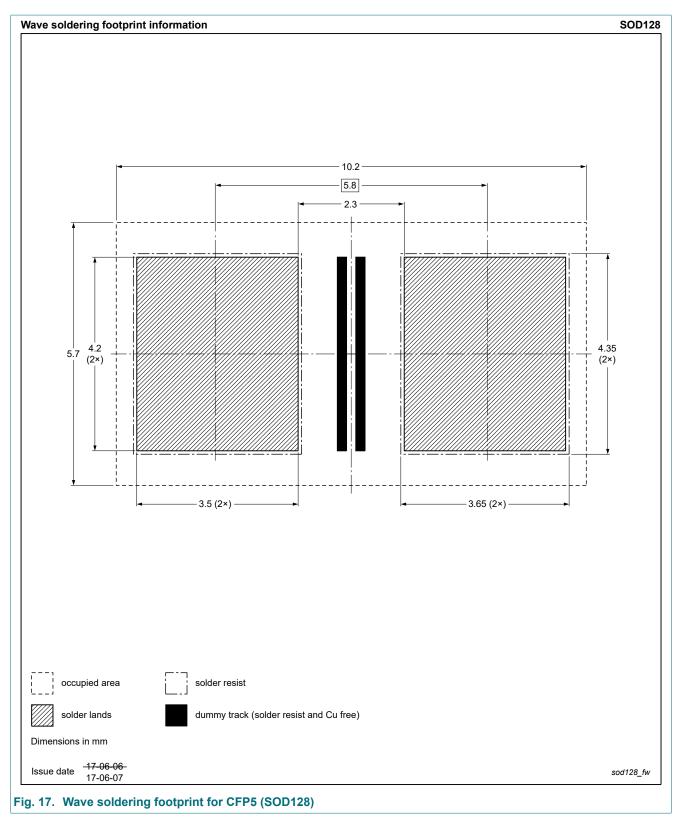
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG60T30ELP-Q v.1	20220404	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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