Product data sheet

1. General description

PNP high-voltage transistor in a SOT23 small Surface-Mounted Device (SMD) plastic package. NPN complement: MMBTA42-Q

2. Features and benefits

- Low current (max. 100 mA)
- High voltage (max. 300 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Telephony
- · Professional communication equipment

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|---------------------------|--|-----|-----|------|------|
| V _{CEO} | collector-emitter voltage | open base | - | - | -300 | V |
| I _C | collector current | | - | - | -100 | mA |
| h _{FE} | DC current gain | V_{CE} = -10 V; I_{C} = -1 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 25 | - | - | |

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--------------------|----------------|
| 1 | В | base | 3 | С |
| 2 | Е | emitter | | j |
| 3 | С | collector | | В |
| | | | 12 | E sym013 |
| | | | SOT23 | Symu13 |



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6. Ordering information

Table 3. Ordering information

| Type number | Package | | | | |
|-------------|---------|--|---------|--|--|
| | Name | Description | Version | | |
| MMBTA92-Q | SOT23 | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23 | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| MMBTA92-Q | 7E% |

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|---------------------------|--------------------------|-----|-----|------|------|
| V_{CBO} | collector-base voltage | open emitter | | - | -300 | V |
| V _{CEO} | collector-emitter voltage | open base | | - | -300 | V |
| V_{EBO} | emitter-base voltage | open collector | | - | -5 | V |
| I _C | collector current | | | - | -100 | mA |
| I _{CM} | peak collector current | | | - | -200 | mA |
| I _{BM} | peak base current | | | - | -100 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 250 | mW |
| Tj | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -65 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|---------------|---|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | - | 500 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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10. Characteristics

Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified

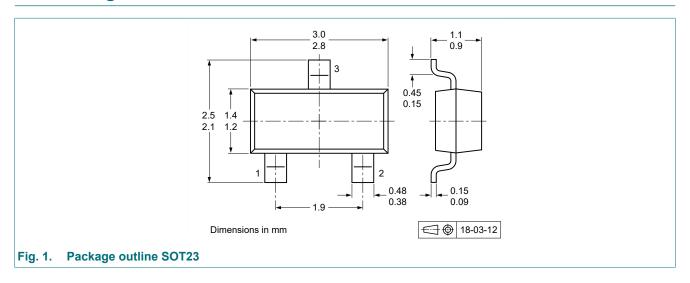
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|--------------------------------------|--|-----|-----|------|------|
| I _{CBO} | collector-base cut-off current | V _{CB} = -200 V; I _E = 0 A; T _{amb} = 25 °C | - | - | -250 | nA |
| I _{EBO} | emitter-base cut-off current | $V_{EB} = -3 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ | - | - | -100 | nA |
| h _{FE} | DC current gain | V_{CE} = -10 V; I_{C} = -1 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 25 | - | - | |
| | | V_{CE} = -10 V; I_{C} = -10 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 40 | - | - | |
| | | V_{CE} = -10 V; I_{C} = -30 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 25 | - | - | |
| V _{CEsat} | collector-emitter saturation voltage | $I_C = -20 \text{ mA}; I_B = -2 \text{ mA}; T_{amb} = 25 \text{ °C}$ | - | - | -500 | mV |
| V _{BEsat} | base-emitter saturation voltage | | - | - | -900 | mV |
| C _c | collector capacitance | $V_{CB} = -20 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ f = 1 MHz | - | - | 6 | pF |
| f _T | transition frequency | V_{CE} = -20 V; I_{C} = -10 mA; f = 100 MHz; T_{amb} = 25 °C | 50 | - | - | MHz |

11. Test information

Quality information

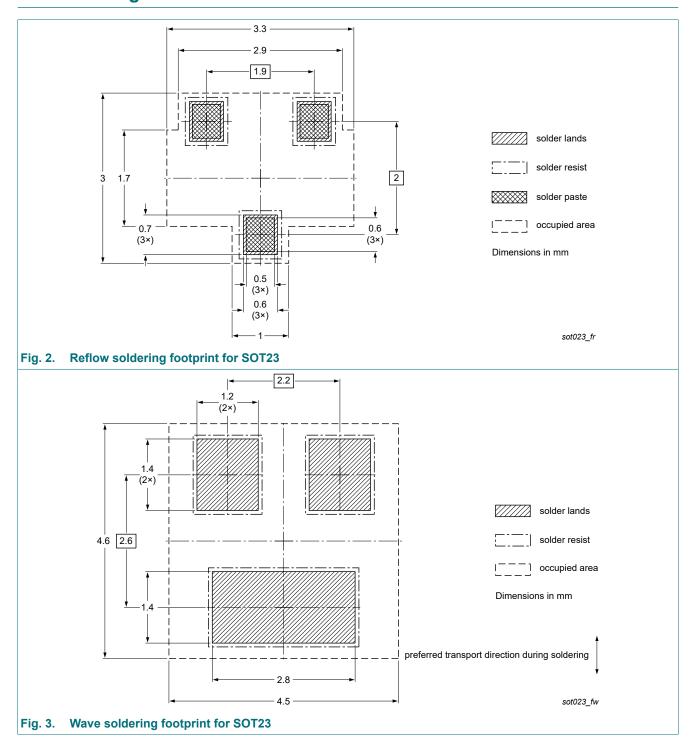
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|---------------|--------------|--------------------|---------------|-------------|
| MMBTA92-Q v.1 | 20230707 | Product data sheet | - | MMBTA92 v.2 |

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15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- Please consult the most recently issued document before initiating or completing a design.
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