

40 V, 3 A Schottky barrier rectifier 24 August 2023

1. General description

Planar Schottky barrier rectifier encapsulated in a CFP3 (SOD123W) power flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- High power capability due to clip bond package
- Small and flat lead SMD plastic package
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

A I I		A 11/1			-		
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 160 °C		-	-	3	A
V _R	reverse voltage	T _j = 25 °C		-	-	40	V
V _F	forward voltage	I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	560	630	mV
I _R	reverse current	V _R = 40 V; pulsed; T _j = 25 °C	[1]	-	10	50	μA
		V _R = 40 V; pulsed; T _j = 125 °C	[1]	-	6	25	mA

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		K 🔣 A
2	A	anode	CFP3 (SOD123W)	sym001

[1] The marking bar indicates the cathode.

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6. Ordering information

Table 3. Ordering information							
Type number Package							
	Name	Description	Version				
PMEG4030CER-Q	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W				

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG4030CER-Q	N8

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
I _F	forward current	δ = 1; T _{sp} ≤ 156 °C		-	4.2	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 160 °C		-	3	A
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.68	W
			[2]	-	1.15	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

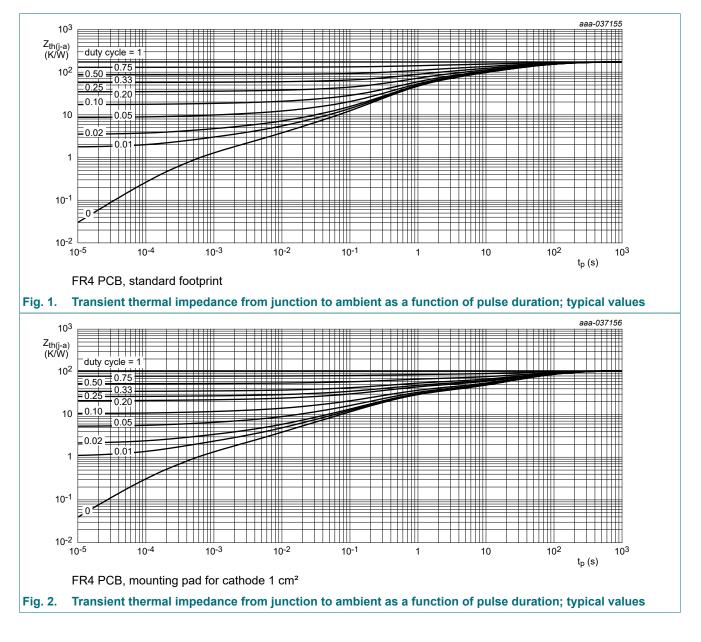
Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui()-a)	thermal resistance from	in free air	[1] [2]	-	-	220	K/W
	junction to ambient		[3] [2]	-	-	130	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	18	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

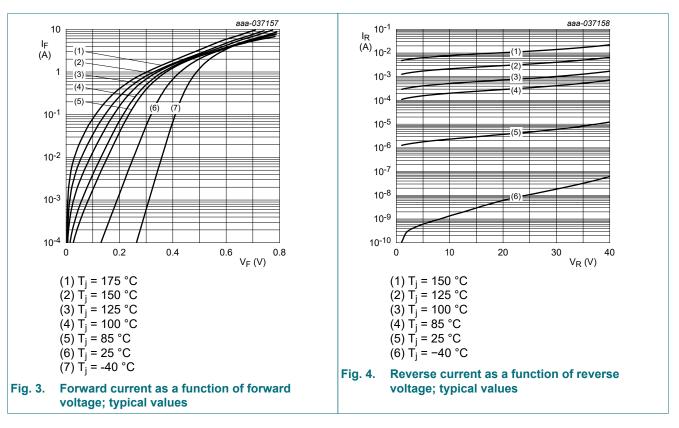
[4] Soldering point of cathode tab.



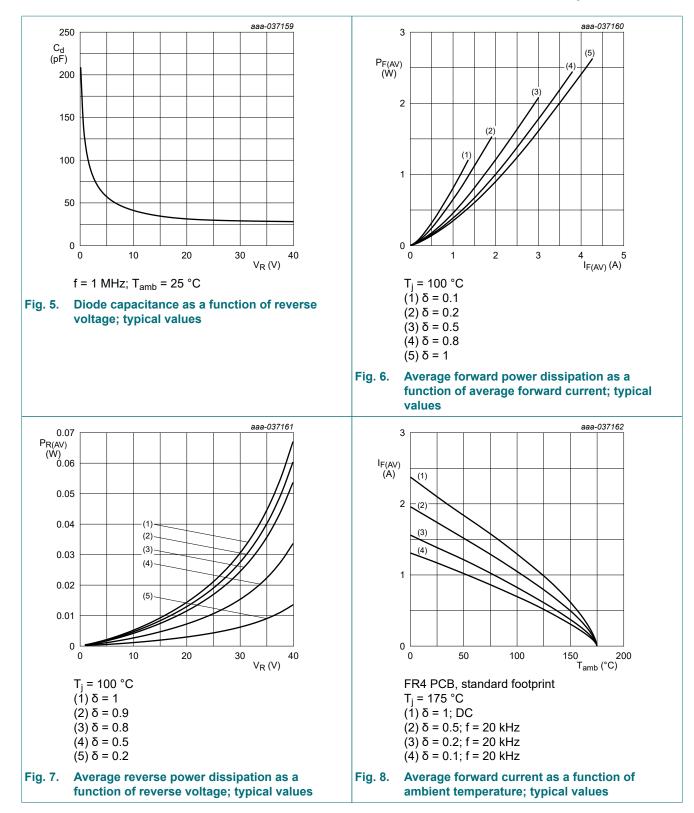
10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I _R = 1 mA; pulsed; T _j = 25 °C	[1]	40	-	-	V
V _F	forward voltage	I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	430	490	mV
		I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	560	630	mV
		I _F = 3 A; pulsed; T _j = -40 °C	[1]	-	580	650	mV
		I _F = 3 A; pulsed; T _j = 125 °C	[1]	-	530	620	mV
I _R reverse cur	reverse current	V_R = 40 V; pulsed; T _j = 25 °C	[1]	-	10	50	μA
		V _R = 40 V; pulsed; T _j = 125 °C	[1]	-	6	25	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	113	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	40	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	4.5	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 200 A/µs; I _F = 6 A; V _R = 26 V; T _j = 25 °C		-	5.5	-	ns
I _{RM}	peak reverse recovery current			-	0.6	-	A
Q _{rr}	reverse recovery charge			-	2.1	-	nC
V _{FRM}	peak forward recovery voltage	I _F = 0.5 A; dI _F /dt = 20 A/μs; T _j = 25 °C		-	420	-	mV
	1	1				1	1

[1] Very short pulse, in order to maintain a stable junction temperature.

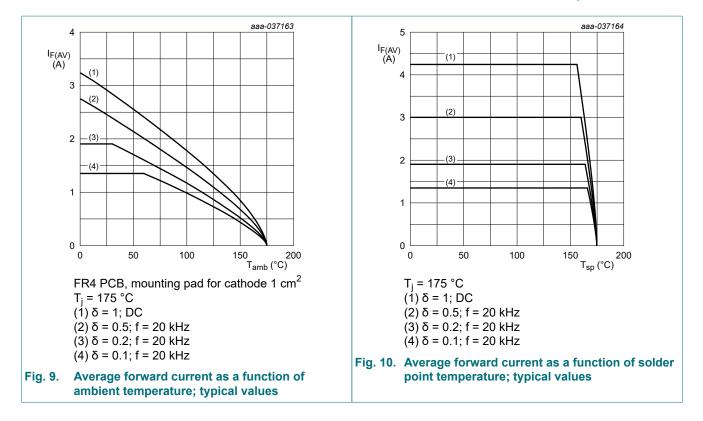


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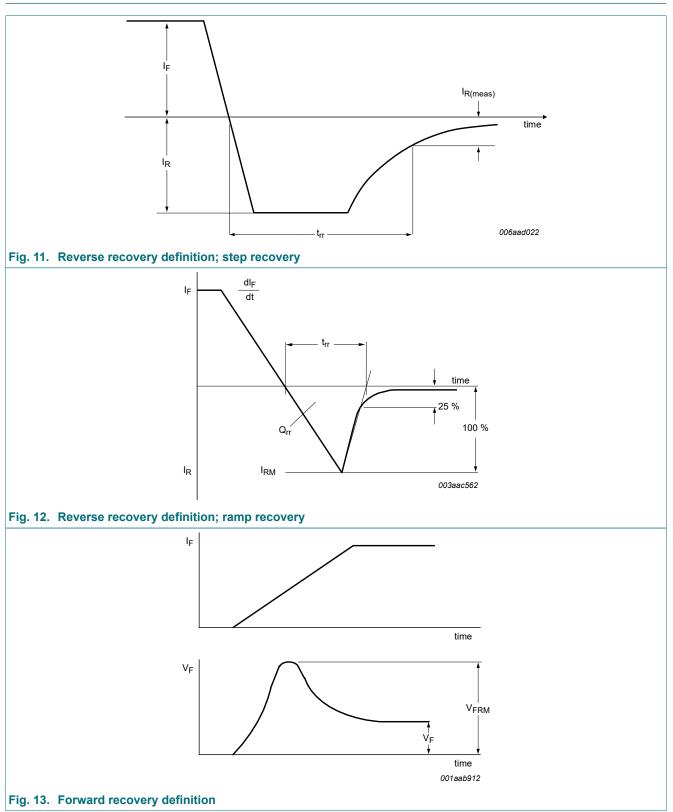
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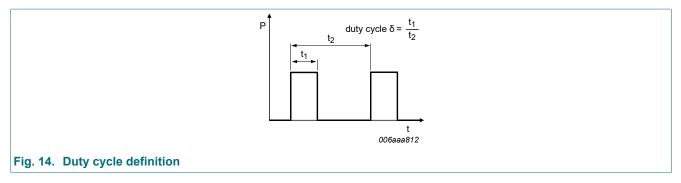
PMEG4030CER-Q

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11. Test information



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The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}=I_M \times \delta$ with I_M defined as peak current

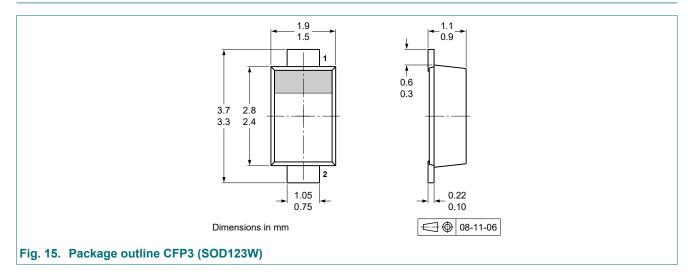
I_{RMS}=I_{F(AV)} at DC, and I_{RMS}=I_M×√δ

with I_{RMS} defined as RMS current.

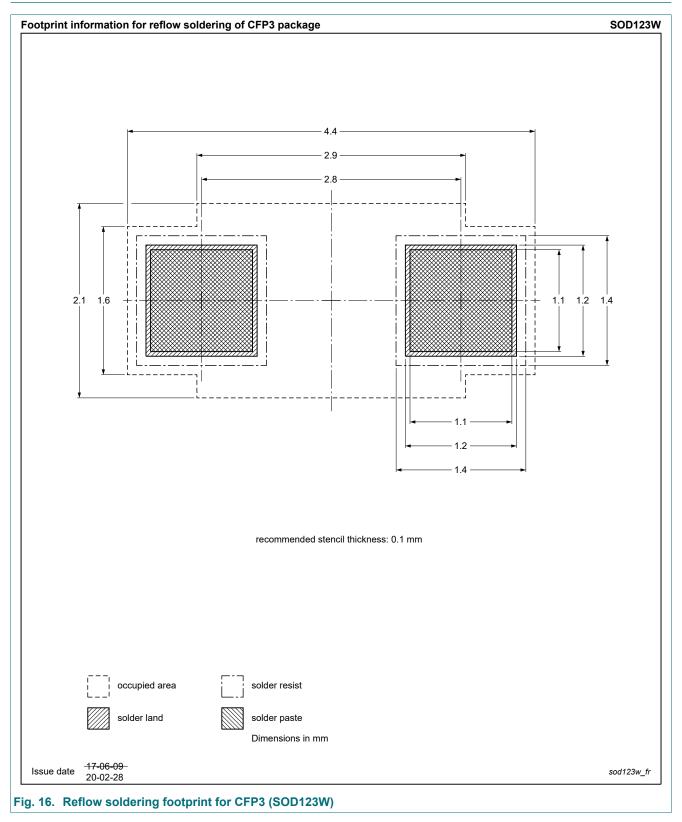
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

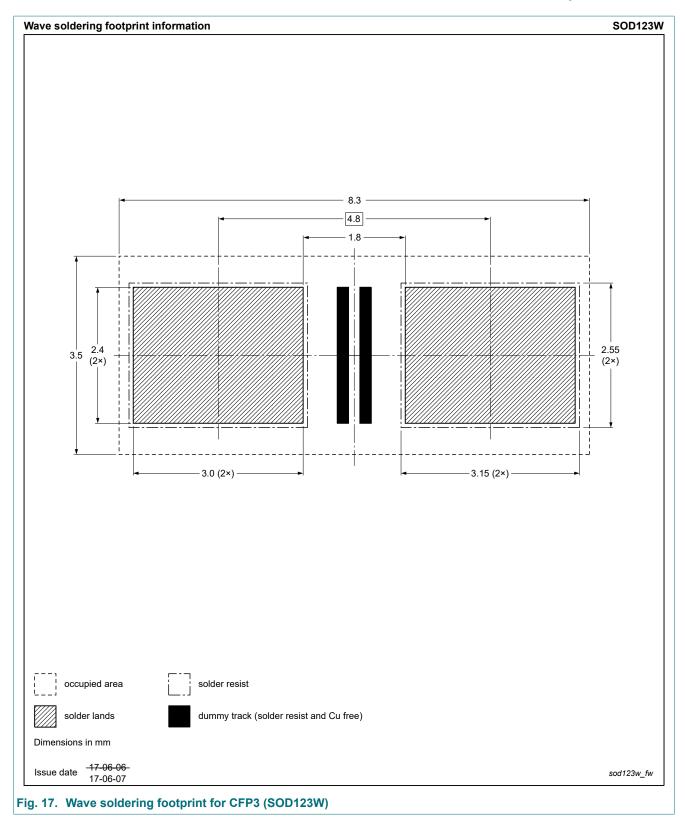
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4030CER-Q v.1	20230824	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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