1. General description

NPN/NPN general-purpose transistor with two independently operating transistors in a SOT363 (SC-88) very small Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Double general-purpose transistor
- Board-space reduction
- · Very small SMD plastic package
- AEC-Q101 qualified

3. Applications

· General-purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	100	mA
h _{FE}	DC current gain	V _{CE} = 6 V; I _C = 1 mA; T _{amb} = 25 °C		120	-	-	

5. Pinning information

Table 2. Pinning information

	able 2. Filling information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	E1	emitter TR1	По Пс П4	C1 B2 E2				
2	B1	base TR1	6 5 4					
3	C2	collector TR2		(TR1 TR2)				
4	E2	emitter TR2						
5	B2	base TR2	☐1 ☐2 ☐3 TSCORC (COTACA)					
6	C1	collector TR1	TSSOP6 (SOT363)	sym020				



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6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PUMX1	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PUMX1	Z%Z

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or	,	,	'	'	
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
Ic	collector current			-	100	mA
I _{CM}	peak collector current	single pulse		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C		-	200	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

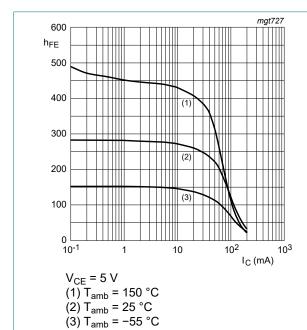
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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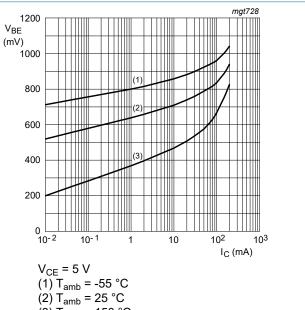
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	tor				'	
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 6 V; I _C = 1 mA; T _{amb} = 25 °C	120	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 50 mA; I_B = 5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	200	mV
C _c	collector capacitance	V_{CB} = 12 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	1.5	pF
f _T	transition frequency	V _{CE} = 12 V; I _C = 2 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz



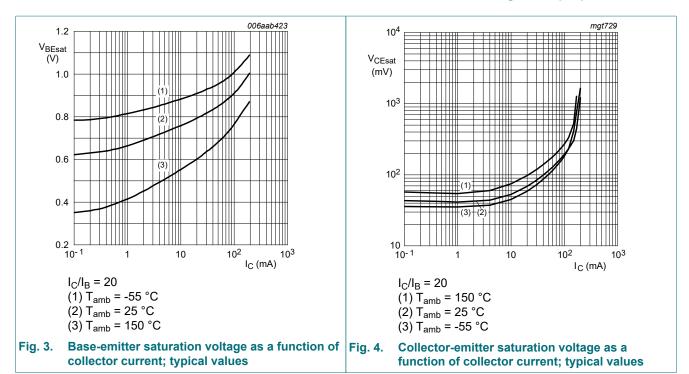
NPN transistor: DC current gain as a function of Fig. 2. Fig. 1. collector current; typical values



(3) T_{amb} = 150 °C

Base-emitter voltage as a function of collector current; typical values

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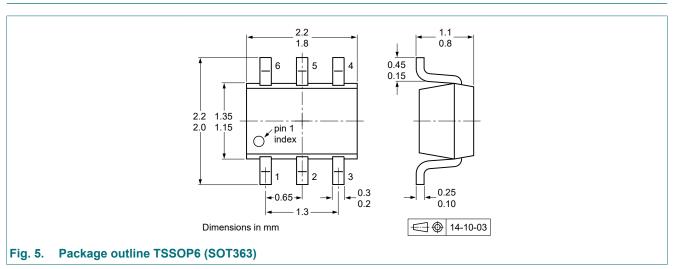


11. Test information

Quality information

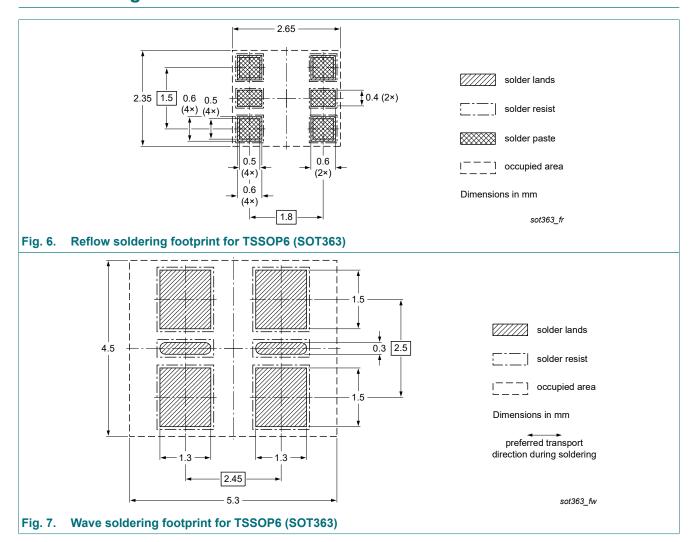
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Table 6. Revision i				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PUMX1 v.5	20241204	Product data sheet	-	PUMX1 v.4
Modifications:	Characteristics	s: Figures 1 - 4 added		
PUMX1 v.4	20100120	Product data sheet	-	PUMX1 v.3
PUMX1 v.3	19990414	Preliminary specification	-	PUMX1 v.2
PUMX1 v.2	19970709	Preliminary specification	-	PUMX1 v.1

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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PUMX1

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