



# PSMN019-100YL

N-channel 100 V, 19 mΩ logic level MOSFET in LPAK56

4 November 2016

Product data sheet

## 1. General description

Logic level N-channel MOSFET in an LPAK56 (Power SO8) package using TrenchMOS technology. This product is designed and qualified for use in a wide range of power supply & motor control equipment.

## 2. Features and benefits

- Advanced TrenchMOS provides low  $R_{DS(on)}$  and low gate charge
- Logic level gate operation
- Avalanche rated, 100 % tested
- LPAK provides maximum power density in a Power SO8 package

## 3. Applications

- Synchronous rectification in power supply equipment
- Chargers & adaptors with  $V_{out} < 10$  V
- Fast charge & USB-PD applications
- Battery powered motor control
- LED lighting & TV backlight

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{DS}$	drain-source voltage	$25\text{ °C} \leq T_j \leq 175\text{ °C}$	-	-	100	V
$I_D$	drain current	$V_{GS} = 5\text{ V}$ ; $T_{mb} = 25\text{ °C}$ ; <a href="#">Fig. 2</a>	-	-	56	A
$P_{tot}$	total power dissipation	$T_{mb} = 25\text{ °C}$ ; <a href="#">Fig. 1</a>	-	-	167	W
<b>Static characteristics</b>						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 5\text{ V}$ ; $I_D = 15\text{ A}$ ; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 11</a>	-	14.6	19	mΩ
<b>Dynamic characteristics</b>						
$Q_{GD}$	gate-drain charge	$I_D = 15\text{ A}$ ; $V_{DS} = 80\text{ V}$ ; $V_{GS} = 5\text{ V}$ ; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>	-	14.1	-	nC

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## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source	 <p><b>LPAK56; Power-SO8 (SOT669)</b></p>	
2	S	source		
3	S	source		
4	G	gate		
mb	D	mounting base; connected to drain		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN019-100YL	LPAK56; Power-SO8	Plastic single-ended surface-mounted package (LPAK56; Power-SO8); 4 leads	SOT669

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PSMN019-100YL	19L100

## 8. Limiting values

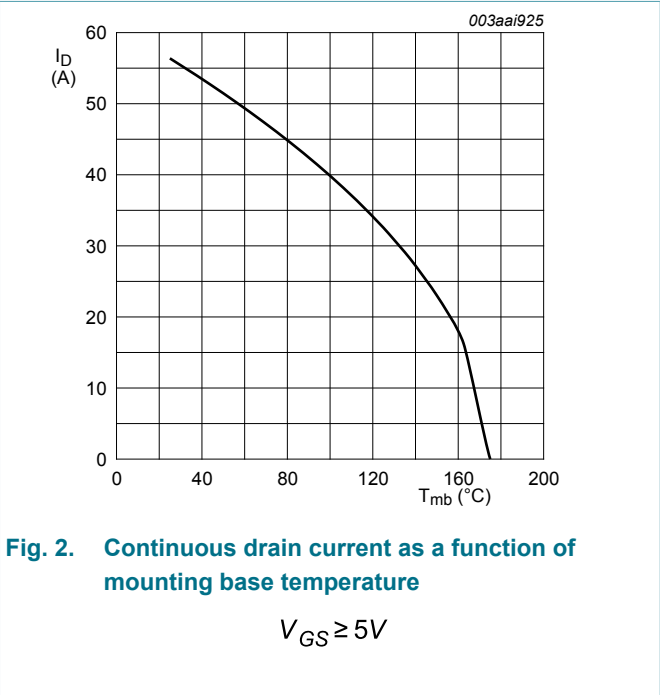
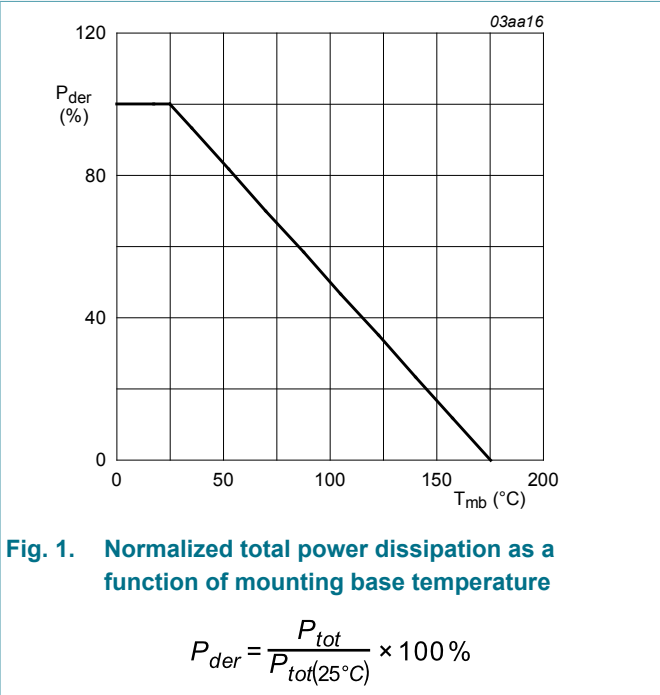
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage	$25\text{ °C} \leq T_j \leq 175\text{ °C}$	-	100	V
$V_{DGR}$	drain-gate voltage	$R_{GS} = 20\text{ k}\Omega$	-	100	V
$V_{GS}$	gate-source voltage		-20	20	V
$P_{tot}$	total power dissipation	$T_{mb} = 25\text{ °C}$ ; Fig. 1	-	167	W
$I_D$	drain current	$V_{GS} = 5\text{ V}$ ; $T_{mb} = 25\text{ °C}$ ; Fig. 2	-	56	A
		$V_{GS} = 5\text{ V}$ ; $T_{mb} = 100\text{ °C}$ ; Fig. 2	-	40	A
$I_{DM}$	peak drain current	pulsed; $t_p \leq 10\text{ }\mu\text{s}$ ; $T_{mb} = 25\text{ °C}$ ; Fig. 3	-	226	A
$T_{stg}$	storage temperature		-55	175	°C

Symbol	Parameter	Conditions		Min	Max	Unit
T <sub>j</sub>	junction temperature			-55	175	°C
Source-drain diode						
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C		-	56	A
I <sub>SM</sub>	peak source current	pulsed; t <sub>p</sub> ≤ 10 μs; T <sub>mb</sub> = 25 °C		-	226	A
Avalanche ruggedness						
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	I <sub>D</sub> = 56 A; V <sub>sup</sub> ≤ 100 V; R <sub>GS</sub> = 50 Ω; V <sub>GS</sub> = 5 V; T <sub>j(init)</sub> = 25 °C; unclamped; Fig. 4	[1][2]	-	94.1	mJ

[1] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.  
[2] Refer to application note AN10273 for further information.



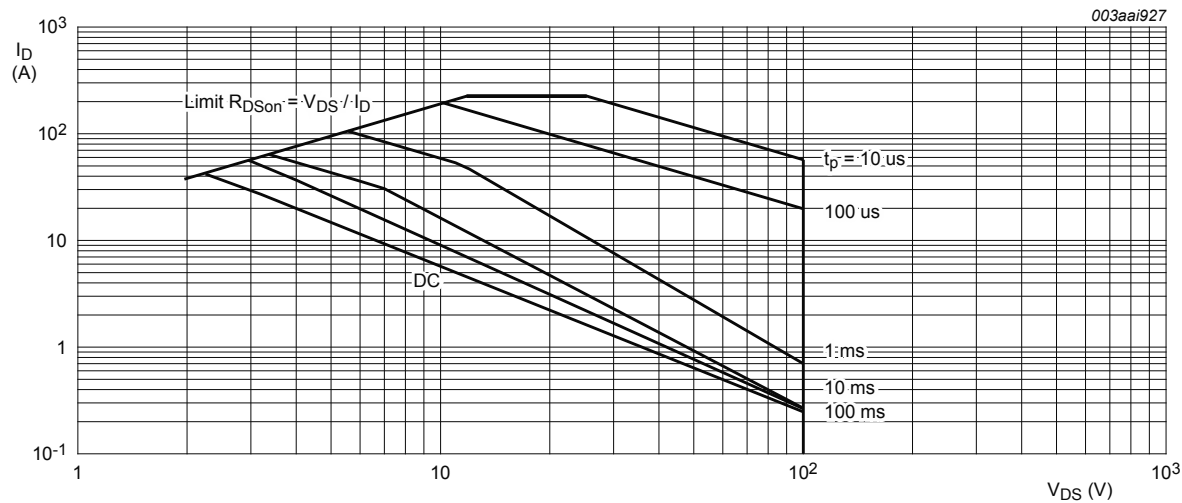


Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^{\circ}\text{C}$ ;  $I_{DM}$  is a single pulse

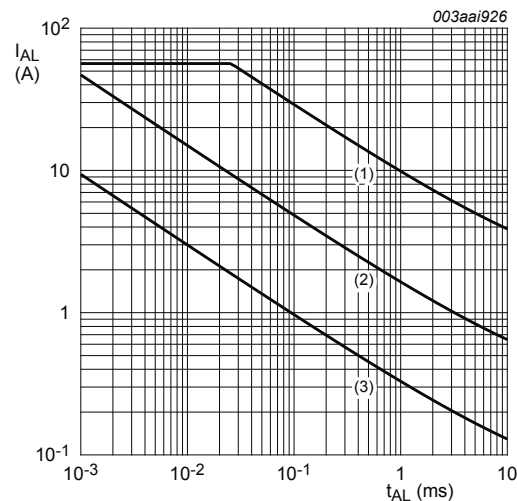


Fig. 4. Avalanche rating; avalanche current as a function of avalanche time

(1)  $T_{j\text{ (init)}} = 25^{\circ}\text{C}$ ; (2)  $T_{j\text{ (init)}} = 150^{\circ}\text{C}$ ; (3) Repetitive Avalanche

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5	-	-	0.9	K/W

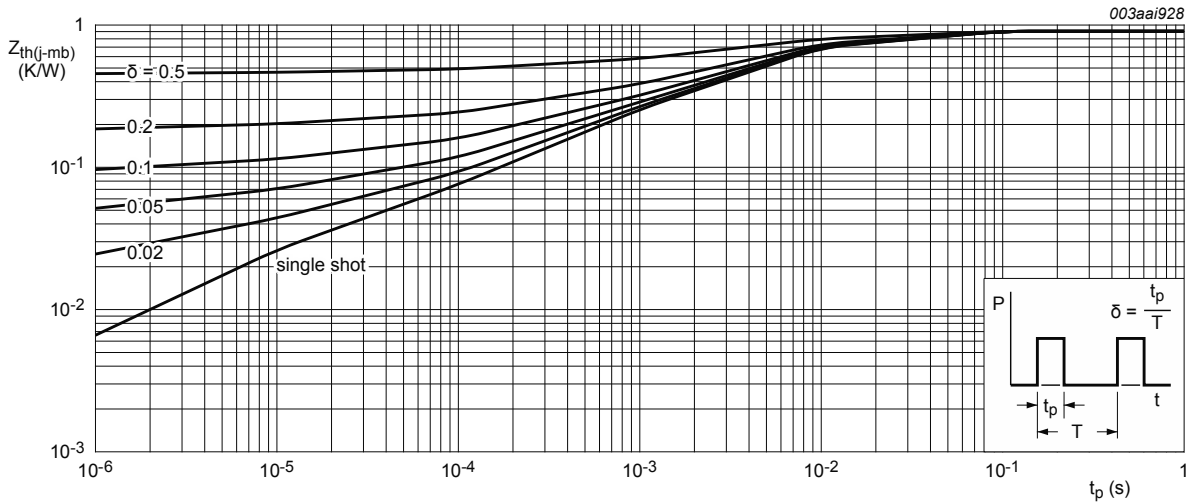


Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250\text{ }\mu\text{A}$ ; $V_{GS} = 0\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$		100	-	-	V
		$I_D = 250\text{ }\mu\text{A}$ ; $V_{GS} = 0\text{ V}$ ; $T_j = -55\text{ }^\circ\text{C}$		90	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1\text{ mA}$ ; $V_{DS}=V_{GS}$ ; $T_j = 25\text{ }^\circ\text{C}$ ; <a href="#">Fig. 9</a> ; <a href="#">Fig. 10</a>		1.4	1.7	2.1	V
		$I_D = 1\text{ mA}$ ; $V_{DS}=V_{GS}$ ; $T_j = -55\text{ }^\circ\text{C}$ ; <a href="#">Fig. 9</a>		-	-	2.45	V
		$I_D = 1\text{ mA}$ ; $V_{DS}=V_{GS}$ ; $T_j = 175\text{ }^\circ\text{C}$ ; <a href="#">Fig. 9</a>		0.5	-	-	V
$I_{DSS}$	drain leakage current	$V_{DS} = 100\text{ V}$ ; $V_{GS} = 0\text{ V}$ ; $T_j = 175\text{ }^\circ\text{C}$		-	-	500	$\mu\text{A}$
		$V_{DS} = 100\text{ V}$ ; $V_{GS} = 0\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$		-	0.04	10	$\mu\text{A}$
$I_{GSS}$	gate leakage current	$V_{GS} = 16\text{ V}$ ; $V_{DS} = 0\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$		-	2	100	nA
		$V_{GS} = -16\text{ V}$ ; $V_{DS} = 0\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$		-	2	100	nA
$R_{DSon}$	drain-source on-state resistance	$V_{GS} = 5\text{ V}$ ; $I_D = 15\text{ A}$ ; $T_j = 25\text{ }^\circ\text{C}$ ; <a href="#">Fig. 11</a>		-	14.6	19	m $\Omega$
		$V_{GS} = 10\text{ V}$ ; $I_D = 15\text{ A}$ ; $T_j = 25\text{ }^\circ\text{C}$ ; <a href="#">Fig. 11</a>		-	14	18	m $\Omega$
		$V_{GS} = 5\text{ V}$ ; $I_D = 15\text{ A}$ ; $T_j = 175\text{ }^\circ\text{C}$ ; <a href="#">Fig. 11</a> ; <a href="#">Fig. 12</a>		-	-	52.4	m $\Omega$
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$I_D = 15\text{ A}$ ; $V_{DS} = 80\text{ V}$ ; $V_{GS} = 10\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$ ; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	72.4	-	nC
		$I_D = 15\text{ A}$ ; $V_{DS} = 80\text{ V}$ ; $V_{GS} = 5\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$ ; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	39	-	nC
$Q_{GS}$	gate-source charge	$T_j = 25\text{ }^\circ\text{C}$ ; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	8.5	-	nC

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Q <sub>GD</sub>	gate-drain charge			-	14.1	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 25 V; V <sub>GS</sub> = 0 V; f = 1 MHz; T <sub>j</sub> = 25 °C; <a href="#">Fig. 15</a>		-	3814	5085	pF
C <sub>oss</sub>	output capacitance			-	222	266	pF
C <sub>rss</sub>	reverse transfer capacitance			-	133	182	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 80 V; R <sub>L</sub> = 5 Ω; V <sub>GS</sub> = 5 V; R <sub>G(ext)</sub> = 5 Ω; T <sub>j</sub> = 25 °C		-	18.5	-	ns
t <sub>r</sub>	rise time			-	36.8	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	59.6	-	ns
t <sub>f</sub>	fall time			-	34.3	-	ns
Source-drain diode							
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 15 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C; <a href="#">Fig. 16</a>		-	0.8	1.2	V
t <sub>rr</sub>	reverse recovery time	I <sub>S</sub> = 15 A; dI <sub>S</sub> /dt = -100 A/μs; V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 25 V; T <sub>j</sub> = 25 °C		-	38	-	ns
Q <sub>r</sub>	recovered charge			-	56	-	nC

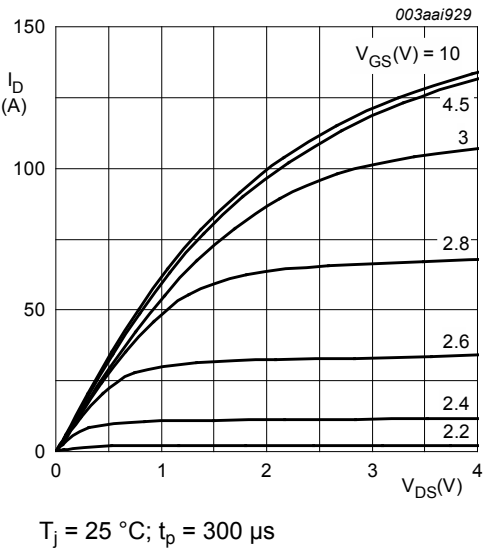


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

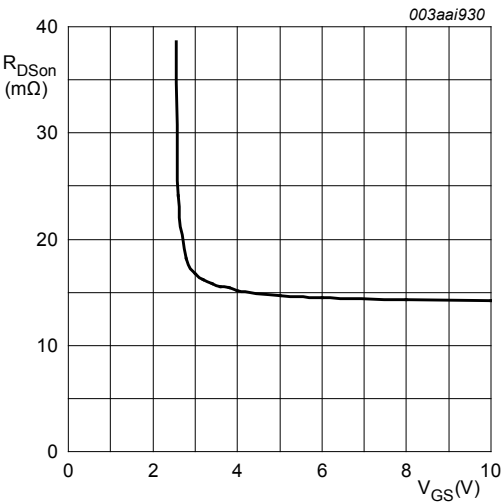


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

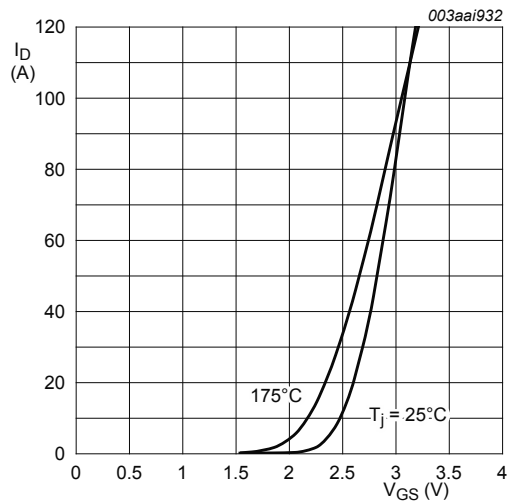


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

$V_{DS} = 10V$

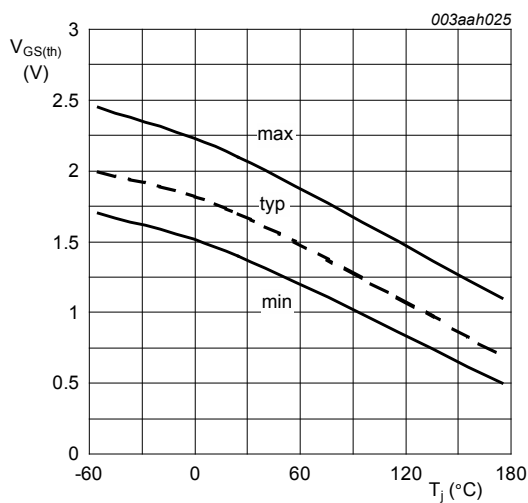


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$I_D = 1\text{ mA}; V_{DS} = V_{GS}$

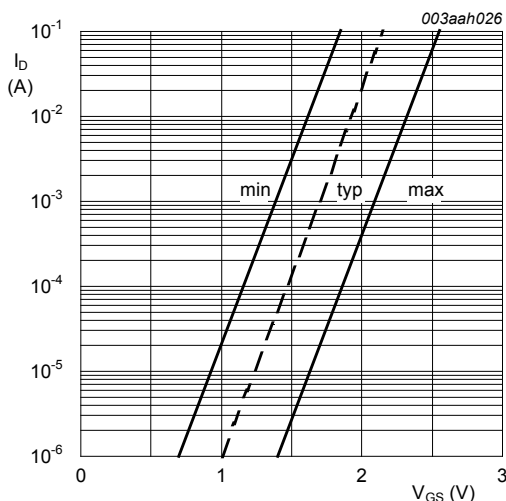
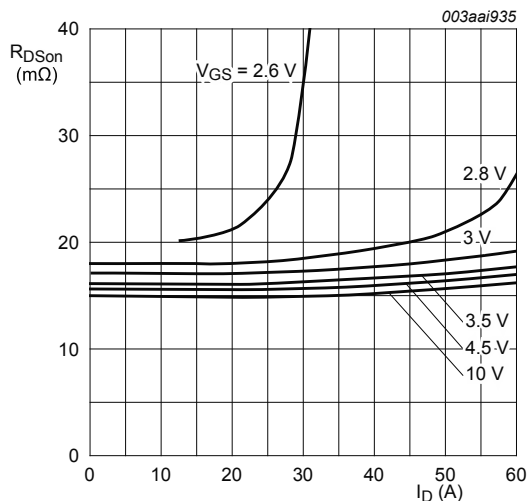


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$T_j = 25^\circ\text{C}; V_{DS} = 5V$



$T_j = 25^\circ\text{C}; t_p = 300\text{ }\mu\text{s}$

Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

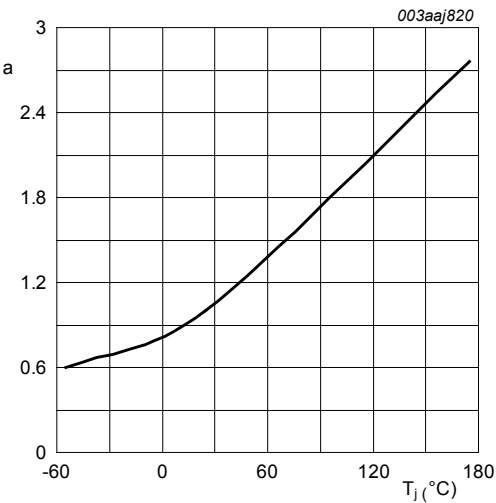


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon}(25^{\circ}\text{C})}$$

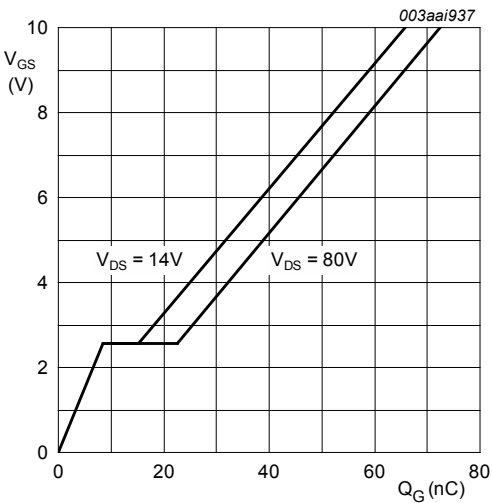


Fig. 13. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25^{\circ}\text{C}; I_D = 15\text{A}$$

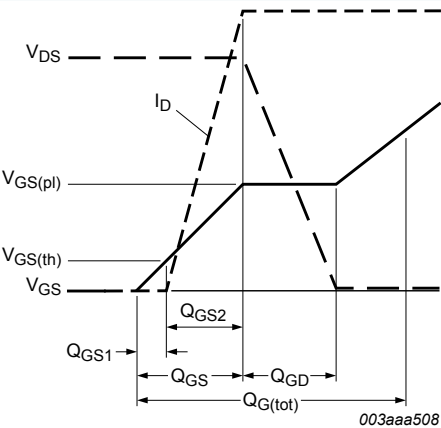


Fig. 14. Gate charge waveform definitions

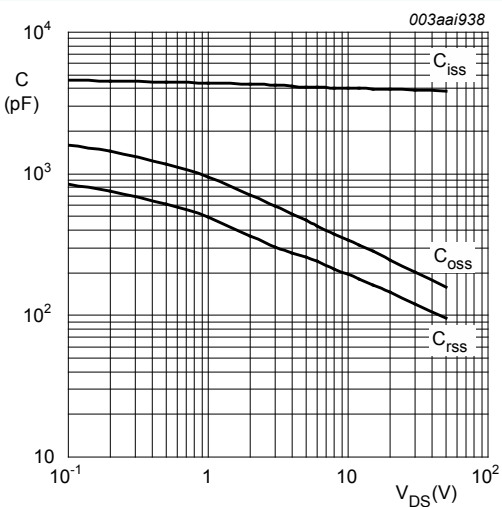


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0\text{V}; f = 1\text{MHz}$$



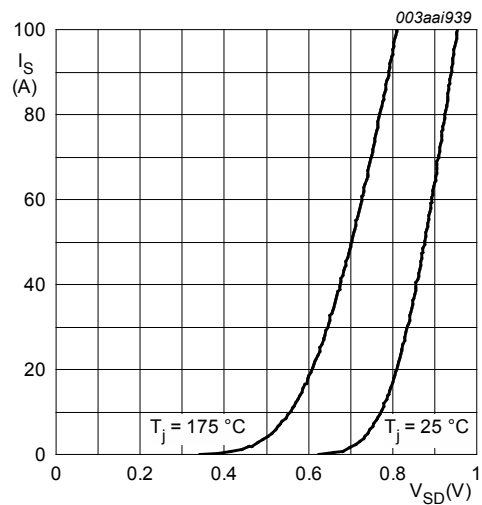


Fig. 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values  
 $V_{GS} = 0V$

11. Package outline

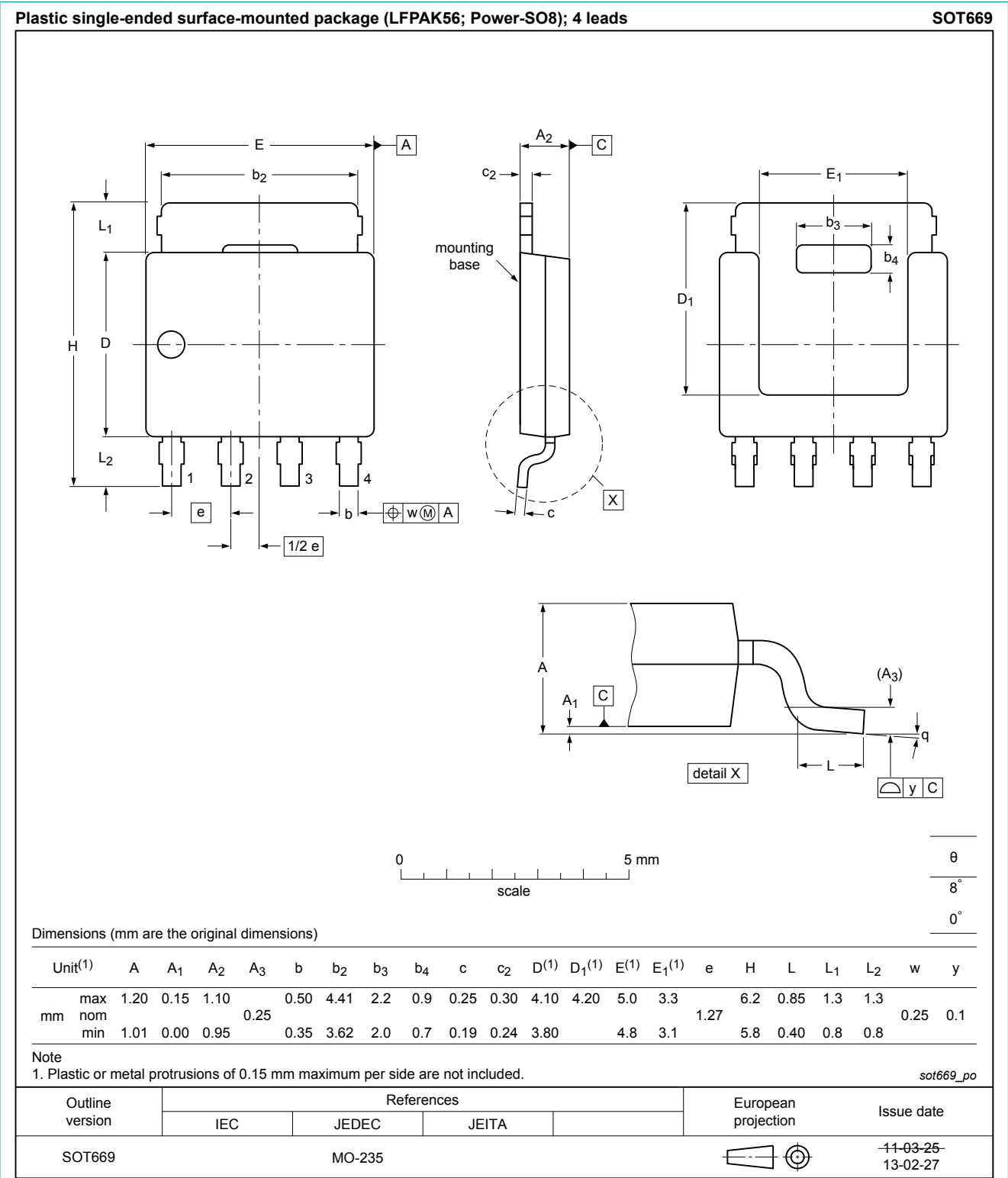


Fig. 17. Package outline LPAK56; Power-SO8 (SOT669)

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Document status [1][2]	Product status [3]	Definition
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