Product data sheet

1. General description

NPN medium power transistors in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		PNP comlement
	Nexperia	JEDEC	
BCP56-Q	SOT223	SC-73	BCP53-Q
BCP56-10-Q			BCP53-10-Q
BCP56-16-Q			BCP53-16-Q

2. Features and benefits

- High collector current capability I_{C} and I_{CM}
- Three current gain selections
- High power dissipation capability
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Linear voltage regulators
- MOSFET drivers
- Low-side switches
- Power management
- **Amplifiers**
- Battery-driven devices



4. Quick reference data

Table 2. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	80	V
Ic	collector current			-	-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	2	А
h _{FE}	DC current gain						
	BCP56-Q	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
	BCP56-10-Q		[1]	63	-	160	
	BCP56-16-Q		[1]	100	-	250	

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$

5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	Е	emitter		В
4	С	collector	□1 □2 □3	Ë
				sym123

6. Ordering information

Table 4. Ordering information

Type number	Package						
	Name	Description	Version				
BCP56-Q	SC-73	, , , , , , , , , , , , , , , , , , , ,	SOT223				
BCP56-10-Q		leads					
BCP56-16-Q							

7. Marking

Table 5. Marking

Type number	Marking code
BCP56-Q	BCP56
BCP56-10-Q	BCP56/10
BCP56-16-Q	BCP56/16

8. Limiting values

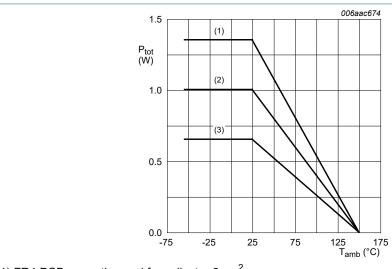
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		100	V
V _{CEO}	collector-emitter voltage	open base		-	80	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current				1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		2	Α
I _B	base current				0.3	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	0.3	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves

9. Thermal characteristics

Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]			125	K/W
			[3]			93	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm²
- [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².

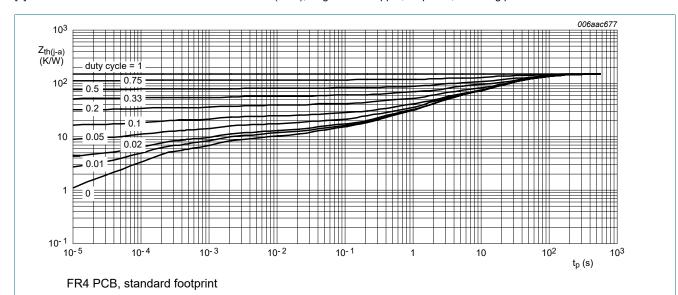
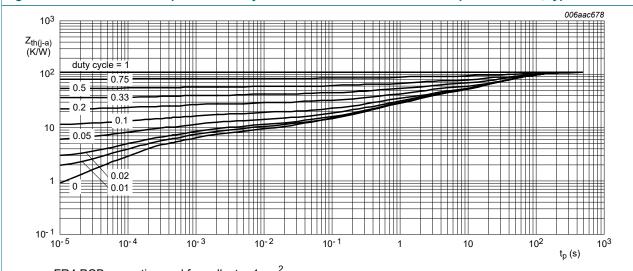
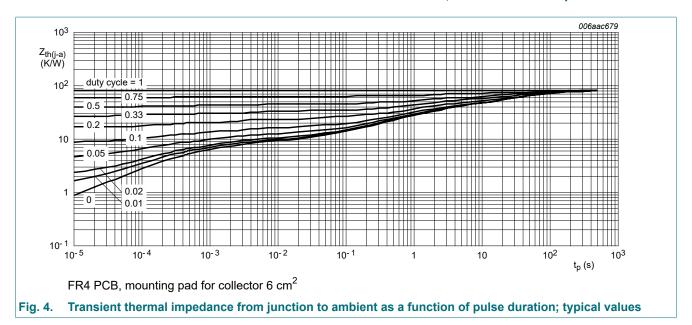


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



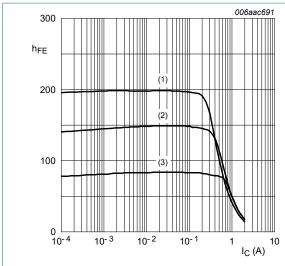
10. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		100	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A		80	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		5	-	-	V
I _{CBO}	collector-base	V _{CB} = 30 V; I _E = 0 A		-	-	100	nA
	cut-off current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain				'	'	
	BCP56-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCP56-10-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	160	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCP56-16-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA		100	-	250	
		V _{CE} = 2 V; I _C = 500 mA		40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	500	mV
V_{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA	[1]	-	-	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	6	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz		100	180	-	MHz

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$



$$V_{CE} = 2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 5. DC current gain as a function of collector current; typical values

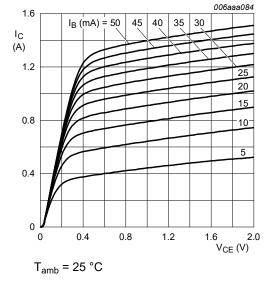
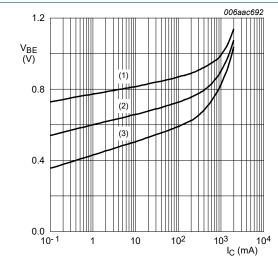


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

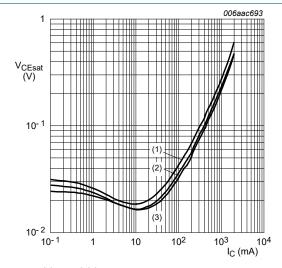


 $V_{CE} = 2 V$

(1)
$$T_{amb} = -55$$
 °C

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



$$V_{CE}$$
 = 2 V

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

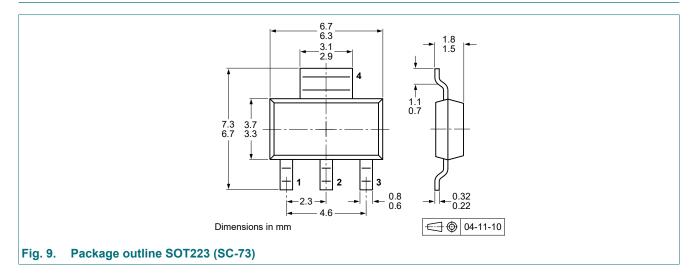
Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

11. Test information

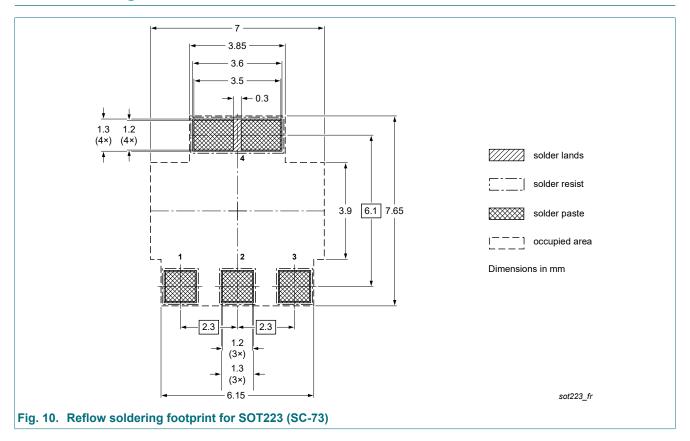
11.1. Quality information

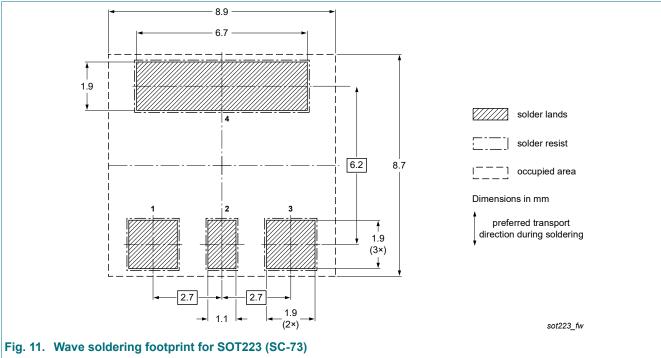
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





14. Revision history

Table 9. Revision history

rabic 3. Revision mistory				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP56-Q_SER v.2	20220624	Product data sheet	-	BCP56-Q_SER v.1
Modifications:	Characteristics at	V _{(BR)CEO} : Conditions corr	rected	
BCP56-Q_SER v.1	20210623	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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BCP56-Q_SER

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