Product data sheet

1. General description

NPN high-voltage transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low current (max. 50 mA)
- High voltage (max. 300 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

· Video output stages

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	300	V
I _C	collector current		-	-	50	mA
h _{FE}	DC current gain	$V_{CE} = 20 \text{ V}; I_{C} = 25 \text{ mA}; T_{amb} = 25 \text{ °C}$	50	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		С
2	С	collector		B /
3	В	base	3 2 1 SOT89	B—————————————————————————————————————

6. Ordering information

Table 3. Ordering information

Type number	Package	ackage				
	Name	Description	Version			
BF620-Q		plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89			



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7. Marking

Table 4. Marking codes

Type number	Marking code
BF620-Q	DC

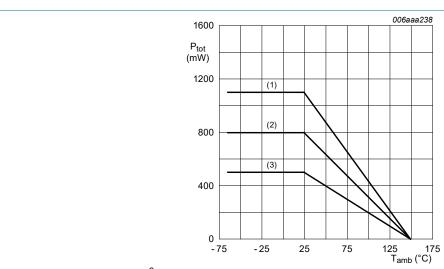
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	300	V
V_{CEO}	collector-emitter voltage	open base		-	300	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	50	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	100	mA
I _{BM}	peak base current			-	50	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.5	W
			[2]	-	0.8	W
			[3]	-	1.1	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm². Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².
- [3]



- (1) FR4 PCB; 6 cm² mounting pad for collector. (2) FR4 PCB; 1 cm² mounting pad for collector.
- (3) FR4 PCB; standard footprint.

Fig. 1. **Power derating curves**

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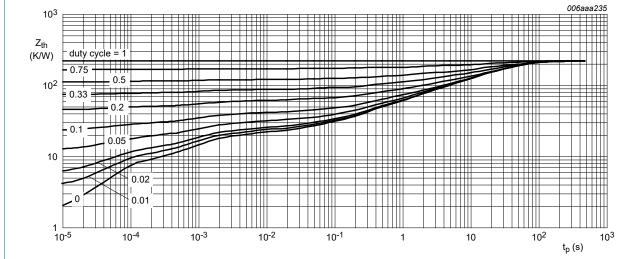
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9. Thermal characteristics

Table 6. Thermal characteristics

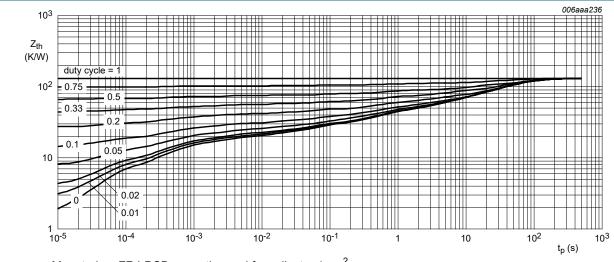
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uiy-a)	thermal resistance from		[1]	-	-	250	K/W
	junction to ambient		[2]	-	-	156	K/W
			[3]	-	-	113	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	30	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



Mounted on FR4 PCB; standard footprint.

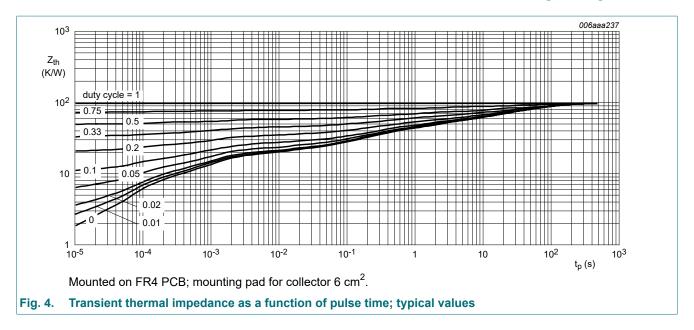
Fig. 2. Transient thermal impedance as a function of pulse time; typical values



Mounted on FR4 PCB; mounting pad for collector 1 cm².

Fig. 3. Transient thermal impedance as a function of pulse time; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 200 V; I _E = 0 A; T _{amb} = 25 °C	-	-	10	nA
	current	V _{CB} = 200 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	50	nA
h _{FE}	DC current gain	V _{CE} = 20 V; I _C = 25 mA; T _{amb} = 25 °C	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 30 \text{ mA}; I_B = 5 \text{ mA}; T_{amb} = 25 ^{\circ}C$	-	-	600	mV
C _{re}	feedback capacitance	i_c = 0 A; V_{CB} = 30 V; f = 1 MHz; I_C = 0 A; T_{amb} = 25 °C	-	-	1.6	pF
f _T	transition frequency	$V_{CE} = 10 \text{ V}; I_{C} = -10 \text{ mA}; f = 100 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	60	-	-	MHz

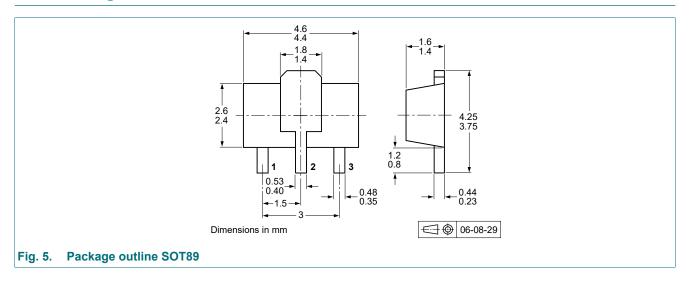
11. Test information

Quality information

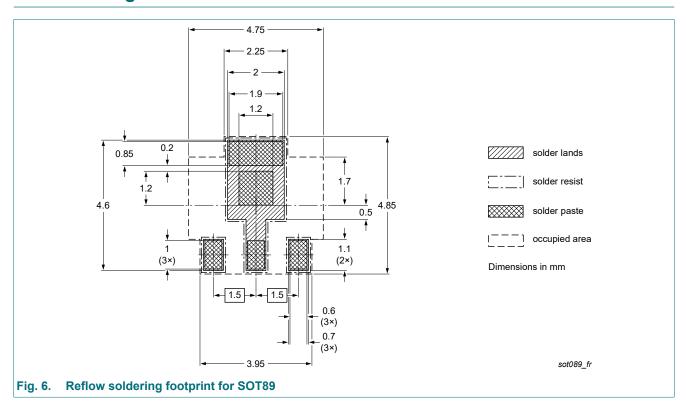
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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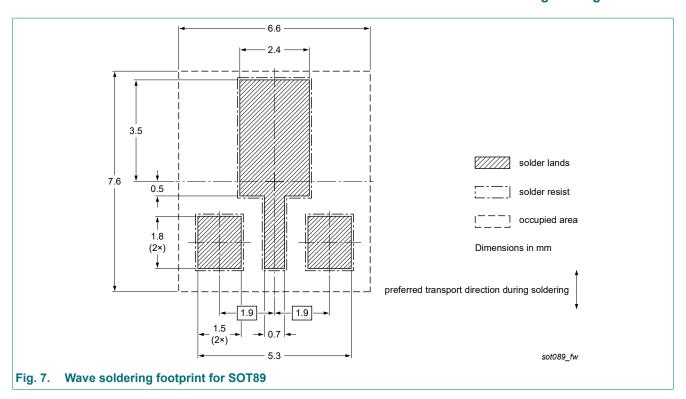
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BF620-Q v.1	20230623	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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