



PBSS301NX

12 V, 5.3 A NPN low V_{CEsat} (BISS) transistor

6 February 2024

Product data sheet

1. General description

NPN low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a SOT89 (SC-62/TO-243) small and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS301PX.

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- AEC-Q101 qualified

3. Applications

- DC-to-DC conversion
- MOSFET gate driving
- Motor control
- Charging circuits
- Power switches (e.g. motors, fans)

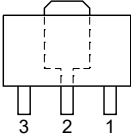
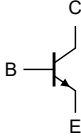
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	12	V
I _C	collector current		-	-	5.3	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	10.6	A
R _{CEsat}	collector-emitter saturation resistance	I _C = 4 A; I _B = 200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	28	40	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym123
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS301NX	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS301NX	%5B

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

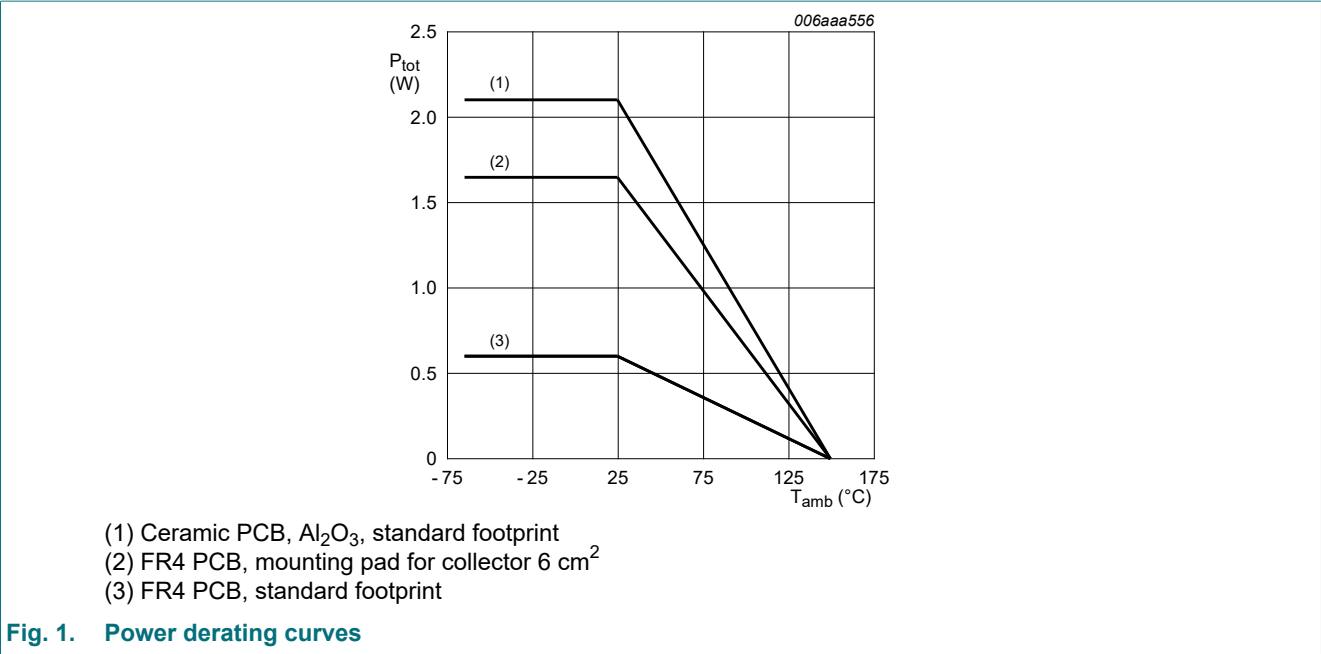
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	12	V
V _{CEO}	collector-emitter voltage	open base		-	12	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	5.3	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	10.6	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.6	W
			[2]	-	1.65	W
			[3]	-	2.1	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

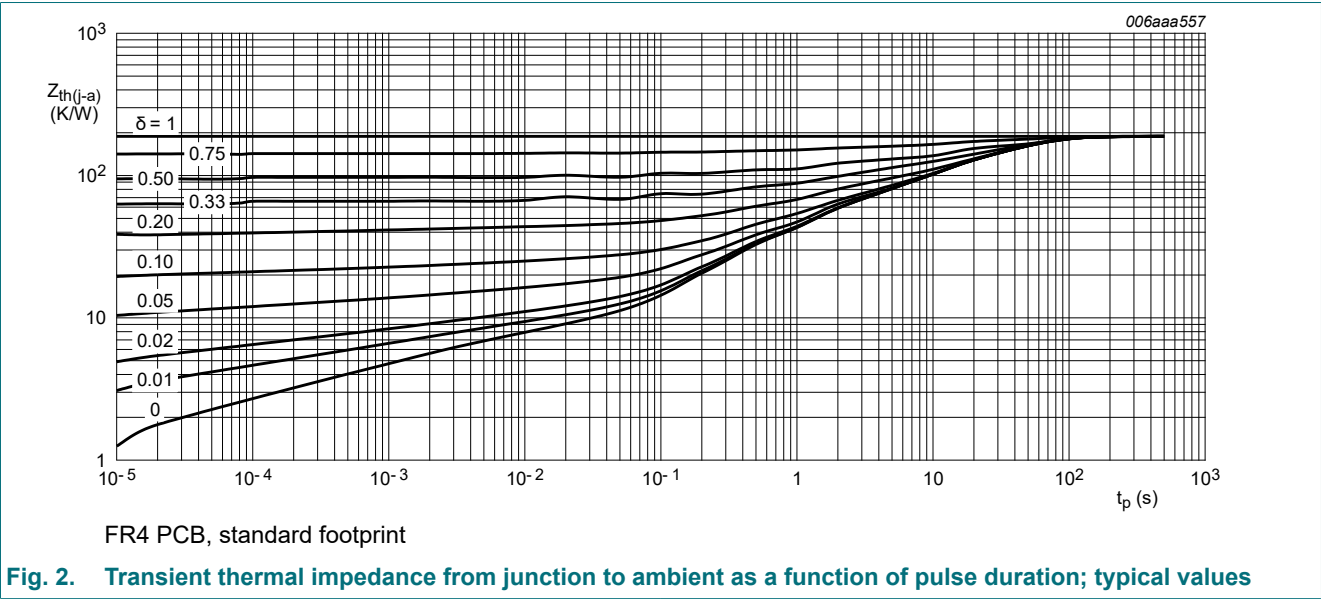


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	208	K/W
			[2]	-	-	76	K/W
			[3]	-	-	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



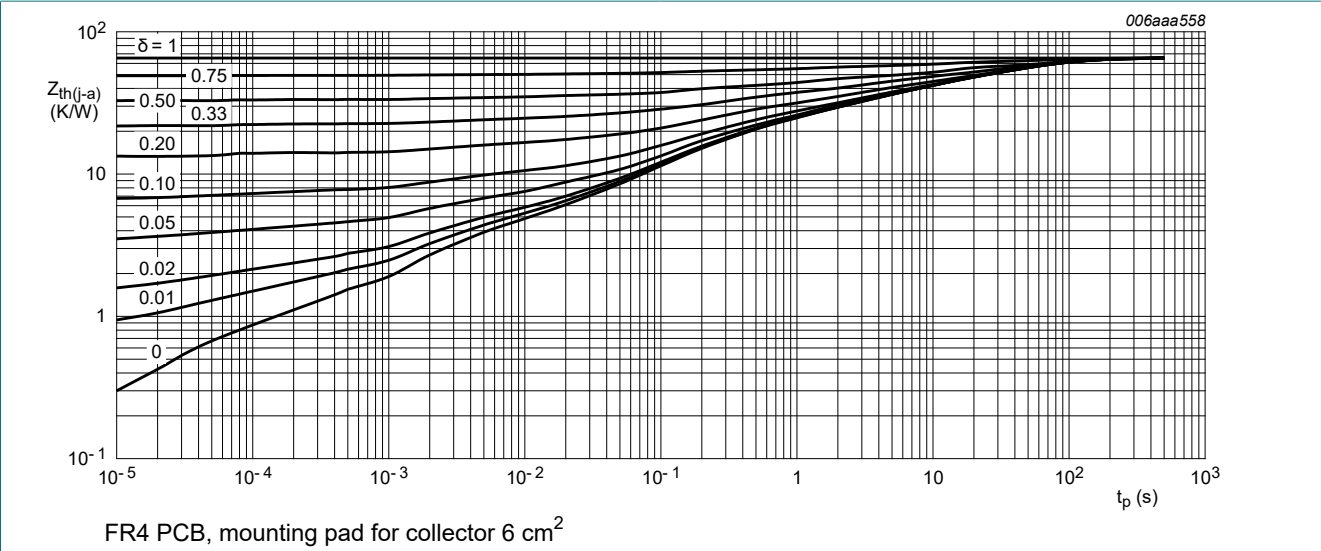


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

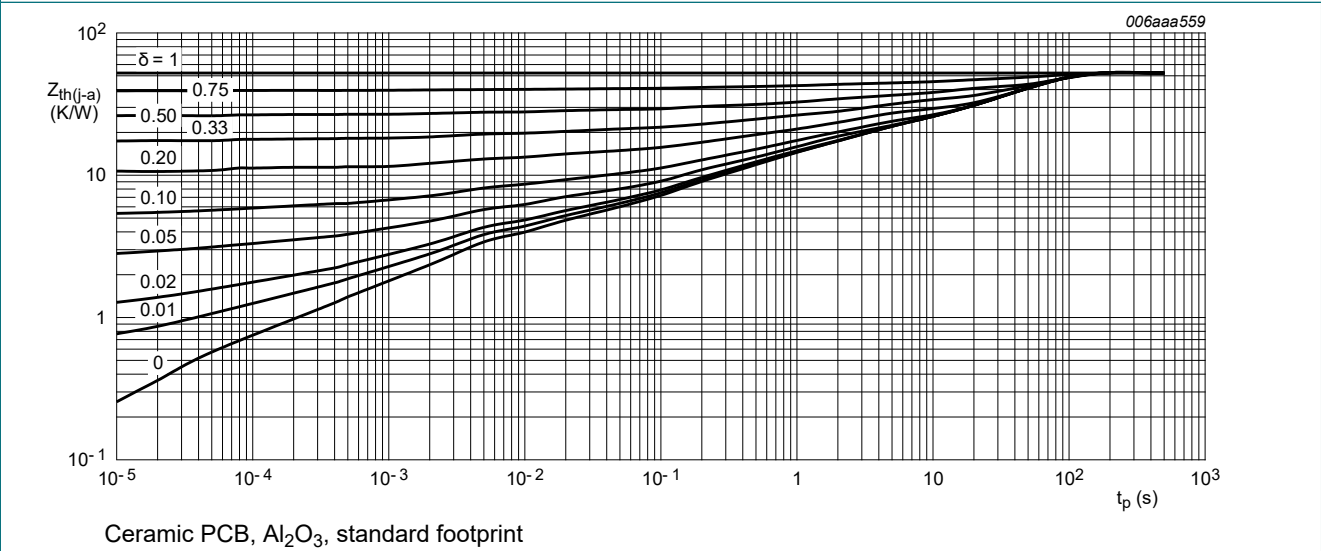


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
ICBO	collector-base cut-off current	VCB = 12 V; IE = 0 A; Tamb = 25 °C		-	-	100	nA
		VCB = 12 V; IE = 0 A; Tj = 150 °C		-	-	50	µA
IEBO	emitter-base cut-off current	VEB = 5 V; IC = 0 A; Tamb = 25 °C		-	-	100	nA
hFE	DC current gain	VCE = 2 V; IC = 0.5 A; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		300	530	-	
		VCE = 2 V; IC = 1 A; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		300	520	-	
		VCE = 2 V; IC = 2 A; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		250	480	-	
		VCE = 2 V; IC = 4 A; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		200	420	-	
		VCE = 2 V; IC = 6 A; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		200	340	-	
VCEsat	collector-emitter saturation voltage	IC = 0.5 A; IB = 50 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	18	25	mV
		IC = 1 A; IB = 50 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	35	50	mV
		IC = 1 A; IB = 10 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	50	70	mV
		IC = 2 A; IB = 40 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	70	100	mV
		IC = 4 A; IB = 200 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	110	160	mV
		IC = 4 A; IB = 400 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	100	140	mV
		IC = 4 A; IB = 40 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	125	190	mV
		IC = 5.3 A; IB = 265 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	140	200	mV
RCEsat	collector-emitter saturation resistance	IC = 4 A; IB = 200 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	28	40	mΩ
		IC = 4 A; IB = 40 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	32	48	mΩ
VBEsat	base-emitter saturation voltage	IC = 1 A; IB = 100 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	0.81	0.9	V
		IC = 4 A; IB = 400 mA; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	0.92	1.05	V
VBEon	base-emitter turn-on voltage	VCE = 2 V; IC = 2 A; pulsed; tp ≤ 300 µs; δ ≤ 0.02; Tamb = 25 °C		-	0.75	0.85	V
td	delay time	VCC = 12.5 V; IC = 3 A; IBon = 0.15 A; IBoff = -0.15 A; Tamb = 25 °C		-	15	-	ns
tr	rise time			-	40	-	ns
ton	turn-on time			-	55	-	ns
ts	storage time			-	195	-	ns
tf	fall time			-	75	-	ns
toff	turn-off time			-	270	-	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
f_T	transition frequency	$V_{CE} = 10\text{ V}$; $I_C = 3\text{ A}$; $f = 100\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	-	140	-	MHz
C_c	collector capacitance	$V_{CB} = 10\text{ V}$; $I_E = 0\text{ A}$; $i_e = 0\text{ A}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	-	125	160	pF

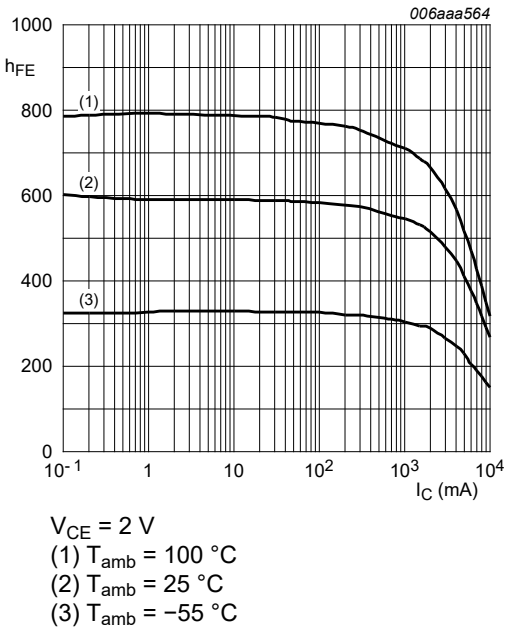


Fig. 5. DC current gain as a function of collector current; typical values

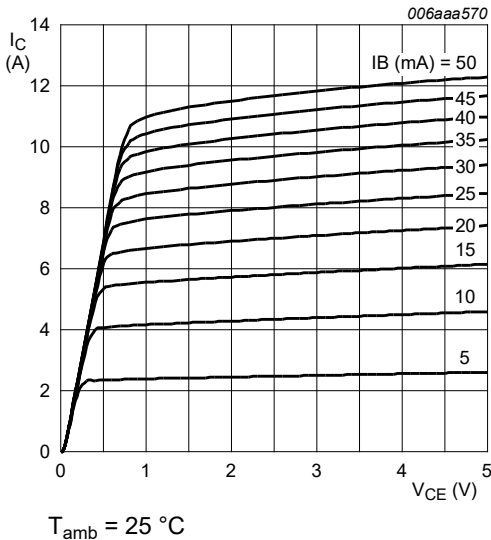


Fig. 6. Collector current as a function of collector-emitter voltage; typical values

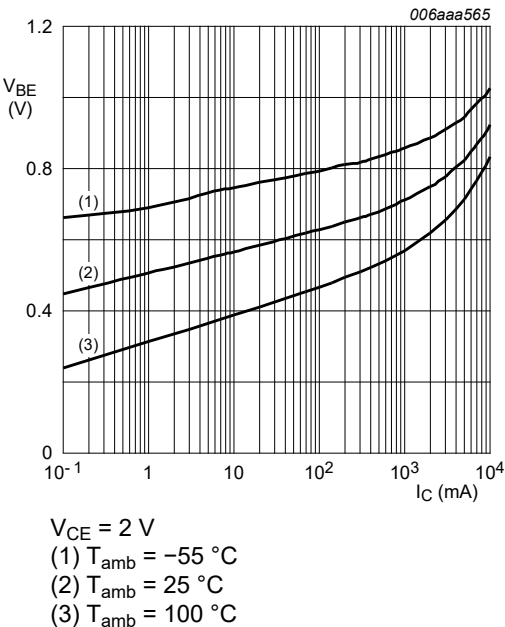


Fig. 7. Base-emitter voltage as a function of collector current; typical values

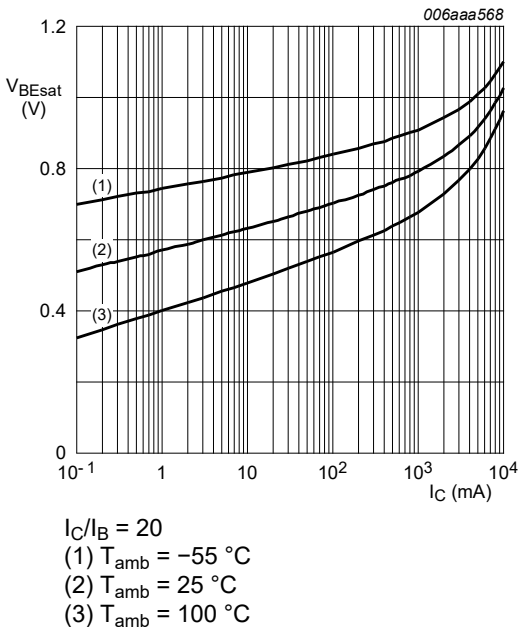


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

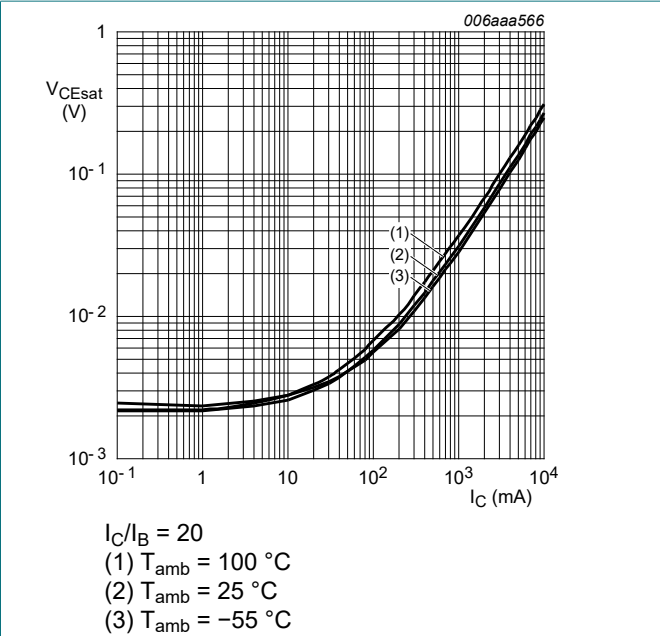


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

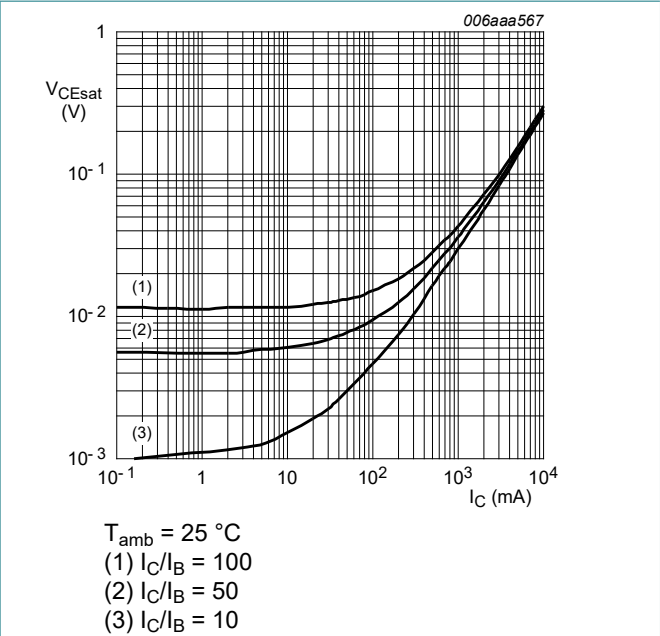


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

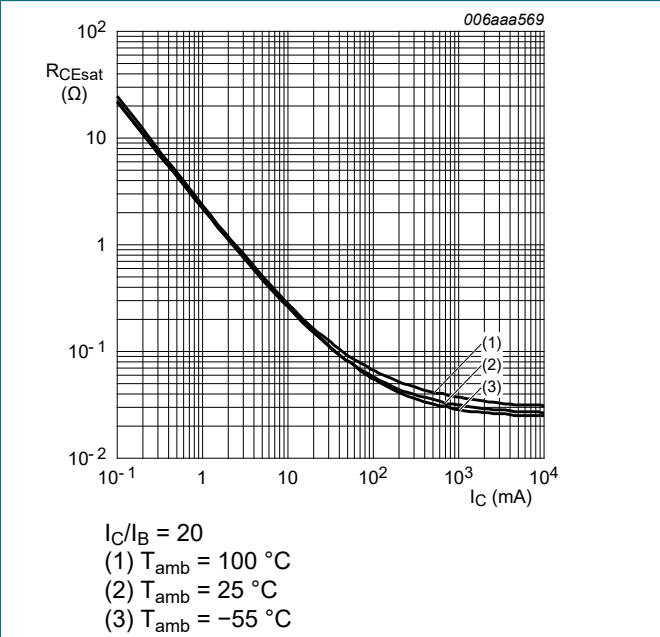


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

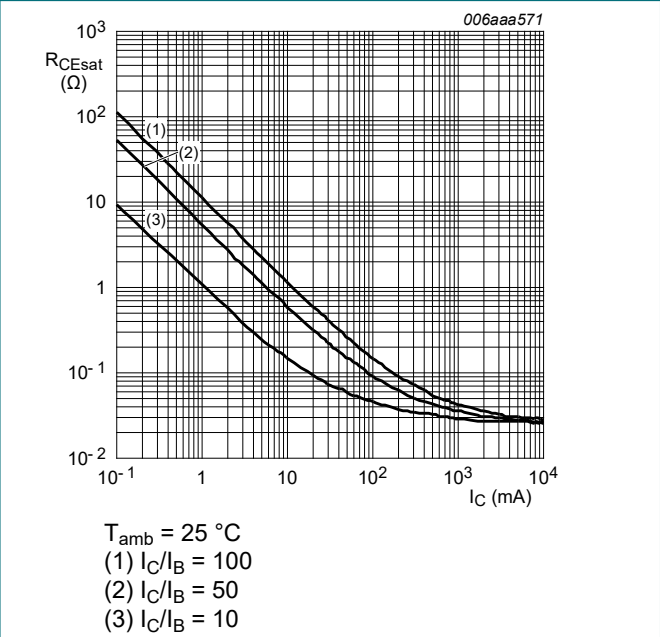


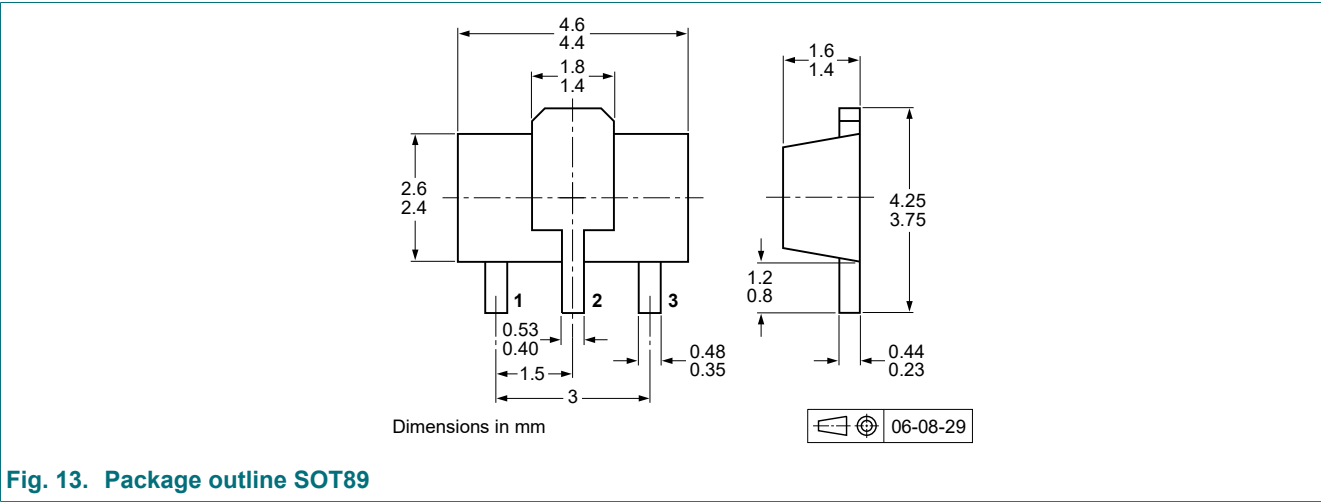
Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

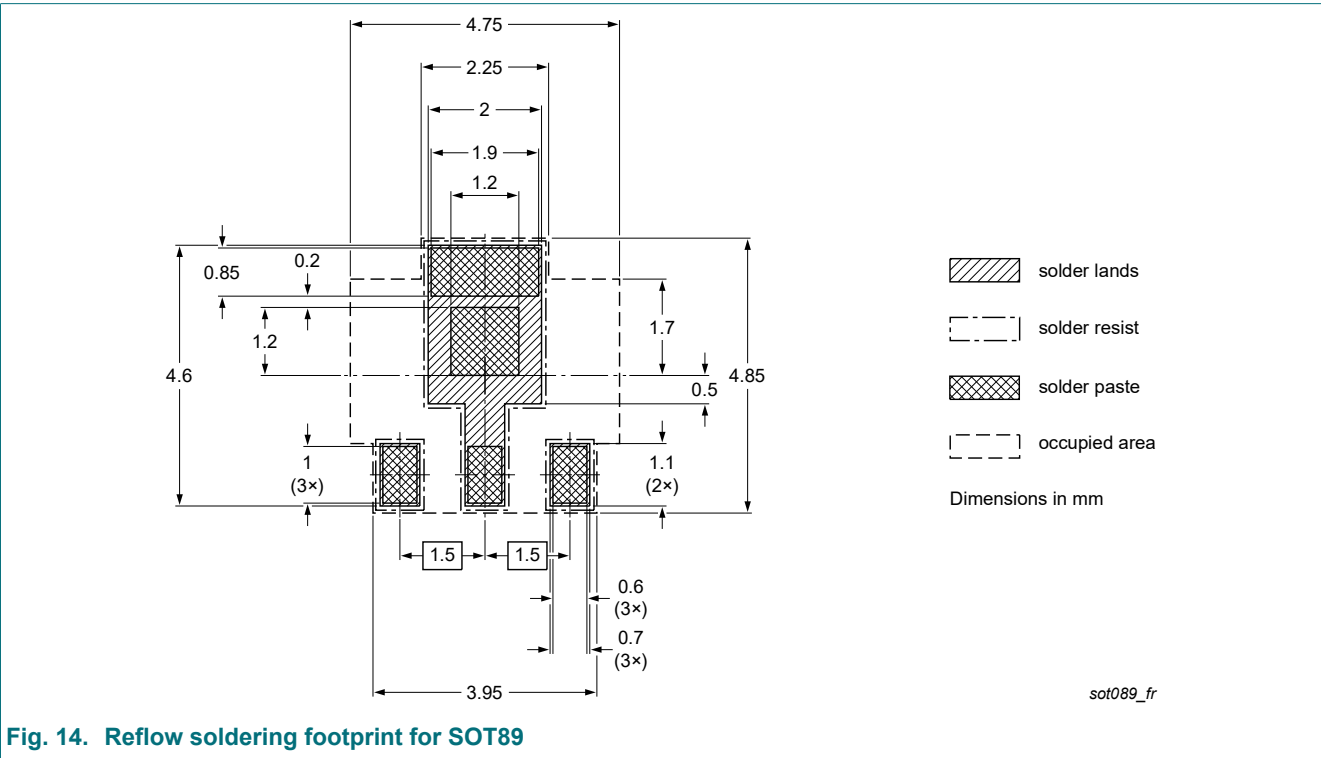
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS301NX v.3	20240206	Product data sheet	-	PBSS301NX v.2
Modifications:	- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. - Legal texts have been adapted to the new company name where appropriate. - Section "Packing information" removed.			
PBSS301NX v.2	20091117	Product data sheet	-	PBSS301NX v.1
PBSS301NX v.1	20060822	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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