## 1. General description

PNP general-purpose transistor in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

#### **Table 1. Product overview**

Type number	Package I		NPN complement:
	Nexperia	JEDEC	
BC856AQC-Q	SOT8009	MO-340CA	BC846AQC-Q
BC856BQC-Q			BC846BQC-Q

## 2. Features and benefits

- High power dissipation capability
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- · General-purpose switching and amplification
- · Space restricted applications

### 4. Quick reference data

#### Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-65	V
I <sub>C</sub>	collector current		-	-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	-200	mA
h <sub>FE</sub>	DC current gain					
	BC856AQC-Q	$V_{CE} = -5 \text{ V; } I_{C} = -2 \text{ mA}$	125	-	250	
	BC856BQC-Q		220	-	475	



# 5. Pinning information

### **Table 3. Pinning**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	7	C
2	E	emitter	3	В—
3	С	collector		
			1 2	E sym132
			<u> </u>	
			Transparent top view	

# 6. Ordering information

### **Table 4. Ordering information**

Type number	Package					
	Name	Description	Version			
BC856AQC-Q		1 3	SOT8009			
BC856BQC-Q		wettable flanks (SWF); 3 terminals; 0.8 mm pitch; body: 1.4 mm x 1.2 mm x 0.48 mm				

# 7. Marking

### Table 5. Marking

Type number	Marking code
BC856AQC-Q	9Т
BC856BQC-Q	9U

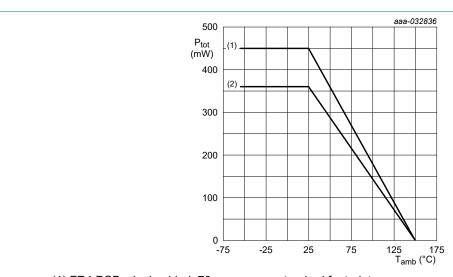
## 8. Limiting values

#### **Table 6. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-65	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-6	V
I <sub>C</sub>	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	360	mW
			[2]	-	450	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided; 35 µm copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided; 70 µm copper; tin-plated and standard footprint.



- (1) FR4 PCB; single-sided; 70  $\mu m$  copper; standard footprint
- (2) FR4 PCB; single-sided; 35 µm copper; standard footprint

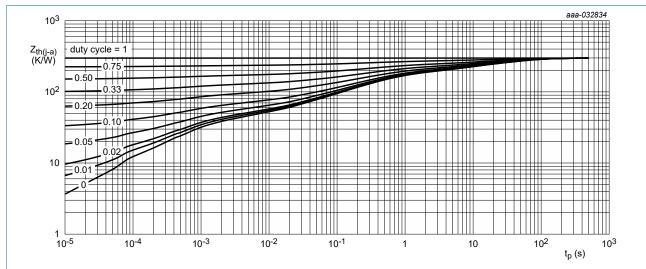
Fig. 1. Power derating curves DFN1412D-3 (SOT8009)

### 9. Thermal characteristics

#### **Table 7. Thermal characteristics**

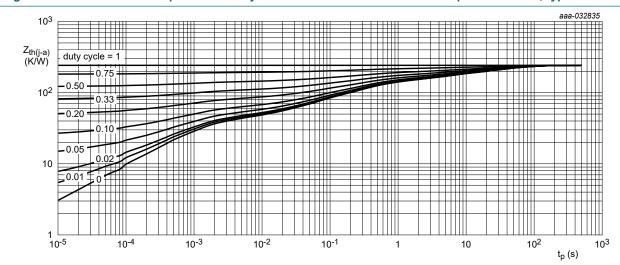
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	348	K/W
			[2]	-	-	278	K/W

- [1] Device mounted on an FR4 PCB; single-sided; 35 µm copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided; 70 µm copper; tin-plated and standard footprint.



FR4 PCB; single-sided; 35 µm copper, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided; 70  $\mu m$  copper, standard footprint

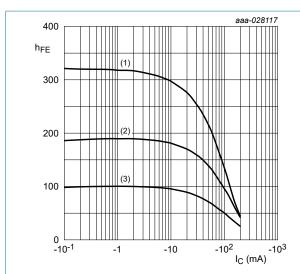
Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

### **Table 8. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = -100 μA; I <sub>E</sub> = 0 A		-80	-	-	V
V <sub>(BR)CES</sub>	collector-emitter peak voltage	$I_C = -2 \text{ mA}; I_E = 0 \text{ A}$		-65	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = -100 μA; I <sub>C</sub> = 0 A		-6	-	-	V
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A}$		-	-	-15	nA
	current	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$		-	-	-100	nA
h <sub>FE</sub>	DC current gain					'	
	BC856AQC-Q	$V_{CE} = -5 \text{ V}; I_{C} = -2 \text{ mA}$		125	-	250	
	BC856BQC-Q			220	-	475	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = -10 mA; I <sub>B</sub> = -0.5 mA		-	-	-300	mV
saturation voltage		I <sub>C</sub> = -100 mA; I <sub>B</sub> = -5 mA	[1]	-	-	-650	mV
V <sub>BE</sub>	base-emitter voltage	$V_{CE} = -5 \text{ V} ; I_{C} = -2 \text{ mA}$	[2]	-600	-	-750	mV
		V <sub>CE</sub> = -5 V ; I <sub>C</sub> = -10 mA	[2]	-	-	-820	mV
V <sub>BEsat</sub>	base-emitter saturation	I <sub>C</sub> = -10 mA ; I <sub>B</sub> = -0.5 mA		-	-700	-	mV
	voltage	I <sub>C</sub> = -100 mA ; I <sub>B</sub> = -5 mA	[1]	-	-850	-	mV
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	2	-	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = -0.5 \text{ V}; I_C = I_c = 0 \text{ A}; f = 1 \text{ MHz}$		-	10	-	pF
NF	noise figure	$V_{CE}$ = -5 V; $I_{C}$ = -200 $\mu$ A; $R_{S}$ = 2 $k\Omega$ ; $f$ = 1 $kHz$ ; B = 200 $Hz$		-	-	10	dB

 $<sup>\</sup>begin{array}{ll} [1] & \text{pulsed; } t_p \leq 300 \; \mu\text{s; } \delta \leq 0.02 \\ [2] & V_{BE} \; \text{decreases by about 2 mV/K with increasing temperature.} \end{array}$ 



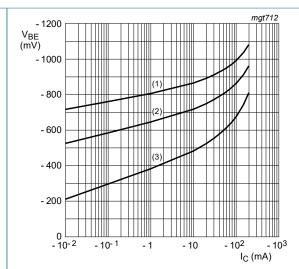
$$V_{CE} = -5 V$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 4. BC856AQC-Q: DC current gain as a function of collector current; typical values

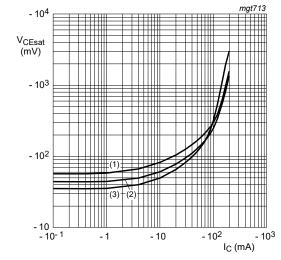


$$V_{CE} = -5 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. BC856AQC-Q: Base-emitter voltage as a function of collector current; typical values



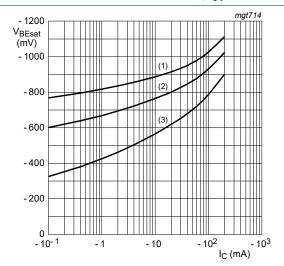
$$I_{\rm C}/I_{\rm B}$$
= 20

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$





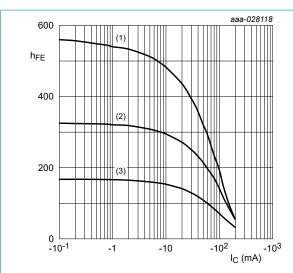
$$I_{\rm C}/I_{\rm B}$$
= 20

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 7. BC856AQC-Q: Base-emitter saturation voltage as a function of collector current; typical values



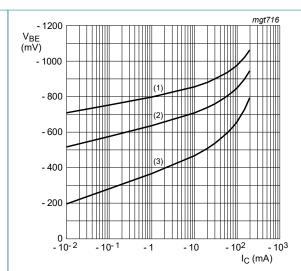
$$V_{CE}$$
 = -5  $V$ 

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. BC856BQC-Q: DC current gain as a function of collector current; typical values



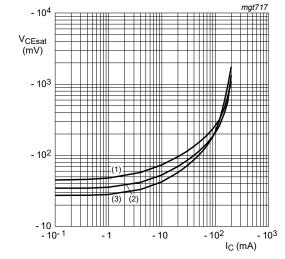
$$V_{CE}$$
 = -5  $V$ 

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 9. BC856BQC-Q: Base-emitter voltage as a function of collector current; typical values

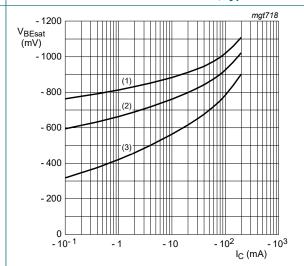


 $I_{C}/I_{B}=20$ 

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 10. BC856BQC-Q: Collector-emitter saturation voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B}=20$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb}$$
 = 150 °C

Fig. 11. BC856BQC-Q: Base-emitter saturation voltage as a function of collector current; typical values

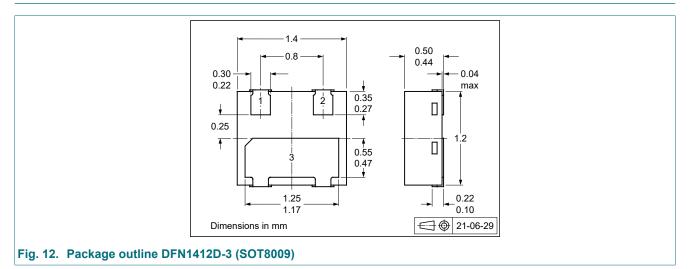
### 11. Test information

### **Quality information**

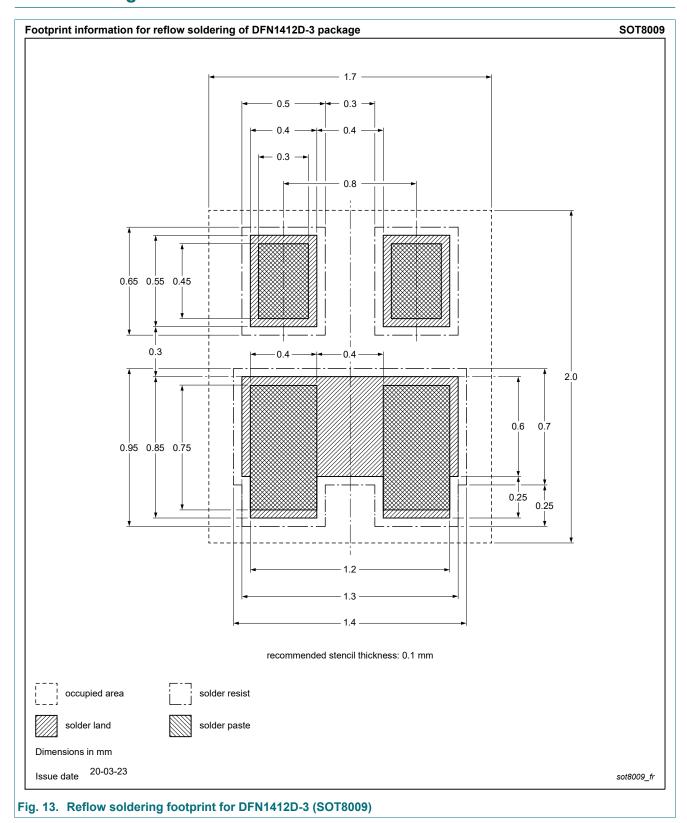
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

BC856XQC-Q\_SER

# 12. Package outline



# 13. Soldering



# 14. Revision history

### Table 9. Revision history

Data sheet ID	Release date		Change notice	Supersedes
BC856XQC-Q_SER v.1	20210923	Product data sheet	-	-

# 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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## 65 V, 100 mA PNP general-purpose transistor

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BC856XQC-Q\_SER

Product data sheet

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