

## **BC847BV**

NPN general purpose double transistor

27 December 2022

**Product data sheet** 

### 1. General description

NPN double transistor in an ultra small SOT666 flat lead plastic package.

PNP complement: BC857BV

### 2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm x 0.55 mm ultra thin package
- Excellent coplanarity due to straight leads
- Low collector capacitance
- Improved thermal behaviour due to flat leads
- Reduces number of components as replacement of two SC-75/SC-89 packaged BISS transistors
- Reduces required board space
- Reduces pick and place costs

#### 3. Applications

General purpose switching and amplification

### 4. Quick reference data

Table 1. Quid	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or		,				
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C		200	-	450	

### 5. Pinning information

#### Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		 E1 B1 C2
6	C1	collector TR1	SOT666	sym020

# nexperia

### 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
<u>BC847BV</u>	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>			

#### 7. Marking

Table 4. Marking codes						
Type number	Marking code					
BC847BV	lF					

#### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or		l			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Per device						
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

#### 9. Thermal characteristics

Table 6. Ther	mal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transisto	or		'				
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1] [2]	-	-	625	K/W
Per device							
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1] [2]	-	-	416	K/W

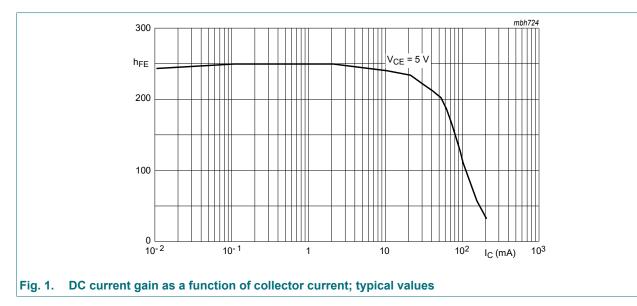
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

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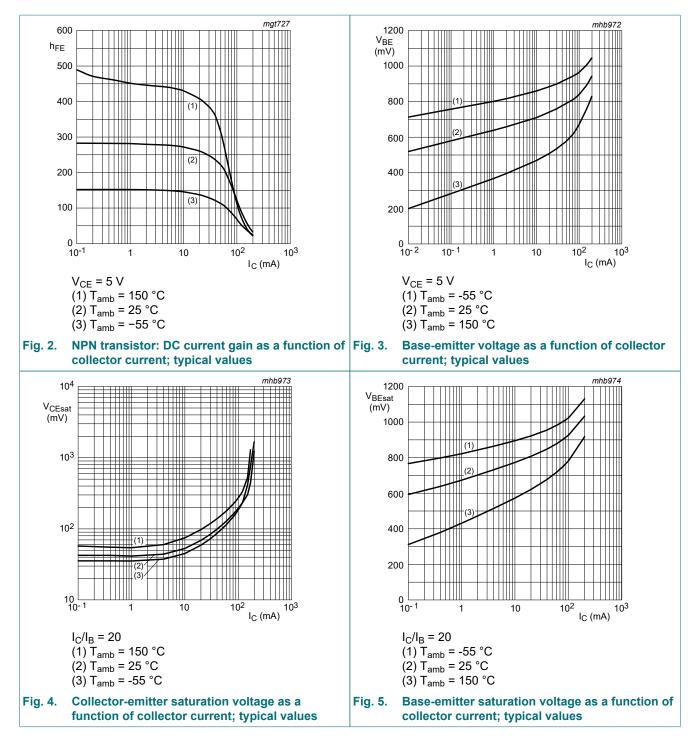
### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per transist	tor					
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	15	nA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	200	-	450	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C	-	-	100	mV
		$I_{C}$ = 100 mA; $I_{B}$ = 5 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	300	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C	-	755	-	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	580	655	700	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	1.5	pF
C <sub>e</sub>	emitter capacitance	$V_{EB}$ = 500 mV; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	11	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	-	-	MHz

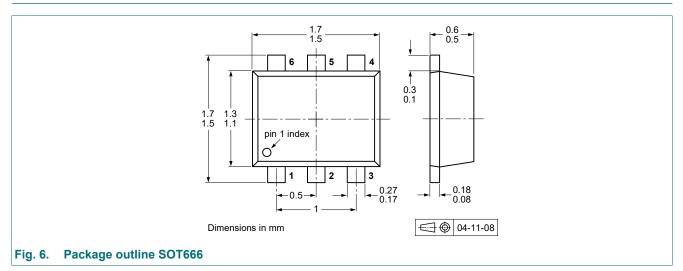


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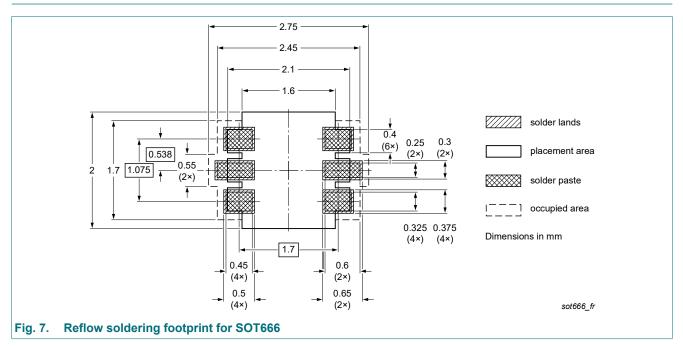
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### 11. Package outline



#### 12. Soldering



### 13. Revision history

Table 8. Revision h	nistory								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes					
BC847BV v.2	20221227	Product data sheet	-	BC847BV v.1					
Modifications:	Nexperia. • Legal texts ha	The format of this data sheet has been redesigned to comply with the identity guidelines of							
BC847BV v.1	20010910	Product data sheet	-	-					

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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